# Figures of Merit for CMOS Low-Noise Amplifiers and Estimates for Their Theoretical Limits

Leonid Belostotski, Senior Member, IEEE, and Eric A.M. Klumperink, Fellow, IEEE

Abstract—This paper examines the theoretical interdependence of lownoise-amplifier (LNA) performance parameters to identify four figures of merit (FoMs) that enable the comparison of LNAs. FoM limits are also estimated to identify performance limitations and to quantify room for theoretical improvement. Finally, the results of a regression analysis on nearly 500 CMOS LNAs, published over the past 20 years, are compared to the identified trends, with the findings showing reasonable agreement despite the adoption of some simplifying assumptions.

Index Terms-CMOS, figure of merit, FoM, LNA, linearity measure

#### I. INTRODUCTION

**N** EW CIRCUITS are constantly being developed for commercial products and research projects. These new circuits feature diverse sets of specifications and often provide novel solutions to the limitations of prior circuits.

Circuit surveys [1]–[6] and performance benchmarking using figures of merit (FoMs) [7], [8] can help to find viable circuit solutions and assess achievable performance. While numerous FoMs have been proposed, their relevance to key design trade-offs is not always obvious (over 30 different FoMs were identified during the compilation of the survey [6]!).

If an FoM captures a key design trade-off, one would expect this to be corroborated by practical data points. To this end, a regression analysis was applied on the data in [6] to identify the underlying trade-offs for low-noise amplifiers (LNAs) [9]. Such regression analysis treats all data points as equally relevant, i.e. it focuses on "typical" rather than cutting-edge performance. Although the regression-based FoMs in [9] identify trade-offs buried in the data, the resultant FoMs are not static, as they are adjusted every time new data is added to the survey. In addition, this type of analysis does not identify theoretical limits.

The contribution and objective of this article is to identify theoretical trade-offs that can be captured in a model to serve as a basis for LNA-FoMs and for estimating their theoretical limits. Such FoMs can serve as benchmarks to guide designers with respect to what is theoretically possible. Although these theoretical limits may model best-case scenarios that are virtually unreachable in practice, they are nevertheless useful for at least two reasons: 1) they quantify how much theoretical room is available for performance improvement; and 2) they help to identify factors that are limiting the potential of practical designs, which might trigger ideas for improving the state of the art. We assume that such FoMs should be based on commonly published data.

While the quality of power and noise matching are not typically included in FoMs, such matching is often important and constrains LNA design. Section II begins by reviewing the minimum noise factor ( $F_{min}$ ) and the optimum admittance ( $Y_{opt}$ ) required to reach  $F_{min}$ , as well as the condition required for simultaneous noise and power matching. We propose employing a classical multi-metrics noise measure and a linearity measure to capture system-performance trade-offs that constrain LNA designs. In Section III, equations for estimating theoretically achievable performance are derived, and the trade-offs with operating frequency, technology node, noise, and power consumption are identified. To keep analysis tractable, seven assumptions are made, justified, and stated explicitly to clearly identify the theoretical boundary conditions for the claims made and the resultant best achievable performance identified. Section IV presents the proposed FoMs and their limits, while Section V discusses the similarity between proposed FoMs and the FoMs obtained via a regression analysis on a dataset of more than 500 LNAs.

# II. LNA PERFORMANCE METRICS: MINIMUM NOISE FACTOR, NOISE AND LINEARITYMEASURES

Common LNA performance metrics include operating frequency  $(\omega_0 = 2\pi f_0)$ , spot noise figure/factor (F) in a 1-Hz bandwidth, gain (G), bandwidth  $(\Delta f)$ , 1-dB compression point  $(P_{1dB})$ , 3rd-order intercept (IP3), and power consumption  $(P_d)$ . On their own, these metrics are valid for comparing LNAs; however, they do not reflect design trade-offs and system-performance trade-offs. As the result this section reviews the fundamental  $F_{min}$  and noise measure and proposes a linearity measure to serve as key FoM components.

## A. Minimum Noise Factor $(F_{min})$

 $F_{min}$  represents the fundamental limit on the best possible noise factor from a transistor. For a MOSFET [10], [11]

$$F_{min} = 1 + 2\frac{\omega_0}{\omega_T} \sqrt{\frac{\delta\gamma}{5} \left(1 - |c|^2\right)} \tag{1}$$

where  $\delta$  and  $\gamma$  are the excess noise coefficients for the induced gate and drain noise currents, c is the correlation coefficient between these noise currents, and  $\omega_T$  is the frequency of the unity current gain.  $F_{min}$  is achieved, when the signal-source admittance equals the optimum admittance for minimum noise,  $Y_{opt} = G_{opt} + jB_{opt}$ , where [10], [12]–[15]

$$\begin{cases}
G_{opt} = \omega_0 C_{gs} \alpha \sqrt{\left(1 - |c|^2\right) \delta/5\gamma} \\
B_{opt} = -\omega_0 C_{gs} \left(1 - \alpha |c| \sqrt{\delta/5\gamma}\right).
\end{cases}$$
(2)

In (2),  $C_{gs}$  is the gate-source capacitance and [16]

$$\alpha = \frac{1+0.5\rho}{\left(1+\rho\right)^2} \text{ with } \rho \equiv \frac{V_{od}}{LE_{sat}},\tag{3}$$

where  $V_{od}$  is the overdrive voltage, L is the channel length, and  $E_{sat}$  is the velocity-saturation field strength [10], [17].

Ideally, an LNA design achieves simultaneous noise and power matching, which requires  $Y_{in} = Y_{opt}^*$ . Many prior publications have noted that such simultaneous matching is not feasible as  $B_{opt} \neq \Im \{Y_{in}\}$ , where  $\Im \{Y_{in}^*\} = \omega C_{gs}$ . However, prior works have simplified the mean-squared drain and gate noise current expressions,  $\overline{i_{dn}^2} = 4k_bT_0B\gamma g_{do}$  and  $\overline{i_{gn}^2} = 4k_bT_0B\delta\omega_0^2 C_{gs}^2/5g_{d0}$ , by replacing  $g_{d0}$  (i.e., the output conductance when  $V_{ds} = 0$ ) with transconductance  $g_m$  rather than  $g_m/\alpha$ , where  $k_b$  is Boltzmann's

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L. Belostotski is with the Department of Electrical and Software Engineering, University of Calgary (e-mail: lbelosto@ucalgary.ca). E.A.M. Klumpernik is with the Faculty of Electrical Engineering, Mathematics and Computer Science, University of Twente (e-mail: e.a.m.klumperink@utwente.nl).

constant,  $T_0$  is the reference temperature, and B is the noise bandwidth. Recent work [18] exploits parameter  $\alpha$  for power and noise matching. The optimum  $C_{gs}$  was derived, which served to minimize  $\Delta F = F - F_{min}$  and reduce the sensitivity,  $S_f \equiv \frac{\partial \Delta F}{\partial \omega} \frac{\omega}{\Delta F}$ , of  $\Delta F$  to  $\omega$ . It was found that

$$\alpha_{opt} = |c| \sqrt{\frac{5\gamma}{\delta}} \tag{4}$$

is optimum as it makes  $\Im\{1/Y_{opt}\} = \Im\{1/Y_{in}^*\} = 1/\omega_0 C_{gs}$ as desirable for simultaneous noise and power matching. E.g., inductive source degeneration realizes  $\Re\{1/Y_{opt}\} = \Re\{1/Y_{in}^*\}$ while preserving  $\Im\{1/Y_{opt}\} = \Im\{1/Y_{in}^*\}$ . This analysis has two consequences for this article:

- it gives equations for  $\alpha_{opt}$  and  $\rho_{opt}$  (via (3)) that provide simultaneous noise and power matching,
- for the signal-source impedance,  $Z_0 \in \mathbb{R}$ , both  $\Delta F$  and its sensitivity to  $\omega$  are minimized when

$$\omega_0 C_{gs}^{opt} = \frac{1}{Z_0} \frac{|c|}{\sqrt{1 - |c|^2}}.$$
(5)

## B. Noise Measure

Since LNAs are intended to reduce system noise factor, LNA F should always be in an LNA FoM. Furthermore, gain (G) is also intuitively important for LNAs. How to account for this trade-off is a research question that dates back to Haus and Adler's [19] work on defining "noise measure." This stepping-stone to defining an FoM was derived by starting with Friis' formula [20]

$$F_{12} = F_1 + \frac{F_2 - 1}{G_1}$$
 and  $F_{21} = F_2 + \frac{F_1 - 1}{G_2}$ , (6)

where  $F_{12(21)}$  and  $F_{1(2)}$  are the noise factors of the cascade of LNA #1-#2 (LNA #2-#1) and LNAs #1(#2), respectively, and  $G_{1(2)}$  [lin] are their available gains. For  $F_{12} < F_{21}$ , (6) results in

$$\frac{F_1 - 1}{1 - G_1^{-1}} < \frac{F_2 - 1}{1 - G_2^{-1}},\tag{7}$$

which shows that the lowest noise factor for a cascade is achieved by positioning the LNA with the lowest

$$F_M \equiv \frac{F-1}{1-G^{-1}},\tag{8}$$

rather than the lowest F, at the front.  $F_M$  is the noise measure. Some important conclusions stemming from [19] include:

- 1) The minimum of  $F_M$  is device-size invariant.
- 2)  $F_M$  of an arbitrary number of two-port amplifier stages with lossless interconnections is not less than the best-stage  $F_M$ .
- 3) F of a network cannot be less than F of the best stage.
- 4) Lossy or active-circuit embedding adds noise and can only degrade the minimum  $F_M$ .

While the noise measure connects the gain and noise factor, it does not link power consumption and other performance metrics to LNA noise. As [19] demonstrates, the best possible performance in terms of noise can only be achieved with a single-transistor, which is hence the focus of Section III.

## C. Linearity Measure

Analogously to noise measure, in this paper *linearity measure* is derived from the input-referred linearity of a two-LNA cascade [10]

$$\frac{1}{IP_{12}} \approx \frac{1}{IP_1} + \frac{G_1}{IP_2} \text{ and } \frac{1}{IP_{21}} \approx \frac{1}{IP_2} + \frac{G_2}{IP_1}$$
 (9)

where  $IP_{12(21)}$  [mW] is the input-referred P<sub>1dB</sub> (IP<sub>1dB</sub>) or IP3 (IIP3) of the cascade of LNA #1(#2) and LNA #2(#1), and  $IP_{1(2)}$  [mW] and  $G_{1(2)}$  [lin] are the IP<sub>1dB</sub> or IIP3 and gains of LNAs #1(#2). If  $P_{1dB,12} > P_{1dB,21}$ , then (9) gives

$$IP_1(G_1 - 1) < IP_2(G_2 - 1).$$
 (10)

Based on (10), the IP<sub>1dB</sub> linearity measure can be defined as

$$P_L \equiv I P_{1dB} \left( G - 1 \right). \tag{11}$$

As shown in (10), the IP<sub>1dB</sub> of a cascade can be maximized by placing the amplifier with the larger  $P_L$  last.  $P_L$  captures the trade-off between gain and linearity, which is useful in defining FoMs.

#### **III. KEY EQUATIONS**

This section builds on Section II and investigates the theoretical properties of single-transistor LNAs in order to identify FoMs and their theoretical limits. To provide insight and keep the math tractable, simple modeling is used based on the following assumptions:

<u>Assumption #1:</u> Transistors biased in strong inversion and saturation are assumed, as their  $F_{min}$ s are lower than in subthreshold.

<u>Assumption #2:</u> The small-signal behavior of a MOSFET in saturation is modeled with only  $g_m$  and  $C_{gs}$ .

<u>Assumption #3:</u> Simultaneous noise and power matching as described in Section II-A is targeted.<sup>1</sup>

Assumption #4: A simple second-order model of the MOSFET current is employed [16]

$$I_d = C_{ox} W v_{sat} \frac{V_{od}^2}{V_{od} + L E_{sat}},\tag{12}$$

where  $V_{od} = V_{gs} - V_T$ ,  $V_{gs}$  is the gate-source voltage,  $V_T$  is the threshold voltage, L and W are the channel length and width,  $C_{ox}$  is the gate oxide capacitance per area, and  $v_{sat}$  is the saturation velocity.

<u>Assumption #5:</u>  $V_{dd} = V_{ds} = V_{od}$  is assumed for minimum power consumption,  $P_d$ , while avoiding triode. Using (3), (12) gives

$$P_d = V_{dd}I_d = V_{od}I_d = \frac{C_{ox}W\mu_{eff}}{2L(1+\rho)}V_{od}^3,$$
(13)

where  $\mu_{eff} \equiv 2v_{sat}/E_{sat}$  is the field-limited charge mobility.

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<u>Assumption #6:</u> Long-channel parameters  $\gamma = 2/3$ ,  $\delta = 4/3$ , and c = j0.395 are assumed. Assumptions #3 and #6 and (4) give  $\alpha_{opt} = 0.62$  and  $\rho_{opt} = 0.4$  via (3).

Assumption #7: For determining linearity-based FoMs, it is assumed that linearity can be assessed via (12).

With these assumptions, the key expressions can be derived.

Overdrive voltage ( $V_{od}$ ): From (12) in Assumption #4

$$V_{od} = \sqrt{\frac{2L}{(1+\rho)C_{ox}\mu_{eff}}} \times \frac{I_d}{W}$$
(14)

Together with (13) from Assumption #5, (14) expresses the relationship between  $I_d/W$  and power consumption as follows

$$\frac{I_d}{W} = \sqrt[3]{\frac{P_d^2 C_{ox} \mu_{eff} \left(1+\rho\right)^5}{2LW^2}}.$$
(15)

*Transconductance*  $(g_m)$ : The differentiation of (12) with respect to  $V_{qs}$  results in

$$g_m = C_{ox} W \mu_{eff} \frac{V_{od}}{L} \frac{1+0.5\rho}{(1+\rho)^2} = C_{ox} W \mu_{eff} \frac{V_{od}}{L} \alpha.$$
(16)

<sup>1</sup>While methods, such as inductive source degeneration, may be required to complete matching, they do not affect equations in this paper.

Using (14) and (15), (16) becomes

$$g_m = \alpha W \sqrt{\frac{2C_{ox}\mu_{eff}}{L}} \sqrt[6]{\frac{P_d^2 C_{ox}\mu_{eff} \left(1+\rho\right)^2}{2LW^2}}$$
(17)

and relates  $g_m$  to power consumption and transistor width.

Frequency of unity current gain ( $\omega_T$ ): Combining (17),  $C_{gs} = 2/3C_{ox}WL$ , and  $\omega_T = g_m/C_{gs}$  (Assumption #2) gives

$$\omega_T = \frac{3}{2} \alpha \sqrt{\frac{2\mu_{eff}}{C_{ox}L^3}} \sqrt[6]{\frac{P_d^2 C_{ox} \mu_{eff} \left(1+\rho\right)^2}{2LW^2}}.$$
 (18)

Transistor width (W): For a single MOSFET, the relationship between the width, W, and  $C_{gs} = C_{gs}^{opt}$  as per Assumption #3 is obtained from (5) as

$$W = \frac{3}{2\omega_0 C_{ox} L Z_0} \times \frac{|c|}{\sqrt{1 - |c|^2}}.$$
 (19)

Minimum noise factor  $(F_{min})$ : Substituting (18) and (19) into (1) and collecting all constants in to a constant  $\mathcal{N}$  yields

$$\frac{(F_{min}-1) P_d^{1/3}}{f_0^{2/3} L^{4/3}} = \frac{4}{\alpha} \sqrt{\frac{\delta\gamma}{5}} \sqrt[3]{\frac{\pi^2 |c| (1-|c|^2)}{9Z_0 \mu_{eff}^2 (1+\rho)}} = \mathcal{N}.$$
 (20)

*Linearity:* The linearity of LNAs and other transconductancebased circuits is commonly characterized in terms of their IP3 and P<sub>1dB</sub> [21], [22]. While for a simple memoryless 3rd-order nonlinearity these values are often simply related as IP3 = P<sub>1dB</sub>+10 dB [21], [23], it is more complicated in general [22]. It is well-known that measured IP3 can be input-power dependent because for some power levels two nonlinearity effects may cancel each other out (e.g., due to derivative superposition) [24]. The tone spacing in two-tone IP3 measurements can also be responsible for modifying IP3 measurement results, but such tone spacings are not universally set equal. The addition of an output filter together with nonlinearity cancellation can reduce intermodulation products, thereby allowing IP3 to become very high, and almost unlimited. In contrast, the measurement procedure for P<sub>1dB</sub> is universal. Thus,  $P_L$  (P<sub>1dB</sub>) is selected for LNA FoMs in this work.

Feedback or gain expansion stages can boost  $P_{1dB}$ , potentially outperforming a stand-alone MOSFET. Therefore, expressions connecting  $P_{1dB}$  to other LNA performance metrics can be highly specific to a sub-class of circuits rather than generally valid.

The data analysis in [6] shows that  $P_{1dB}$  and  $V_{dd}$  exhibit a 41% correlation and  $P_{1dB}$  and *G* exhibit a 12% correlation whereas  $P_{1dB}$  and  $P_d$  exhibit a 62% correlation and  $P_d$  and  $V_{dd}$  exhibit a 32% correlation. Since  $P_{1dB}$  depends stronger on  $P_d$  than  $V_{dd}$ , the onset of  $P_{1dB}$  is often due to MOSFET nonlinear behavior rather than clipping, thus giving rise to Assumption #7.

Using (16) and Assumption #7, the condition for gain compression is investigated through the compression of  $g_m$ . For a given compression level ( $G_c$ ) and the input signal,  $v_{sig} = A \cos(\omega_0 t)$ , gain compression can be related to the average of  $g_m$  via  $\langle g_m(v_{in}) \rangle =$  $G_c g_m|_{v_{in}=0}$ , which, with the help of (16), becomes

$$G_{c} g_{m}|_{v_{in}=0} = \frac{C_{ox}W\mu_{eff}}{2A}$$
(21)  
  $\times \int_{-A}^{+A} \frac{(V_{od} + v_{in})}{L} \frac{\frac{1}{2}(V_{od} + v_{in})/LE_{sat} + 1}{((V_{od} + v_{in})/LE_{sat} + 1)^{2}} dv_{in}.$ 

Substituting (17) into (21) and completing the integration produces

$$A^{2} = V_{od}^{2} \frac{(\rho+1)^{2} (\rho+2) (1-G_{c})}{\rho (\rho (\rho+2) (1-G_{c}) + 1)} = V_{od}^{2} \mathcal{R}$$
(22)

where  $A \leq V_{od}$  is desirable to avoid subthreshold onset (hard-clipping). Then, from (22),  $\mathcal{R} \leq 1$  and 1-dB compression (i.e.,

 $G_c = 0.9$ ) constrain  $0.5 \le \rho \le 2$ . Within this range, the maximum  $\mathcal{R} = 1$  when  $\rho = 0.5$  or 2, thereby allowing for the maximum  $v_{sig}$  be applied to the input of the LNA. A in (22), which corresponds to power  $P_{comp} = A^2/2Z_0$ , is the signal amplitude, resulting in the  $G_c$ -level of compression. From (22) it is possible to obtain

$$P_{comp} = V_{od}^2 \frac{\mathcal{R}}{2Z_0}.$$
(23)

Note that, if  $G_c = 0.9$ , then  $P_{comp} = IP_{1dB}$ . Furthermore, by using (23) and (19) in (12) and combining the constants on the right-hand side (labeled as  $\mathcal{L}$ ), a relationship between linearity, power consumption, transistor length, and frequency can be expressed as

$$\frac{P_{comp}}{P_d L f_0} = \frac{4\pi}{v_{sat}} \frac{\sqrt{\left(1 - |c|^2\right)}}{|c|} \frac{\left(\rho + 1\right)\mathcal{R}}{3\rho} = \mathcal{L}\frac{\left(\rho + 1\right)\mathcal{R}}{3\rho}.$$
 (24)

Equation (24) reaches  $\mathcal{L}$  when  $\rho = 0.5$  and  $0.5\mathcal{L}$  when  $\rho = 2$ . Therefore,  $\rho = 0.5$  will be used in Section IV-B to maximize (24).

The derivations of (19) make assumptions that are consistent with the derivations of (20) and Assumption #5 (i.e. minimizing  $V_{ds} = V_{dd}$ to  $V_{dd} = V_{od}$ ). While such low  $V_{ds}$  may lead to clipping, since  $V_{dd}$  clipping is not a prominent cause for gain compression (see the regression-analysis discussion above), no unduly clipping due to  $V_{dd}$ is considered when deriving (24). The impact of  $V_{dd} = V_{od}$  is in increasing  $\mathcal{L}$ , which will form a limit on FoM in Section IV-B.

### IV. FIGURES OF MERIT

Based on the expressions in Section III, four FoMs are suggested.

#### A. Noise and Sensitivity FoMs

Having captured key low-noise design trade-offs in (20), the noise FoM is proposed  $as^2$ 

$$FoM_N \equiv \frac{F_M P_d^{1/3}}{L^{4/3} f_0^{2/3}}$$
(25)

where L is in  $\mu$ m,  $f_0$  is in GHz, and  $P_d$  is in mW.  $F_M$  from (8) replaces  $(F_{min} - 1)$  in (20) to form (25) as motivated in Section II-B. Note that  $F_M$  depends on gain via (8). Since most papers do not specify available gain,  $F_M$  in (25) would be calculated based on the gain specified. For LNAs that achieve their  $F_{min}$  and have large gain,  $F_M$  reduces to  $F_{min} - 1$ , and, as defined in (20),  $FoM_N$  comes close to achieving its limit, which is found via

$$FoM_{N,limit} = \frac{\left(10^{-6} \text{ m}\right)^{4/3} \left(10^{9} \text{ Hz}\right)^{2/3}}{\left(10^{-3} \text{ W}\right)^{1/3}} \mathcal{N}\left(\alpha_{opt}, \rho_{opt}\right) \approx 0.4,$$
(26)

where the terms preceding  $\mathcal{N}$  account for the units of parameters used in (25),  $\mu_{eff} = 2v_{sat}/E_{sat}$ ,  $v_{sat} = 8.43 \times 10^4$  m/s,  $E_{sat} = 4.7 \times 10^6$  V/m. Since  $\rho$  impacts  $\mathcal{N}$  in (20) and since  $|S_{11}| < -10$  dB, rather than the perfect power match, is generally acceptable, LNAs biased with  $\rho < \rho_{opt}$  may realize  $FoM_N$  that are closer to  $FoM_{N,limit}$  than comparable LNAs with  $\rho = \rho_{opt}$ . This potential  $FoM_N$  lowering, if any, is relatively small and comparison to  $FoM_{N,limit}$  remains meaningful.

Sensitivity identifies the minimum detectable signal for a given noise factor. Thus, the sensitivity FoM can be defined based on  $FoM_N$  as

$$FoM_{S} \equiv \frac{L^{4/3} f_{0}^{2/3}}{F_{M}} \left(\frac{P_{d}}{\Delta f}\right)^{-1/3}$$
(27)

<sup>2</sup>"Noise FoM" is the short-form name of a noise-gain-power-frequencytechnology FoM that models design trade-offs between these parameters and, akin to LNA noise factor, is expected to be reduced during an LNA design.



Fig. 1. Comparison of FoMs calculated for CMOS LNAs based on the data provided in [6], along with their limit lines.

where bandwidth  $\Delta f$  is introduced to normalize  $P_d$  to that of a narrowband LNA and is defined based on either  $S_{11}$  or gain as appropriate for a given application. After introducing the relative bandwidth  $B_r \equiv \Delta f/f_0$ , (27) is rewritten as

$$FoM_S = \frac{f_0^{1/3} B_r^{1/3}}{FoM_N} = \frac{L^{4/3} f_0 B_r^{1/3}}{F_M P_d^{1/3}}.$$
 (28)

The limit to  $FoM_S$ 

$$FoM_{S,limit} \approx 2.5 f_0^{1/3} B_r^{1/3}$$
 (29)

depends on  $1/FoM_{N,limit}$ , the operating frequency, and bandwidth, where the maximum  $B_r = 2$ .

# B. Dynamic-Range FoMs

An intermediate FoM (i.e. a linearity FoM) can be based on (24):

$$FoM_L = \sqrt[3]{\frac{P_L}{P_d L f_0}}.$$
(30)

In (30),  $P_{comp}$  is replaced with the linearity measure,  $P_L$  in mW, as defined in (11) with input-referred P<sub>1dB</sub> ( $IP_{1dB}$ ). Furthermore, the cubic root of the left-hand side of (24) is then taken to give (30). This cubic root is introduced intentionally to obtain  $P_d^{1/3}$  (as in  $FoM_N$ ), and does not alter the interdependence of its constituent parameters. Consequently, the trade-offs between all parameters in  $FoM_N$ ,  $FoM_S$ , and  $FoM_L$  are relative to  $P_d^{1/3}$ , thereby permitting the relative comparison of these FoMs. The limit for  $FoM_L$  can also be obtained using  $\mathcal{L}^{1/3}$  from (24). On its own, this limit is not very useful because the linearity of a single transistor, which led to (24), can be improved by adding additional transistors, and many LNAs would be expected to outperform this "limit." However, while additional transistors can improve linearity, they also increase noise measure and worsen  $FoM_N$ . Therefore, a dynamic-range FoM, which relates  $FoM_L$  and  $FoM_N$ , is proposed as

$$FoM_D \equiv \frac{FoM_L}{FoM_N} \tag{31}$$

Substituting (25) into (31) gives

$$FoM_D = \frac{P_L^{1/3} L f_0^{1/3}}{F_M P_d^{2/3}}.$$
(32)

For a 1-dB compression, where  $G_c \approx 0.9$  and  $\rho = 0.5$ , the limit on  $FoM_D$  is found by substituting the values of the constants in the right-hand side of (24) to obtain

$$FoM_{D,limit} \approx \frac{\left(10^{-3}\,\mathrm{W}\right)^{1/3}}{\left(10^{-6}\,\mathrm{m}\right) \times \left(10^{9}\,\mathrm{Hz}\right)^{1/3}} \frac{\mathcal{L}^{1/3}}{\mathcal{N}} \approx 1.6.$$
 (33)

Note that  $FoM_D$  is not a substitute for a dynamic range  $DR \equiv IP_{1dB}/F$ . The two metrics have different purpose.  $FoM_D$  identifies the interdependence of LNA performance parameters and prescribes the expected trade-offs between them. In contrast DR shows the range between the smallest and largest useful signal levels.

Alternatively, when bandwidth is a consideration,  $FoM_S$  and  $FoM_L$  are used to define

$$FoM_D^{BW} = FoM_L \times FoM_S = \frac{P_L^{1/3}Lf_0^{2/3}}{F_M P_d^{2/3}} B_r^{1/3}.$$
 (34)

which has a bandwidth- and frequency-dependent limit of

$$FoM_{D,limit}^{BW} \approx 1.6 \times f_0^{1/3} B_r^{1/3}.$$
 (35)

# V. DISCUSSION

Using the data in the LNA survey [6], the four proposed FoMs are calculated and plotted in Fig. 1 along with the limit lines.

In [9], a regression analysis was performed to identify trends in the LNA data to propose FoMs. The FoMs in [9, (3)-(5)] are equivalent to  $(FoM_N)^{-1}$ ,  $FoM_S$ , and  $FoM_D^{BW}$  and can be written as

$$\begin{cases} \widetilde{FoM_N} = \frac{T_{cas}^{1.1} P_0^{0.32}}{L(f_0 + 14.2)^2} \\ \widetilde{FoM_S} = \frac{L \times B_r^{0.32} (f_0 + 17.4)^{2.3}}{T_{cas} P_0^{0.31}} \\ \widetilde{FoM_D^{BW}} = \frac{OIP3 \times L \times B_r^{0.32} (f_0 + 17.4)^{2.3}}{T_{cas} P_0^{0.31}}, \end{cases}$$
(36)

where  $T_{cas} = T_0 F_M$  and the  $\tilde{\cdot}$  indicates that these are empirical results based on a regression analysis, rather than the derived equations reported in this paper. These FoMs are based on LNA measurements that have been compiled over the last 20 years. The shape and the powers of the terms in (36) closely resemble the configurations of the FoMs proposed in Section IV. The most obvious exception is the frequency term, which has a power of 2/3 in (25) and (34), 1 in (28), and 1/3 in (32), in contrast, 14.2 GHz and 17.4 GHz offsets are added to  $f_0$  in (36), and the powers of 2 and 2.3 are used. These offsets allowed the regression analysis to provide a significantly better fit to the data and enabled better modeling of the measured noise factors that exhibit changes in slope with frequencies near these offsets. The change in slope follows the existing data rather than the theoretical expectations that form the basis of (25), (28), (32), and (34). However, the best least-squares power fit to  $(f_0 + 14.2)^2$ and  $(f_0 + 17.4)^{2.3}$  from 0.1 to 10 GHz takes the form of  $f_0^{0.3}$  and  $f_0$  with  $R^2$  values of 0.84 and 0.99, respectively. This fit reflects the theoretical expectations in the FoMs proposed in this work albeit with a larger difference (2/3 vs 1) for the power of  $f_0$  in  $FoM_D^{BW}$ and  $\widetilde{FoM_D^{BW}}$ . When deriving  $\widetilde{FoM_D^{BW}}$ , [9] did not identify much correlation between the output-referred IP3 (OIP3) and the other parameters in the data, and, as a result,  $FoM_S$  was simply multiplied by OIP3 to realize  $FoM_D^{BW}$ . Also, unlike Assumption #2,  $C_{gs}$  is not the only parasitic capacitance present in real circuits; indeed, the frequency dependence is expected to become more pronounced at high frequencies as reflected in (36). In addition, the power term for L is slightly lower in (36) than in (25) and (28). The data shows that the power term for L is lower than 1 for LNAs with L > 0.65 nm, and higher than 1 when  $L \leq 65$  nm. For  $FoM_D^{BW}$ , the power of L is the same as in  $FoM_D^{BW}$ . Fig. 1(c) shows increased gap between  $FoM_D$ and  $FoM_{D,limit}$  above 10 GHz, possibly due to more pronounced higher-order reactive distortions.

In this work,  $P_{1dB}$  was selected as a linearity parameter to be used in  $FoM_D^{BW}$ . The relationships between  $P_{1dB}$  and L,  $P_d$ , and  $f_0$ were identified given the LNA-related assumptions. An additional ~70 LNAs have been added to [6] since the publication of [9]. The current trends in [6] indicate a reduction in the power of  $P_d$  to 0.2 and a reduction in the power of L to 0.9.

#### VI. CONCLUSION

This article identified LNA-related performance trade-offs based on the theoretical description of single-transistor circuits. Seven assumptions, made during these derivations, are clearly identified. From these trade-offs, four FoMs were proposed, and their limits with respect to LNA performance were derived. These FoMs normalize trade-offs by transistor length aiming to highlight the merits of LNA design methodologies. The FoMs only use data that are readily available in literature. Finally, the identified FoMs were compared to the results of a regression analysis of experimental results for nearly 500 CMOS LNAs reported over the past 20 years [6]. As the results of this comparison showed, the identified FoM exhibited reasonable agreement with the data in [6], even for the simplified model used in this work.

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