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(54) **WIRELESS RECEIVER WITH HIGH LINEARITY**

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(71) Applicant: **MediaTek Singapore Pte. Ltd.,**
Singapore (SG)

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(72) Inventors: **Yuan-Ching LIEN**, Taipei City (TW);
Bernard Mark TENBROEK, Kent (GB);
Eric KLUMPERINK, Enschede (NL);
Bram NAUTA, Enschede (NL)

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(57) **ABSTRACT**

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A wireless receiver with high linearity, having an out-band signal bypass filter, a mixer, and a baseband circuit. The out-band signal bypass filter has a first terminal and a second terminal respectively receiving a positive differential signal and a negative differential signal from a former-stage circuit, and the out-band signal bypass filter provides an out-band signal bypass path from the first terminal to the second terminal. The mixer receives a filtered signal from the out-band signal bypass filter. The baseband circuit is coupled to the mixer for generation of an in-phase signal and a quadrature phase signal.

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