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(54) **FREQUENCY DIVIDER**

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See application file for complete search history.

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(57) **ABSTRACT**

A frequency divider comprising, a first latch circuit and a second latch circuit, the second latch circuit being crossed-coupled to the first latch circuit. Each latch comprises a respective sense amplifier coupled to a respective latch. The sense amplifiers comprise a first clock input for receiving a first clock signal. The latches comprise a second clock input for receiving a second clock signal having a second frequency, the second frequency being substantially double the first frequency.

**20 Claims, 2 Drawing Sheets**

