

# Unified Frequency-Domain Analysis of Switched-Series- $RC$ Passive Mixers and Samplers

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**Abstract**—A wide variety of voltage mixers and samplers are implemented with similar circuits employing switches, resistors, and capacitors. Restrictions on duty cycle, bandwidth, or output frequency are commonly used to obtain an analytical expression for the response of these circuits. This paper derives unified expressions without these restrictions. To this end, the circuits are decomposed into a polyphase multipath combination of single-ended or differential switched-series- $RC$  kernels. Linear periodically time-variant network theory is used to find the harmonic transfer functions of the kernels and the effect of polyphase multipath combining. From the resulting transfer functions, the conversion gain, output noise, and noise figure can be calculated for arbitrary duty cycle, bandwidth, and output frequency. Applied to a circuit, the equations provide a mathematical basis for a clear distinction between a “mixing” and a “sampling” operating region while also covering the design space “in between.” Circuit simulations and a comparison with mixers published in literature are performed to support the analysis.

**Index Terms**—Frequency conversion, linear periodically time variant (LPTV), low noise, noise folding, passive mixing, voltage sampling

## I. INTRODUCTION

**I**N radio front ends, mixers normally perform frequency conversion, with continuous-time input and output signals. Samplers convert a continuous-time input into a discrete-time output signal but can also provide frequency conversions (aliasing). Mixing and sampling seem to be quite different functions and are analyzed differently but can be implemented by similar circuits. Some examples are shown in Fig. 1.

The basic building block of these circuits consists of a resistor and a capacitor in series, which are switched to the input voltage  $V_{in}$ . As there is no parallel resistance across the capacitance when the switch is off, the capacitor holds its charge in the OFF state. The circuits are purely passive because the switches act as time-variant resistances and no power gain is possible. Still, the passive circuits in Fig. 1 have very interesting properties, like very high linearity [1], upconversion of a low-pass

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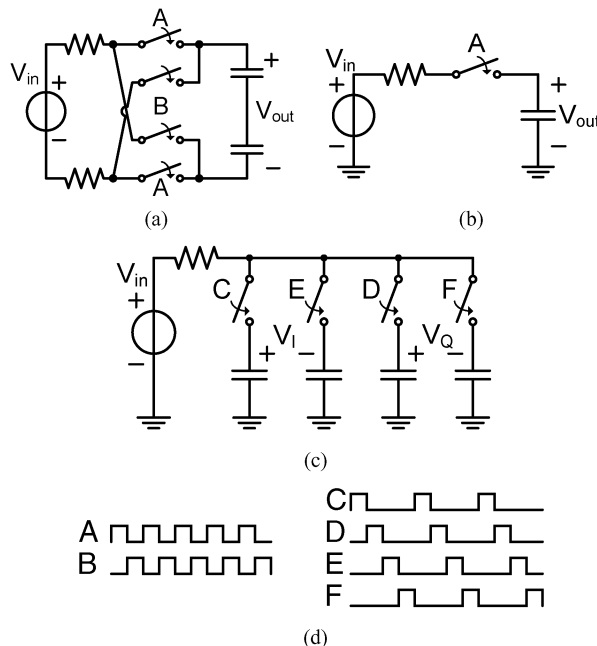


Fig. 1. Switched- $RC$  application examples. (a) Switching mixer. (b) Sampling mixer/track and hold. (c) In-phase/Quadrature mixer with (d) polyphase clocks A–F.

baseband filter into an RF bandpass filter [2], and the possibility to cancel local oscillator (LO) harmonics [3]. We will analyze the transfer function and noise of these circuits in this paper assuming a perfect hold operation. Thus, the analysis does not hold for circuits with a (low) resistor in parallel to the capacitor [4], which is also often used in “current-driven” passive mixers [5]–[7]. Moreover, charge sampling [8] circuits are not covered, as they periodically short circuit the capacitors and dump the capacitor charge to ground.

Individual switched-series- $RC$  circuits have been analyzed individually in literature by posing restrictions on the design parameters. The differential (DI) switching mixer [Fig. 1(a)] has a fixed 50% duty cycle [9]. The switched  $RC$  in Fig. 1(b) is not only in use as a sampling mixer [10], [11] but also as a track-and-hold sampler [12]. In both cases, the  $RC$  time is considered to be infinitely small. The conversion gain and noise figure (NF) for the  $I/Q$  image reject mixer [Fig. 1(c)] as proposed by Tayloe [13] have only been calculated for dc output frequency [14].

In this paper, we propose a unified frequency-domain analysis that can be applied to *all* of these switched-series- $RC$  circuits and for arbitrary duty cycle,  $RC$  time, and output frequency. In a two-step approach, we will combine single-state building blocks, called kernels, with polyphase clocking into multistate systems. The first step investigates the effect of combining the outputs of multiple kernels, switched with polyphase clocks. In the second step, the precise behavior of a single kernel is

analyzed. The switching nature of the networks requires us to use linear periodically time-variant (LPTV), instead of linear time-invariant (LTI), network theory.

First, in Section II, the notation used for LPTV systems is summarized. In Section III, the decomposition into polyphase kernels is qualitatively explained. The effect of polyphase combining is covered in Section IV. Section V performs the LPTV analysis on single-ended (SE) and DI kernels to find the kernel harmonic transfer functions (HTFs). In Section VI, the design space is divided into “sampling” and “mixing” regions, for which approximate gain and noise equations are derived. The theoretical results are applied to an application example in Section VII, while conclusions are presented in Section VIII.

## II. LPTV SYSTEMS

For an LPTV network, the output spectrum  $V_o$  is a summation of an infinite number of frequency-shifted and filtered input spectra  $V_i$  [10]

$$V_o(f_o) = \sum_{n=-\infty}^{\infty} H_n(f_o) V_i(f_o - n f_s) \quad (1)$$

where  $f_s = 1/T_s$  is the frequency for which the system is periodic,  $H_n(f_o)$ 's are the HTFs,  $n$  is the harmonic index, and  $f_o$  is the frequency at the output. Each HTF is the transfer function for the frequency shift  $n f_s$ . For the argument of  $V_i$ , the shorthand notation for the input frequency is introduced as

$$f_i \equiv f_o - n f_s. \quad (2)$$

For random signals, the relation between the input power spectral density (PSD)  $N_i$  and the output PSD  $N_o$  in LPTV systems is [15]

$$N_o(f_o) = \sum_{n=-\infty}^{\infty} |H_n(f_o)|^2 N_i(f_o - n f_s). \quad (3)$$

One of the HTFs  $H_n$ , with index  $n = w$ , renders the desired frequency conversion. Usually, for a downconversion mixer,  $w = -1$ , and for Nyquist sampling,  $w = 0$ . The conversion gain  $CG_w(f_o)$  depends upon  $w$  such that

$$CG_w(f_o) = H_w(f_o). \quad (4)$$

The single sideband NF also depends on  $w$

$$F_w(f_o) = \frac{SNR_{in}}{SNR_{out}} = \frac{N_o(f_o) + N_{o,int}(f_o)}{N_i(f_o - w f_s)} \cdot \frac{1}{|CG_w(f_o)|^2} \quad (5)$$

$$NF_w(f_o) = 10 \log(F_w(f_o))$$

where  $N_{o,int}(f_o)$  is the output noise PSD due to internal noise sources.

## III. DECOMPOSITION INTO POLYPHASE KERNELS

The circuits in Fig. 1 share the following properties.

- 1) The capacitor voltages are the circuit states.
- 2) The output voltage is a combination of states.
- 3) The clocks are polyphase.

A set of clocks is polyphase if they have the same duty cycle and start at regularly spaced intervals within the period time. The clocks are not allowed to overlap when high. Familiar examples to combine output voltages include the following.

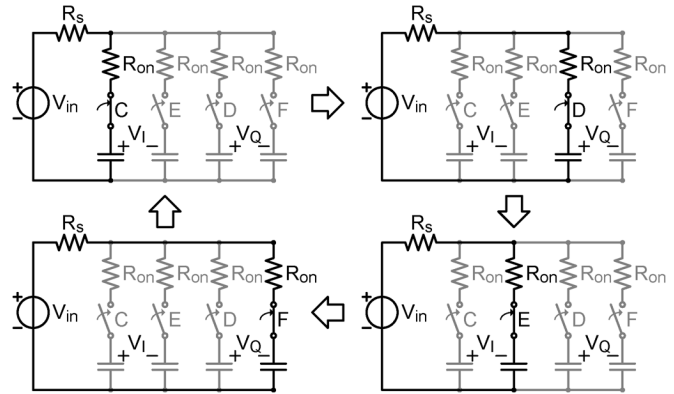


Fig. 2. Polyphase SE kernel example.

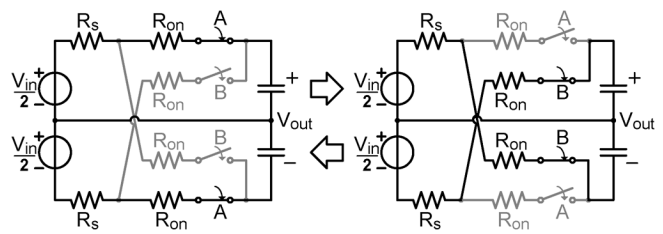


Fig. 3. Polyphase DI kernel example.

- 1) Subtract outputs to cancel even LO harmonics [Fig. 1(a)].
- 2) Take in-phase and quadrature outputs for image rejection [Fig. 1(c)].

An increasing number of states increase the complexity of the LPTV analysis. However, because the clocks do not overlap, the capacitor voltages (the circuit states) are independent. Therefore, we apply a two-step approach.

- 1) Calculate the effect of combining the capacitor voltages in a polyphase system (Section IV).
- 2) Calculate the transfer to a single capacitor voltage with a single-state LPTV calculation (Section V).

With this approach, the combination of basic single-state building blocks (kernels) into complex multistate systems is possible.

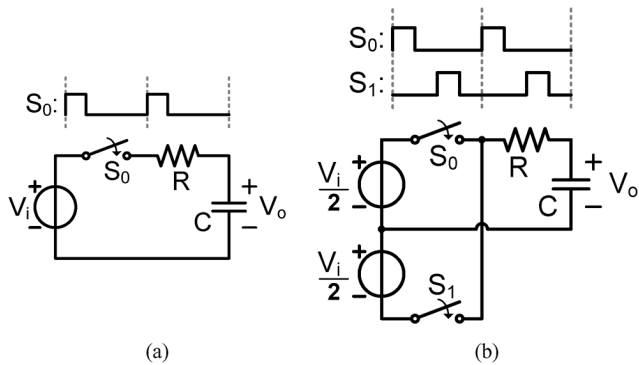
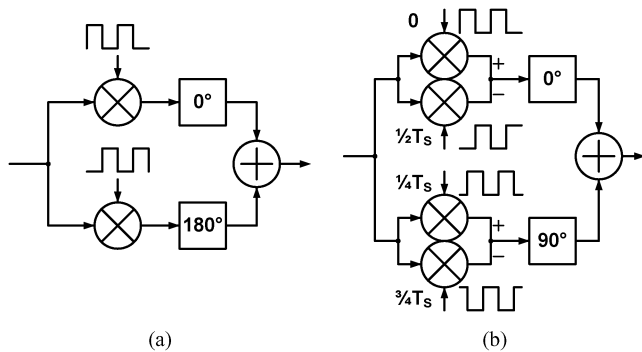
### A. SE and DI Kernel Examples

To clarify the polyphase kernel concept, the IQ mixer in Fig. 1(c) is plotted step by step in Fig. 2. The switches are modeled with infinite off-resistance and finite on-resistance  $R_{on}$ . The source is characterized by the resistance  $R_s$ . During each of the switch intervals, there is a single current loop flowing from the source through  $R_s$  and  $R_{on}$  into the capacitor and back to the source. For calculating the transfer function,  $R_s$  and  $R_{on}$  can be replaced by the equivalent series resistance

$$R = R_s + R_{on}. \quad (6)$$

Analysis of this current loop, defined as the SE kernel, is sufficient to find the capacitor voltage.

For DI inputs, a second kernel is needed, as the example in Fig. 3 shows. Here, a capacitor is connected to the input twice in each period. Compared to the SE kernel [Fig. 4(a)], in the definition of the DI kernel [Fig. 4(b)], the second switch is delayed half a period after the first switch and has the same duty cycle.


 Fig. 4. (a) SE kernel and (b) DI kernel;  $S_0$  and  $S_1$  have duty cycle  $D$ .

 Fig. 5. Polyphase multipath example (a) with  $L = 2$  and  $D = 50\%$  and (b) with  $L = 4$  and  $D = 50\%$ .

### B. Kernel NF

In the kernels, the noise contributions from resistors  $R_s$  and  $R_{on}$  can be represented by an equivalent noise source with white PSD  $N_i$  and  $N_{int}$ . The ratio between the noise powers is equal to the ratio in resistance

$$\frac{N_{int}}{N_i} = \frac{R_{on}}{R_s}. \quad (7)$$

Both of these equivalent voltage noise sources appear in series with the resistors and can be moved in the loop toward the input voltage source. Applying this to (3)–(5) results in a simpler expression for the kernel NF

$$F_w(f_o) = \underbrace{\left(1 + \frac{R_{on}}{R_s}\right)}_A \cdot \underbrace{\frac{\sum_{n=-\infty}^{\infty} |H_n(f_o)|^2}{|H_w(f_o)|^2}}_B. \quad (8)$$

Part A corresponds to the LTI NF for the SE kernel with closed switch. Part B represents the noise folding due to the circuit switching.

Therefore, for calculating the NF, the LPTV kernel analysis can be done with the total resistance  $R$ , and the effect of  $R_{on}$  can be factored in afterward.

## IV. POLYPHASE MULTIPATH SYSTEMS

First, the effect of combining multiple kernels in a polyphase multipath system is studied. Two familiar examples of a polyphase system are as follows:

- 1) DI mixer [Fig. 5(a)]; ,
- 2) Hartley image rejection mixer [Fig. 5(b)].

In general, an  $L$ -path polyphase system has  $L$  independent parallel LPTV circuits (with HTFs  $H_n$ ) driven by a clock shifted  $l \cdot (T_s/L)$  in time, where  $l$  is the path number. Before combining, each path is phase shifted with  $-w \cdot l \cdot 360^\circ/L$ , where  $w$  indicates the desired frequency shift ( $w = -1$  for the examples). The signal contributions of all paths are then summed into a single output. The effects of such a system on deterministic signals and random noise are examined in this section.

### A. Signals

From Fourier analysis, it is known that shifting a signal in time causes a linear phase shift in the frequency domain. Mathematically, if the time shift is  $\sigma_d$ , the Fourier pair of signal  $v$  is

$$v(t - \sigma_d) \xleftrightarrow{\mathcal{F}} V(f) e^{-j2\pi f \sigma_d}. \quad (9)$$

For an LPTV system with HTFs  $H_n$ , shifting the switching moments in time has the same effect as shifting the input and output in the opposite time direction

$$\begin{aligned} v_i(t + \sigma_d) &\xleftrightarrow{\mathcal{F}} V_i(f_i) e^{j2\pi f_i \sigma_d} \\ v_o(t + \sigma_d) &\xleftrightarrow{\mathcal{F}} V_o(f_o) e^{j2\pi f_o \sigma_d} \\ V_o(f_o) e^{j2\pi f_o \sigma_d} &= \sum_{n=-\infty}^{\infty} H_n(f_o) V_i(f_o - n f_s) e^{j2\pi(f_o - n f_s) \sigma_d}. \end{aligned} \quad (10)$$

Bringing the phase terms together

$$V_o(f_o) = \sum_{n=-\infty}^{\infty} H_n(f_o) e^{-j2\pi n f_s \sigma_d} V_i(f_o - n f_s). \quad (11)$$

The HTFs get a phase term that is equal to  $e^{-j2\pi n f_s \sigma_d}$  when the switching moments are shifted forward by  $\sigma_d$  in time. Therefore, the  $l$ th path has a phase shift of  $n \cdot l \cdot 360^\circ/L$  due to the delayed clocking.

Together with the phase shifter blocks, each path  $l$  has the total phase term

$$e^{-j2\pi n l/L} e^{j2\pi w l/L} = e^{-j2\pi(n-w)l/L}. \quad (12)$$

Summing the signals of all paths gives the HTFs of the total system

$$\begin{aligned} H_{n,\text{polyphase}}(f_o) &= \sum_{l=0}^{L-1} H_n(f_o) e^{-j2\pi(n-w)l/L} \\ &= \begin{cases} 0, & \frac{n-w}{L} \neq \text{integer} \\ L \cdot H_n(f_o), & \frac{n-w}{L} = \text{integer} \end{cases} \end{aligned} \quad (13)$$

Therefore, a polyphase multipath system yields a processing gain of  $L$  for all frequency shifts satisfying

$$n = i \cdot L + w, \quad \text{where } i = \dots, -2, -1, 0, 1, 2, \dots \quad (14)$$

while the transfer functions for all other frequency shifts are zero. For example, with  $L = 2$  and  $w = -1$ , this results in cancellation for all  $n = \dots, -2, 0, 2, \dots$ . For all  $L > 2$ , either  $n$  or  $-n$  or both are canceled, resulting in image rejection.

### B. Noise

For a comparison between the noise PSD of a single kernel and  $L$  kernels combined in a polyphase manner, regard the

example in Fig. 2. We observe the following for a polyphase system.

- 1) Each path has a separate  $R_{\text{on}}$ .
- 2)  $R_s$  is connected to the paths at different intervals.

The multiple  $R_{\text{on}}$ 's are physically separate resistances, so their noise is completely uncorrelated. The polyphase combination will add the noise powers so that the output noise due to  $R_{\text{on}}$  increases with the number of paths  $L$ .

The source resistance  $R_s$  is shared between paths but is never connected to several paths at the same time. Assuming white noise, the noise voltage at one time is completely uncorrelated to the noise voltage at a different time. Because the polyphase clocks do not overlap, the noise powers due to  $R_s$  will also be uncorrelated and will add in power.

As a consequence, the polyphase system noise is  $L$  times the noise of a kernel alone

$$N_{o,\text{polyphase}} = N_{o,\text{kernel}} \cdot L \quad (15)$$

where  $N_{o,\text{kernel}}$  is calculated with (3). From (13), the signal power is

$$S_{o,\text{polyphase}} = S_{o,\text{kernel}} \cdot L^2. \quad (16)$$

Then, according to (5)

$$F_{\text{polyphase}} = \frac{F_{\text{kernel}}}{L}. \quad (17)$$

Adding polyphase paths lowers the NF as long as the clocks do not overlap.

## V. KERNELS

Section III defined two kernels (shown in Fig. 4) that are sufficient to find the transfer function to each of the capacitor voltages. In this section, the HTFs of the kernels are first qualitatively described to provide intuitive insight and then exactly derived.

### A. Qualitative Analysis

The kernels have only two degrees of freedom: the duty cycle  $D$  and the  $RC$  time associated with the resistor and capacitor. The  $RC$  bandwidth is defined as

$$f_{rc} \equiv \frac{1}{2\pi RC}. \quad (18)$$

For duty cycle  $D$ , the switch will be closed for time  $D \cdot T_s$ . The ratio between the switch-on time and the  $RC$ -time constant will be designated with  $\Gamma$

$$\Gamma \equiv \frac{DT_s}{RC} = 2\pi D \frac{f_{rc}}{f_s}. \quad (19)$$

For large  $\Gamma$ , the output voltage will settle to the input voltage during the switch-on time. For small  $\Gamma$ , the output has no time to settle. The value of  $\Gamma$  will therefore influence the response of the circuit, and we can define two regions with  $\Gamma = 2$  as the border. The choice for this particular border (as opposed to  $\Gamma = 1$  or any other value) is motivated at the end of Section VI-C.

When the  $RC$ -time constant is relatively small ( $\Gamma \gg 2$ ), the behavior of the circuit can be understood by looking at the node voltages (the Thevenin equivalent in Fig. 6). When the switch is closed, the capacitor voltage will follow the input, and when the switch is open, the last voltage will be held on the capacitor.

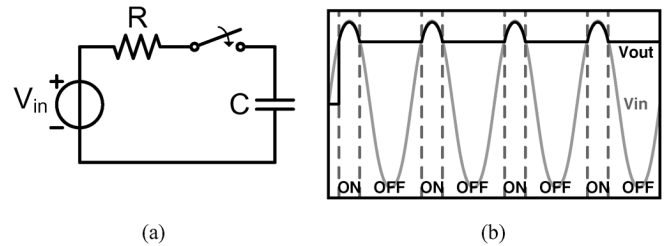


Fig. 6. SE kernel (a) Thevenin equivalent with (b) waveform example for  $\Gamma \gg 2$ .

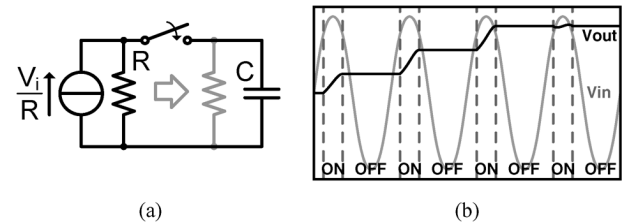


Fig. 7. SE kernel (a) Norton equivalent with (b) waveform example for  $\Gamma \ll 2$ .

Such operation is commonly referred to as track and hold and is widely used in samplers. Therefore, *the part of the design space for which  $\Gamma \gg 2$  is defined as the sampling region.*

For a relatively large time constant ( $\Gamma \ll 2$ ), the operation of the circuit can be understood by looking at the branch currents (the Norton equivalent in Fig. 7). With the large  $RC$ -time constant, the resistor can conceptually be moved after the switch (as illustrated in gray) because there is basically no time for the resistor to discharge the capacitor in a cycle. By doing so, the node before the switch has no defined voltage when the switch is opened. In developing an intuitive feeling for the *output* voltage, this can be ignored. We see that the input current is multiplied by the clock and low-pass filtered at the output, identical in operation to a switching mixer. Therefore, *the part of the design space for which  $\Gamma \ll 2$  is defined as the mixing region.*

Note that the naming of the regions does not reflect the application but merely the similarity in operation, i.e., a sampling region SE kernel can be used as a downconverter.

### B. LPTV Analysis

For the LPTV analysis, the timing definitions from Opal and Vlach [16] in Fig. 8 are used, where

$$\sigma_0 = 0 \quad \sigma_k = \sum_{i=1}^k \tau_i, \quad k = 1, \dots, K. \quad (20)$$

The switching pattern is periodic about time  $T_s$  (having a frequency of  $f_s = 1/T_s$ ) and defines  $K$  intervals during which the system has a valid LTI state-space description. The  $k$ th interval is defined during time  $nT_s + \sigma_k < t < nT_s + \sigma_{k+1}$  (where  $n$  is an integer).

The HTFs of the kernels can be calculated by the method based on state-space system modeling as described by Ström and Signell [17]. Several properties make the calculations easier.

- 1) The polyphase decomposition into single-state kernels reduces all matrix operations into scalar ones.
- 2) The kernel capacitor voltage is both state and output.
- 3) Circuit analysis reveals that the state has no discontinuous jumps during the switching moments.

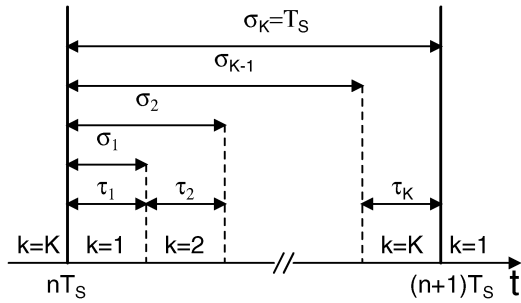


Fig. 8. Timing diagram of the intervals.

With these simplifications, Appendix A derives a method for calculating the HTFs, using the following:

- 1) the state-space matrices  $A_k$  and  $B_k$  for each interval, defined in (67);
- 2) the frequency-domain response of the output sampled at the switching moments  $G_k$ , as defined in (76).

The SE kernel [Fig. 4(a)] has two intervals. Linear analysis reveals that the switch-on interval ( $k = 1$ ) has the state-space description

$$\frac{d}{dt}v_o(t) = -\frac{1}{RC}v_o(t) + \frac{1}{RC}v_i(t), \quad nT_s \leq t < nT_s + \sigma_1 \quad (21)$$

so  $A_1 = -(1/RC) \equiv -2\pi f_{rc}$ , and  $B_1 = (1/RC) \equiv 2\pi f_{rc}$ . The switch-off interval ( $k = 2$ ) has the state-space description

$$\frac{d}{dt}v_o(t) = 0, \quad nT_s + \sigma_1 \leq t < (n+1)T_s \quad (22)$$

so  $A_2 = 0$ , and  $B_2 = 0$ . The switching-moment transfer functions  $G_k$  for the SE kernel are derived in Appendix B.

For the two intervals, (78) is evaluated by inserting  $A_k$ ,  $B_k$ , and  $G_k$

$$\begin{aligned} H_{n,1}(f) &= \frac{f_{rc}}{jf + f_{rc}} \frac{1 - e^{-j2\pi n f_s \tau_1}}{j2\pi n} \\ &\quad - \frac{jf}{jf + f_{rc}} f_s \frac{e^{j2\pi f \tau_2} - 1}{j2\pi f} G_0(f - n f_s) \\ H_{n,2}(f) &= f_s \frac{e^{j2\pi f \tau_2} - 1}{j2\pi f} G_0(f - n f_s). \end{aligned} \quad (23)$$

Summing according to (79) and applying

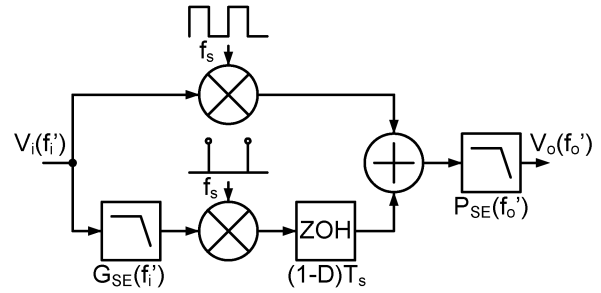
$$1 - \frac{jf}{jf + f_{rc}} = \frac{f_{rc}}{jf + f_{rc}} \quad (24)$$

give the HTFs, which are a function of the output frequency  $f_o = f$ . According to (80)

$$H_n(f_o) = \frac{f_{rc}}{j f_o + f_{rc}} \left[ \frac{1 - e^{-j2\pi n f_s \tau_1}}{j2\pi n} + f_s \frac{e^{j2\pi f_o \tau_2} - 1}{j2\pi f_o} G_0(f_o - n f_s) \right]. \quad (25)$$

This expression can be simplified. The duty cycle  $D$  is the duration of the first interval relative to the period time

$$D = \frac{\tau_1}{T_s} = \tau_1 f_s \quad (1 - D) = \frac{\tau_2}{T_s} = \tau_2 f_s. \quad (26)$$


 Fig. 9. SE kernel block diagram, equivalent to (28). For harmonic index  $n$ , the normalized input frequency is  $f'_i = f'_o - n$ .

If these expressions are substituted into (25), then all frequencies can be normalized to the switching frequency  $f_s$ . The normalized frequencies are defined as

$$f'_o \equiv \frac{f_o}{f_s} \quad f'_{rc} \equiv \frac{f_{rc}}{f_s}. \quad (27)$$

The HTFs are rewritten in normalized frequencies as

$$H_{n,SE}(f'_o) = P_{SE}(f'_o) \left[ \frac{1 - e^{-j2\pi D n}}{j2\pi n} + \frac{e^{j2\pi(1-D)f'_o} - 1}{j2\pi f'_o} G_{SE}(f'_o - n) \right] \quad (28)$$

where

$$P_{SE}(f'_o) \equiv \frac{1}{1 + j \frac{f'_o}{f'_{rc}}} \quad (29)$$

and in normalized input frequency  $f'_i = f_i/f_s = f'_o - n$

$$G_{SE}(f'_i) \equiv G_0(f'_i) = \frac{e^{j2\pi D f'_i} - e^{-2\pi D f'_{rc}}}{e^{j2\pi f'_i} - e^{-2\pi D f'_{rc}}} \frac{1}{1 + j \frac{f'_i}{f'_{rc}}}. \quad (30)$$

Equation (28) describes the *exact SE kernel frequency-domain behavior for all duty cycles  $D$  and RC bandwidths  $f_{rc}$* . The HTFs are also represented by the block diagram in Fig. 9, which consists of filters  $G_{SE}$  and  $P_{SE}$ , a zero-order hold, ideal multipliers, and a summing node.

For the DI kernel in Fig. 4(b), the same procedure is used. The DI kernel has four intervals. The first interval ( $nT_s \leq t < (n+D)T_s$ ) has duty cycle  $D$  with

$$\frac{d}{dt}v_o(t) = -\frac{1}{RC}v_o(t) + \frac{1}{2RC}v_i(t). \quad (31)$$

The second interval ( $(n+D)T_s \leq t < (n+(1/2))T_s$ ) ends after half the period time

$$\frac{d}{dt}v_o(t) = 0. \quad (32)$$

In the third interval ( $(n+(1/2))T_s \leq t < (n+(1/2)+D)T_s$ ), the negative input is connected

$$\frac{d}{dt}v_o(t) = -\frac{1}{RC}v_o(t) - \frac{1}{2RC}v_i(t). \quad (33)$$

For the final interval ( $(n+(1/2)+D)T_s \leq t < (n+1)T_s$ )

$$\frac{d}{dt}v_o(t) = 0. \quad (34)$$

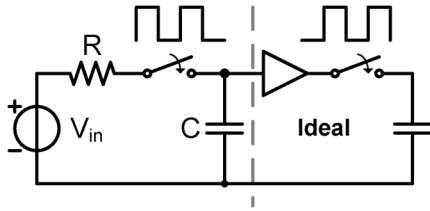


Fig. 10. SE kernel time-discrete schematic.

The calculation of  $G_k$  is very similar to the SE-kernel case in Appendix B.

Ultimately, the HTFs for the DI kernel can be derived as

$$H_{n,\text{DI}}(f'_o) = P_{\text{DI}}(f'_o) \left[ \frac{1 - e^{-j2\pi Dn}}{j2\pi n} + \frac{e^{j2\pi(\frac{1}{2}-D)f'_o} - 1}{j2\pi f'_o} G_{\text{DI}}(f'_o - n) \right] \quad (35)$$

where

$$P_{\text{DI}}(f'_o) \equiv \frac{(1 - e^{-j2\pi \frac{1}{2}n})}{2} \frac{1}{1 + j \frac{f'_o}{f'_{rc}}} \quad (36)$$

$$G_{\text{DI}}(f'_i) \equiv - \frac{e^{j2\pi Df'_i} - e^{-2\pi Df'_{rc}}}{e^{j2\pi \frac{1}{2}f'_i} + e^{-2\pi Df'_{rc}}} \frac{1}{1 + j \frac{f'_i}{f'_{rc}}} \quad (37)$$

Equation (35) describes the *exact DI kernel frequency-domain behavior for all duty cycles  $D$  and RC bandwidths  $f_{rc}$* . It is very similar to the one derived for the SE kernel. The extra  $(1 - e^{-j2\pi(1/2)n})/2$  evaluates to zero for all even  $n$  and to unity for all odd  $n$ .

### C. Discrete-Time Output

In sampling systems, the output is the discrete-time voltage held on the capacitor. In such cases, a second switch and a capacitor (presumed ideal) take over the capacitor voltage for further processing, shown schematically in Fig. 10.

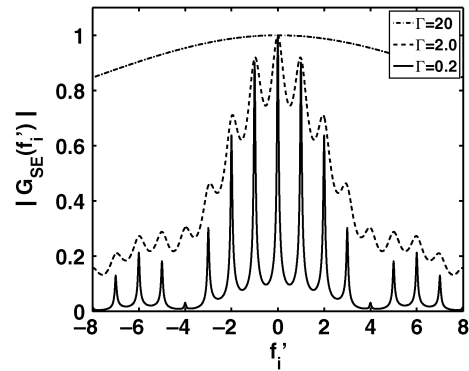
The discrete-time transfer function is simply  $G_{\text{SE}}$ , and the sampled-and-held output is expressed as

$$H_{n,\text{SE,DT}}(f'_o) = \frac{e^{j2\pi f'_o} - 1}{j2\pi f'_o} \cdot G_{\text{SE}}(f'_o - n). \quad (38)$$

## VI. KERNEL SAMPLING AND MIXING REGION

In this section, the distinction between the kernel sampling and mixing regions, as described in the qualitative analysis in Section V, is further explored. Section VI-A derives simplified expressions for  $G_{\text{SE}}$ , which are used in Section VI-B to derive approximate HTFs for the two regions. Using these results, Section VI-C derives the output noise PSD and the total output noise power for each region. Section VI-D motivates the choice for the  $\Gamma = 2$  borderline between the regions and further discusses the region differences.

The equations are written using the sinc form (93), and some integrals and sums from Appendix C are used.


 Fig. 11.  $G_{\text{SE}}$  plotted versus normalized input frequency  $f'_i = f_i / f_s$  for several  $\Gamma$  and duty cycle  $D = 0.25$ .

### A. Switching Moments

A plot of the switching-moment transfer function  $G_{\text{SE}}(f'_i)$  (30) is shown in Fig. 11 for several values of  $\Gamma$  (19). It is clear from the figure that  $G_{\text{SE}}$  is shaped differently for various  $\Gamma$  and that approximate (and simpler) expressions can be derived.

1) *Sampling Region*: The sampling region was defined by  $\Gamma \gg 2$ . The duty cycle is bounded  $0 \leq D \leq 1$ , so (30) has to be considered for large values of  $f'_{rc}$

$$G_{\text{SE}}(f'_i) \approx e^{-j2\pi f'_i(1-D)} \frac{1}{1 + j \frac{f'_i}{f'_{rc}}}, \quad \Gamma \gg 2. \quad (39)$$

2) *Mixing Region*: For the mixing region, defined as  $\Gamma \ll 2$ , the limit of  $\Gamma \rightarrow 0$  is calculated. In order to get a meaningful limit result, we need to scale  $f'_i$  appropriately with  $Df'_{rc}$ , namely, as  $f'_i = aDf'_{rc} - n$  for constant  $a$ ; otherwise, the limit of the HTFs is zero at noninteger  $f'_i$ . The scaling reflects the fact that the peaks around integer  $f'_i$  in Fig. 11 become narrower as  $Df'_{rc}$  decreases. Rewriting (30) in terms of  $a$

$$G_{\text{SE}}(aDf'_{rc} - n) = \frac{e^{j2\pi D(aDf'_{rc} - n)} - e^{-2\pi Df'_{rc}}}{e^{-j2\pi n} e^{j2\pi aDf'_{rc}} - e^{-2\pi Df'_{rc}}} \cdot \frac{1}{1 + j \frac{aDf'_{rc} - n}{f'_{rc}}}. \quad (40)$$

We first consider the limit of small  $f'_{rc}$  at constant  $D$  and  $n \neq 0$

$$\begin{aligned} & \lim_{f'_{rc} \rightarrow 0} G_{\text{SE}}(aDf'_{rc} - n) \\ &= \lim_{f'_{rc} \rightarrow 0} \frac{e^{-j2\pi Dn} - e^{-2\pi Df'_{rc}}}{1 \cdot e^{j2\pi aDf'_{rc}} - e^{-2\pi Df'_{rc}}} \cdot \frac{1}{1 + jaD - j \frac{n}{f'_{rc}}} \\ &= \lim_{f'_{rc} \rightarrow 0} \frac{e^{-j2\pi Dn} \cdot 1 - 1}{(1 + j2\pi aDf'_{rc}) - (1 - 2\pi Df'_{rc}) + \mathcal{O}(f'_{rc}{}^2)} \cdot \frac{f'_{rc}}{-jn} \\ &= \frac{1}{ja + 1} \text{sinc}(Dn) e^{-j\pi Dn}. \end{aligned} \quad (41)$$

Next, a similar calculation gives the limit for small  $D$  at constant  $f'_{rc}$  and  $n \neq 0$

$$\lim_{D \rightarrow 0} G_{\text{SE}}(aDf'_{rc} - n) = \frac{1}{ja + 1}. \quad (42)$$

Calculations for  $n = 0$  yield the same results. Comparing both limits, we can conclude that, for small values of  $\Gamma$  (i.e., either  $D$  or  $f'_{rc}$  or both are small) and corresponding small values of  $f'_o$ , we have

$$G_{SE}(f'_o - n) \approx \frac{\text{sinc}(Dn)e^{-j\pi Dn}}{1 + j\frac{f'_o}{Df'_{rc}}}, \quad \Gamma \ll 2. \quad (43)$$

### B. HTFs

With the calculated  $G_{SE}$ , the complete continuous-time transfer function for the mixing and sampling regions can be derived.

1) *Mixing Region*: For the mixing region, first, the exact HTFs (28) are put into an alternate form (where  $f'_i = f'_o - n$ )

$$\begin{aligned} H_{n,SE}(f'_o) = & D \frac{1}{1 + j\frac{f'_o}{Df'_{rc}}} \frac{1 - e^{-j2\pi Dn}}{j2\pi Dn} \\ & + (1 - D) \frac{e^{j2\pi(1-D)f'_o} - 1}{j2\pi(1-D)f'_o} G_{SE}(f'_i) \\ & + (1 - D) \frac{j\frac{f'_o}{Df'_{rc}}}{1 + j\frac{f'_o}{Df'_{rc}}} \\ & \times \left[ \frac{1}{1 + j\frac{f'_o}{Df'_{rc}}} \frac{1 - e^{-j2\pi Dn}}{j2\pi Dn} \right. \\ & \left. - \frac{e^{j2\pi(1-D)f'_o} - 1}{j2\pi(1-D)f'_o} G_{SE}(f'_i) \right]. \quad (44) \end{aligned}$$

For small  $f'_o$  and small  $Df'_{rc}$

$$\frac{e^{j2\pi(1-D)f'_o} - 1}{j2\pi(1-D)f'_o} G_{SE}(f'_o - n) \approx G_{SE}(f'_o - n) \quad (45)$$

so that (43) gives

$$\frac{e^{j2\pi(1-D)f'_o} - 1}{j2\pi(1-D)f'_o} G_{SE}(f'_o - n) \approx \frac{1}{1 + j\frac{f'_o}{Df'_{rc}}} \frac{1 - e^{-j2\pi Dn}}{j2\pi Dn}. \quad (46)$$

Therefore, the bracketed term in (44) vanishes, and the HTFs for the mixing region become

$$H_{n,SE}(f'_o) \approx \frac{\text{sinc}(Dn)}{1 + j\frac{f'_o}{Df'_{rc}}} e^{-j\pi Dn}, \quad \Gamma \ll 2. \quad (47)$$

The maximum value of the transfer function depends on the sinc of  $D \cdot n$ . For each frequency shift, low-pass filtering as a function of output frequency occurs.

2) *Sampling Region*: The substitution of (39) into (28) gives the HTFs for the sampling region as (48), shown at the bottom of the page. From the two terms, part A is the contribution of the track interval when the switch is closed. Part B is the contribution of the hold interval when the switch is opened.

### C. Kernel Noise

The noise calculations assume white thermal noise generated by the resistance  $R$  with double-sided PSD

$$N_i = 2kTR. \quad (49)$$

Then, the output noise PSD is expressed as (3)

$$N_o(f_o) = 2kTR \sum_{n=-\infty}^{\infty} |H_n(f_o)|^2. \quad (50)$$

The PSD will also be integrated over  $f_o$  to find the total noise power  $P_o$

$$P_o = \int_{-\infty}^{\infty} |N_o(f_o)|^2 df_o. \quad (51)$$

The output noise PSD and the total noise power will be calculated from the approximate HTFs of the mixing (47) and sampling (48) regions.

1) *Mixing Region*: The HTFs for the mixing region have a factor solely depending on  $n$  and a low-pass factor solely depending on  $f_o$ . Therefore, we first calculate the PSD for zero output frequency and then multiply by the equivalent noise bandwidth to get the total output noise power. Summing (47) over  $n$  for  $f_o = 0$  according to (50) and using (97) give

$$N_o(0) = N_i \sum_{n=-\infty}^{\infty} \text{sinc}^2(Dn) = 2kTR \frac{1}{D}, \quad \Gamma \ll 2. \quad (52)$$

Using (95), the equivalent noise bandwidth  $B_n$  is

$$B_n = \int_{-\infty}^{\infty} \left| \frac{1}{1 + j\frac{f_o}{Df'_{rc}}} \right|^2 df_o = \frac{D}{2RC} \quad (53)$$

so that the total noise power is equal to that of an LTI  $RC$  network

$$P_o = N_o(0) \cdot B_n = \frac{kT}{C}. \quad (54)$$

$$H_{n,SE}(f'_o) \approx \frac{1}{1 + j\frac{f'_o}{Df'_{rc}}} \left[ \underbrace{D \text{sinc}(Dn) e^{-j\pi Dn}}_{\text{Part A}} + (1 - D) \underbrace{\frac{\text{sinc}((1-D)f'_o)}{1 + j\frac{f'_o - n}{Df'_{rc}}} e^{-j\pi(1-D)f'_o} e^{-j2\pi Dn}}_{\text{Part B}} \right], \quad \Gamma \gg 2 \quad (48)$$

2) *Sampling Region*: The same strategy can be used for the sampling region HTFs (48). For parts A and B in (48)

$$|A + B|^2 = |A|^2 + AB^* + A^*B + |B|^2 \quad (55)$$

where \* denotes the complex conjugate. Therefore, the sum over  $n$  in (50) for zero output frequency ( $f'_i = -n$ ) is expressed as (56), shown at the bottom of the page. Using (96)–(98), this evaluates to

$$N_o(0) \approx 2kT \left[ \underbrace{\frac{DR}{AA^*}}_{AA^*} + \underbrace{\frac{(1-D)^2}{2f_s C}}_{BB^*} + \underbrace{2(1-D)R}_{AB^* + A^*B} \right], \quad \Gamma \gg 2. \quad (57)$$

Note that, for  $\Gamma \gg 2$ , the  $BB^*$  term, contributed by the hold interval, is dominant.

Finding the equivalent noise bandwidths of these terms is complicated because of the low-pass input filter in (48). The calculations are made easier by assuming the following.

- 1) Part A is dominated in bandwidth by  $(1/(1 + j(f_o/f_{rc})))$ .
- 2) Part B is dominated in bandwidth by  $\text{sinc}((1-D)f'_o)$ .
- 3) The integrated cross terms  $AB^*$  and  $A^*B$  are zero.

Parts A and B do not overlap in the time domain; therefore, the integrated cross-power spectrum must be zero according to Parseval's theorem

$$\int_{-\infty}^{\infty} A(f)B^*(f)df = \int_{-\infty}^{\infty} a(t)b^*(t)dt = 0. \quad (58)$$

Using (94) and (95), the noise equivalent bandwidths are

$$B_{n,AA^*} = \int_{-\infty}^{\infty} \left| \frac{1}{1 + j\frac{f_o}{f_{rc}}} \right|^2 df_o = \frac{1}{2RC} \quad (59)$$

$$B_{n,BB^*} = \int_{-\infty}^{\infty} \text{sinc}^2 \left( (1-D)\frac{f_o}{f_s} \right) df_o = \frac{f_s}{1-D}. \quad (60)$$

Therefore, we see that the track interval (part A) has a lower low-frequency PSD but a much higher noise bandwidth than the hold interval (part B). If we calculate the total noise power

$$\begin{aligned} P_o &= N_{o,AA^*}(0) \cdot B_{n,AA^*} + N_{o,BB^*}(0) \cdot B_{n,BB^*} \\ &= D \frac{kT}{C} + (1-D) \frac{kT}{C} = \frac{kT}{C} \end{aligned} \quad (61)$$

we find that the total integrated noise is the well-known  $kT/C$  for sampled-data systems. It is distributed proportionally to the interval length over the track-and-hold interval.

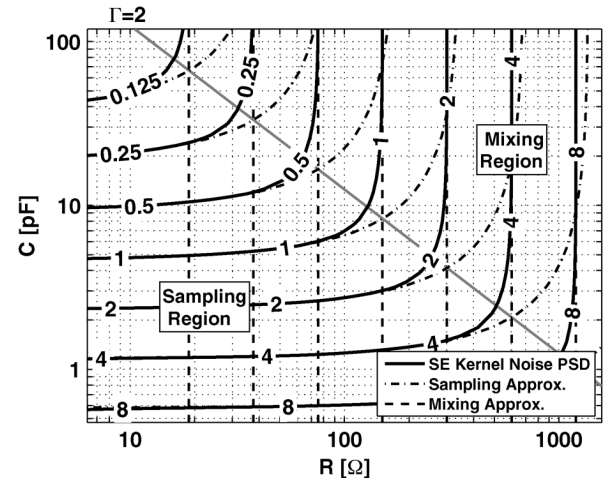


Fig. 12. Contour plot of the SE kernel noise PSD ( $f_o = 0$  Hz,  $f_s = 100$  MHz, and  $D = 25\%$ ) [ $10^{-17}$  V<sup>2</sup>/Hz] calculated by substituting HTFs (28) into (3), with sampling (57) and mixing (52) region approximation.

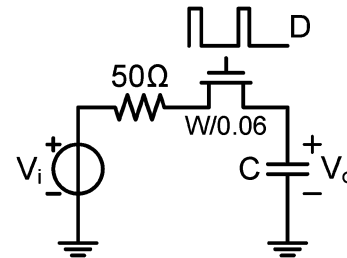


Fig. 13. Application example, SE kernel.

#### D. Region Comparison and Boundary

Fig. 12 shows a contour of the mixing region PSD (52) and sampling region PSD (57) in dashed lines and the 10 000 term evaluation of the exact PSD [(3) and (28)] in solid lines. In their respective regions, the approximations are fairly good and show an interesting property.

- 1) For the mixing region, the noise PSD is determined mostly by the resistance  $R$ .
- 2) For the sampling region, the noise PSD is determined mostly by the capacitance  $C$ .

When dimensioning a circuit for noise, this difference between the two regions is crucial. Furthermore, we can see that (52) and (57) are equal for  $\Gamma = 2$ , i.e., the dashed lines intersect on the gray  $\Gamma = 2$  line. This motivates our choice for the border between the two regions.

It is not surprising that the total integrated noise power is equal to  $kT/C$  for *both* regions. Usually, the integrated noise is obtained with a time-domain approach [12], and it is comforting that our frequency-domain analysis produces the same results.

For a given switching frequency  $f_s$ , duty cycle  $D$ , and  $RC$  bandwidth  $f_{rc}$ , the kernel HTFs are a function of the harmonic

$$N_o(0) = N_i \sum_{n=-\infty}^{\infty} \left[ \underbrace{D^2 \text{sinc}^2(Dn)}_{AA^*} + \underbrace{(1-D)^2 \left| \frac{1}{1 - j\frac{n}{f_{rc}}} \right|^2}_{BB^*} + \underbrace{(1-D) \frac{D \text{sinc}(Dn)}{1 + j\frac{n}{f_{rc}}} e^{j\pi Dn}}_{AB^*} + \underbrace{(1-D) \frac{D \text{sinc}(Dn)}{1 - j\frac{n}{f_{rc}}} e^{-j\pi Dn}}_{A^*B} \right] \quad (56)$$



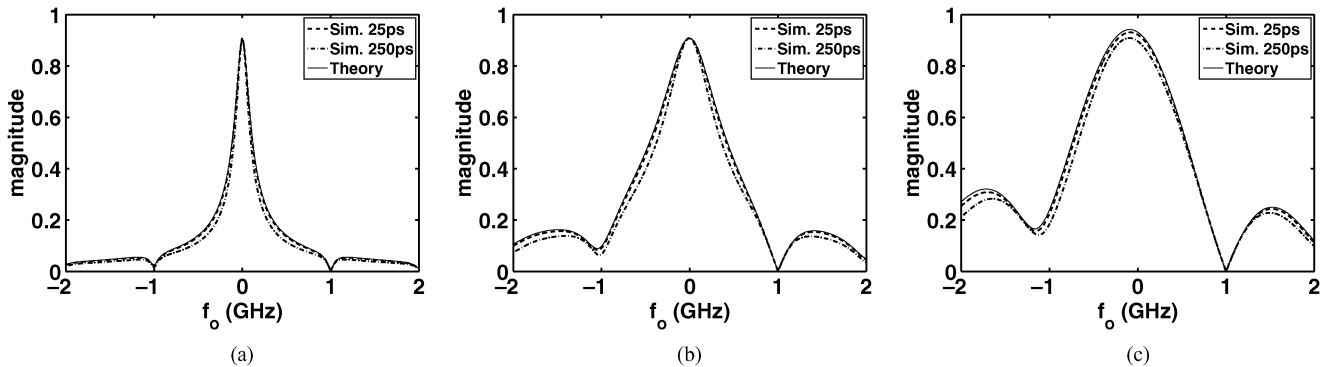


Fig. 14. SE kernel simulated and calculated downconversion HTF ( $n = -1$ ) for 25% duty cycle and several values of  $\Gamma$ . An ideal square-wave clock of 1 GHz and fixed  $R_{sw} = 50 \Omega$  is used for theoretical derivations, whereas finite rise and fall times of 25 and 250 ps are used in the simulations. (a) Mixing region,  $\Gamma = 1/2$ . (b) Borderline,  $\Gamma = 2$ . (c) Sampling region,  $\Gamma = 8$ .

index  $n$  and the output frequency  $f_o$ . We now investigate the dependence of the approximate HTFs on these two arguments. The mixing region HTFs (47) depend upon  $n$  and  $f_o$  such that

$$|H_{n,SE}(f'_o)| \propto |\text{sinc}(Dn)|, \quad \Gamma \ll 2 \quad (62)$$

$$|H_{n,SE}(f'_o)| \propto \frac{1}{\left|1 + j \frac{f'_o}{Df_{rc}}\right|}, \quad \Gamma \ll 2 \quad (63)$$

a low-pass filter as a function of *output frequency*  $f_o$  and a sinc filter as a function of *frequency shift*  $nf_s$ . Similarly, the sampling region HTFs (48) for low duty cycle  $D$  depend on  $n$  and  $f_o$  like

$$|H_{n,SE}(f'_o)| \propto \frac{1}{\left|1 + j \frac{n}{f'_{rc}}\right|}, \quad \Gamma \gg 2 \quad (64)$$

$$|H_{n,SE}(f'_o)| \propto |\text{sinc}((1-D)f'_o)|, \quad \Gamma \gg 2 \quad (65)$$

a low-pass filter as a function of *frequency shift*  $nf_s$  and a sinc filter as a function of *output frequency*  $f_o$ . We conclude that the finite  $RC$  bandwidth results in filtering of *output frequencies* for the mixing region and filtering of *frequency shifts* for the sampling region.

As a side note, in order to retain the same fractional bandwidth and output noise PSD, for both regions, a scaling of the switching frequency  $f_s$  requires only an inverse scaling of the capacitance  $C$ .

Thus, we see that the analysis renders interesting insights that assist circuit design. We exemplify this further in the next section.

## VII. APPLICATION EXAMPLE

In this section, the application of the derived equations is illustrated by an example downconversion mixer design. Circuit simulations are performed with 65-nm-minimum-length CMOS switches (PSP MOS model [18]) in Cadence, using the periodic steady-state analysis. Suppose that the goal is to dimension a direct-downconversion mixer ( $w = -1$ ) with the following properties:

- 1) 50- $\Omega$  source impedance  $R_s$ ;
- 2) 1-GHz-LO-frequency  $f_s$  full-swing square-wave clock;
- 3) 100-MHz baseband bandwidth;
- 4) SE input;
- 5) I/Q image rejection.

With an SE input, the choice for an SE kernel, shown in Fig. 13, is obvious. For a given total series resistance  $R =$

$50 \Omega + R_{on}$ , capacitance  $C$ , and duty cycle  $D$ , (28) gives the exact HTFs. Arbitrarily beginning with a duty cycle of 25% and a switch-on resistance (set by the NMOS width  $W$ ) equal to the source impedance, Fig. 14 shows the simulated and calculated HTF magnitudes for  $n = -1$  and several values of  $\Gamma$ . In each plot, two simulated curves are included, with 25- and 250-ps clock rise/fall time, respectively. In all cases, the derived equations give an accurate model for the simulated results, even for an almost triangle clock wave.

For the dimensioning of the SE kernel, the derived results in Section VI are used to choose between the mixing and the sampling region. The operating region of the kernel depends on  $\Gamma$ , defined in (19). From the mixing region ( $\Gamma \ll 2$ ) approximate HTFs (47), the following properties were derived:

- 1) antialias sinc filter, with  $-3$ -dB cutoff roughly at  $n = 1/(2D)$  (62);
- 2) baseband low-pass filter with  $-3$ -dB cutoff at  $f_o = D \cdot f_{rc}$  (63);
- 3) output noise PSD scaling with resistance (52).

From the sampling region ( $\Gamma \gg 2$ ) approximate HTFs (48), the following properties were derived:

- 1) antialias low-pass filter with  $-3$ -dB cutoff at  $n = f_{rc}/f_s$  (64);
- 2) baseband sinc filter, with  $-3$ -dB cutoff roughly at  $f_o = f_s/2$  (65);
- 3) output noise PSD scaling mostly with capacitance (57).

Because of the better suppression of higher frequency shifts  $nf_s$  and the ease of setting a baseband bandwidth, the mixing region is chosen. The required capacitance to set the correct bandwidth is (18)

$$C = \frac{D}{2\pi(50 \Omega + R_{on}) \cdot 100 \text{ MHz}}. \quad (66)$$

This also ensures that  $\Gamma < 2$  so that the kernel operates in the mixing region.

With the HTFs from (28) inserted into (8), the kernel NF is shown in Fig. 15. Clearly, lower switch-on resistances give better NFs (mixing region in Fig. 12), and we choose  $R_{on} = 5 \Omega$ .

In Section IV-A, it was concluded that a minimum of three polyphase paths are needed for image rejection. However, I/Q outputs are preferred for demodulation, so a four-path system was chosen ( $L = 4$ ). The resulting mixer circuit is shown in Fig. 16. Fig. 15 also shows the NF versus duty cycle, showing an optimum around  $D = 40\%$ . As the maximum duty cycle avoiding overlap is 25%, this is the choice.

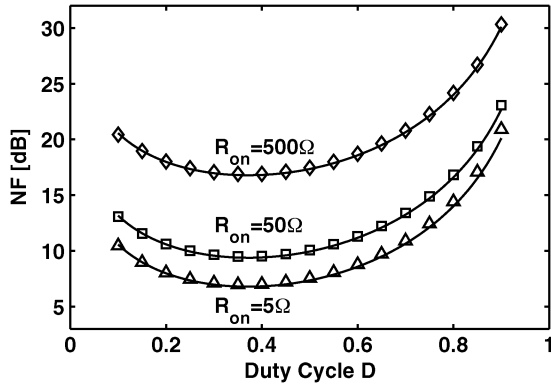


Fig. 15. SE kernel (markers) simulated and (lines) calculated NF for several  $R_{on}$ , swept over duty cycle ( $\Gamma < 2$ ).

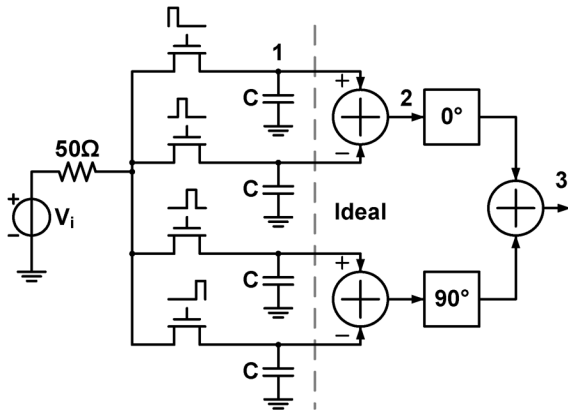


Fig. 16. Application example, complete mixer.

With the dimensioning finished, a simulation of the voltages on the mixer nodes in Fig. 17 confirms the cancellation of the image, and all frequency shifts with even  $n$ . Due to the polyphase combining, a 6-dB improvement in NF and a 12-dB improvement in conversion gain are observed, as predicted by (13) and (17), respectively.

To illustrate the diversity of our analysis, calculations of conversion gain and NF of published passive mixers were compared with the reported numbers in Table I.

## VIII. CONCLUSION

It has been shown that a group of passive mixers and samplers can be described by multipath polyphase systems of SE or DI switched-series- $RC$  kernels. The identification of kernels combined in a polyphase manner greatly simplified the calculation of the HTFs and provides helpful design insights. Unlike the equations in literature, the derived expressions are valid for the complete design space spanned by the duty cycle,  $RC$ -time constant, and output frequency. The derived conversion gain, output noise, and NF equations clearly show two distinct operating regions, defined as the mixing and sampling regions. The borderline between the two regions is determined by the noise behavior and corresponds to a ratio between the switching frequency and  $RC$  bandwidth equal to  $\pi$  times the duty cycle. From the exact HTFs, easier-to-use approximate expressions were derived for each region. It was shown that the output noise PSD depends mostly on the resistance for the mixing region and on the capacitance for the sampling region. Moreover, it was concluded that the finite  $RC$  bandwidth results in a baseband low-pass filter for

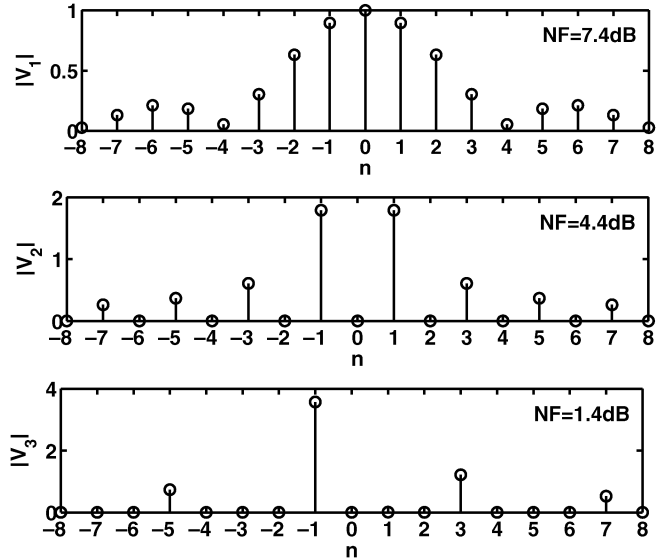


Fig. 17. Magnitude of the simulated HTFs of the voltage on nodes 1, 2, and 3 in Fig. 16 for  $f_o = 0$  Hz.

the mixing region and a built-in “antialias” filter for the sampling region. Our analysis has been supported by circuit simulations and performance calculations of published mixers.

## APPENDIX A LPTV CALCULATION METHOD

Ström and Signell [17] made three observations. First, the response in an interval depends only on the input stimulus and initial interval conditions

$$\frac{d}{dt}v_o(t) = A_kv_o(t) + B_kv_i(t), \quad nT_s + \sigma_{k-1} \leq t < nT_s + \sigma_k \quad (67)$$

where  $v_i(t)$  is the input voltage,  $v_o(t)$  is the output voltage, and  $A_k$  and  $B_k$  are the state-space parameters defining this first-order system. Second, if  $v_{o,k}(t)$  is defined as being equal to zero outside and equal to the output voltage inside the  $k$ th interval (Fig. 18)

$$v_{o,k}(t) = v_o(t) \cdot w_k(t) \quad (68)$$

$$w_k(t) = \begin{cases} 1, & nT_s + \sigma_{k-1} \leq t < nT_s + \sigma_k \\ 0, & \text{elsewhere} \end{cases} \quad (69)$$

then the output is the sum of all  $v_{o,k}(t)$

$$v_o(t) = \sum_{k=1}^K v_{o,k}(t). \quad (70)$$

Finally, the state-space response (67) can be made zero outside the interval by disconnecting the input source and subtracting the final conditions at the interval end [17, eq. (7)]

$$\begin{aligned} \frac{d}{dt}v_{o,k}(t) &= A_kv_{o,k}(t) + B_kv_{i,k}(t) \\ &+ \sum_{n=-\infty}^{\infty} [v_o(t)\delta(t - nT_s - \sigma_{k-1}) \\ &\quad - v_o(t)\delta(t - nT_s - \sigma_k)], \end{aligned} \quad -\infty < t < \infty \quad (71)$$

where

$$v_{i,k}(t) = v_i(t) \cdot w_k(t) \quad (72)$$

and  $\delta(t)$  is the Dirac delta function.

TABLE I  
 PUBLISHED PASSIVE MIXERS

Year Name	$f_s$	Kernel	Duty Cycle $D$	$\Gamma^1$	$L^2$	$w^3$	$\frac{R_{on}}{R_s}$	Reported Gain [dB]	Reported NF [dB]	Calculated Gain [dB] <sup>7</sup>	Calculated NF [dB] <sup>8</sup>
1997 Shahani [9]	1.61GHz	DI	50%	< 2	2	-1	n/a	-3.6 <sup>5</sup>	10 <sup>5</sup>	-3.9	n/a
2005 Zhou [19]	5.1-5.4GHz	SE	50%	< 2	2	-1	20	2.1 <sup>6</sup>	17 <sup>4,6</sup>	2.1	17.1
2009 Soer [1]	0.2-2.0GHz	DI	25%	< 0.2	4	-1	$\frac{1}{10}$	5.1 <sup>6</sup>	1.8 <sup>6</sup>	5.1	1.9
1993 Chan [20]	50MHz	SE	50%	200	2	-19	3	-6 <sup>5</sup>	18 <sup>5</sup>	-6.3 <sup>9</sup>	19.3 <sup>9</sup>
1997 Parssinen [11]	500MHz	SE	18%	> 2	1	-4	n/a	-2 <sup>6</sup>	23 <sup>5</sup>	-2.1	n/a

<sup>1</sup>< 2: Mixing Region, >2: Sampling Region

<sup>2</sup>Number of polyphase paths

<sup>3</sup>Harmonic index of desired frequency conversion

<sup>4</sup>14 dB DSB noise figure reported, SSB noise figure is 17 dB

<sup>5</sup>Measured in publication

<sup>6</sup>Estimated / derived in publication

<sup>7</sup>Gain as defined in (4), with HTFs (28) for the SE kernel and (35) for the DI kernel, including the gain of polyphase combining (13).

<sup>8</sup>NF as defined in (8), with HTFs (28) for the SE kernel and (35) for the DI kernel, including the contribution of polyphase combining (17).

<sup>9</sup>Single-ended to differential conversion introduces an extra 1/2 gain factor and a 1 GHz input bandwidth limits the contributions in (8) to  $n = -20 \dots 20$ .

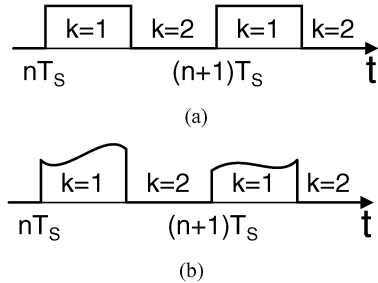


Fig. 18. Example of (a)  $w_1(t)$  and (b)  $v_{o,1}(t)$ .

Because (71) is valid for all  $t$ , the Fourier transform [denoted by  $\mathcal{F}(\cdot)$ ] can be used. The Fourier transforms of (71) and (70) are

$$\begin{aligned} & (j2\pi f - A_k)V_{o,k}(f) \\ &= B_k \cdot \mathcal{F}(v_i(t) \cdot w_k(t)) \\ &+ \sum_{n=-\infty}^{\infty} [\mathcal{F}(v_o(t) \cdot \delta(t - nT_s - \sigma_{k-1})) \\ &\quad - \mathcal{F}(v_o(t) \cdot \delta(t - nT_s - \sigma_k))] \end{aligned} \quad (73)$$

$$V_o(f) = \sum_{k=1}^K V_{o,k}(f). \quad (74)$$

The multiplication of  $w_k(t)$  and  $v_i(t)$  becomes a convolution in the frequency domain resulting in [21, eq. (6.20) + (5.13)]

$$\mathcal{F}(v_i(t) \cdot w_k(t)) = \sum_{n=-\infty}^{\infty} \frac{1 - e^{-j2\pi n f_s \tau_k}}{j2\pi n} e^{-j2\pi n f_s \sigma_{k-1}} V_i(f - n f_s). \quad (75)$$

To obtain a closed-form expression for the transfer function, we want to express the sampled output voltage terms in (73) as a function of the input voltage. Suppose that there is a function  $G_k(f)$  such that the output voltage at switching moment  $t = nT_s + \sigma_k$  can be expressed as

$$\begin{aligned} & \sum_{n=-\infty}^{\infty} \mathcal{F}(v_o(t) \delta(t - nT_s - \sigma_k)) \\ &= \sum_{n=-\infty}^{\infty} [G_k(f) \cdot \mathcal{F}(v_i(t))] * \delta(f - n f_s) \cdot f_s e^{-j2\pi n f_s \sigma_k} \end{aligned} \quad (76)$$

where the  $*$  operator is the convolution integral [21, eq. (3.18)]. Working out the Fourier transform [21, eq. (12.7) + (5.13)]

$$\begin{aligned} & \sum_{n=-\infty}^{\infty} \mathcal{F}(v_o(t) \delta(t - nT_s - \sigma_k)) \\ &= \sum_{n=-\infty}^{\infty} G_k(f - n f_s) \cdot f_s e^{-j2\pi n f_s \sigma_k} V_i(f - n f_s). \end{aligned} \quad (77)$$

We will show that  $G_k(f)$  does exist for the SE and DI kernels.

After filling in the transformed terms, (73) becomes

$$\begin{aligned} V_{o,k}(f) &= \sum_{n=-\infty}^{\infty} H_{n,k}(f) V_i(f - n f_s) \\ H_{n,k}(f) &= \frac{1}{j2\pi f - A_k} \left[ B_k \frac{1 - e^{-j2\pi n f_s \tau_k}}{j2\pi n} e^{-j2\pi n f_s \sigma_{k-1}} \right. \\ &\quad + f_s G_{k-1}(f - n f_s) e^{-j2\pi n f_s \sigma_{k-1}} \\ &\quad \left. - f_s G_k(f - n f_s) e^{-j2\pi n f_s \sigma_k} \right]. \end{aligned} \quad (78)$$

According to (74), the total HTFs are then

$$H_n(f) = \sum_{k=1}^K H_{n,k}(f). \quad (79)$$

The substitution of (78) into (79) is in the form of the HTF definition (1) if the following notation is introduced:

$$f_o = f \quad f_i = f - n f_s. \quad (80)$$

We see that  $G_k$  must have the input frequency  $f_i$  as argument.

## APPENDIX B SE KERNEL $G_k$

In this Appendix, the switching-moment transfer functions  $G_k(f_i)$  are determined for the SE kernel in Fig. 4(a).

During an interval, the LTI system response includes a zero-input term and a zero-initial-value term [21, eq. (8.29) + eq. (8.41)]

$$\begin{aligned} v_{o,k}(t) &= \phi_k(t-t_0)v_{o,k}(t_0) + B_k \int_{t_0}^t \phi_k(t-\tau)v_i(\tau) d\tau \\ \phi_k(t) &= e^{A_k t}. \end{aligned} \quad (81)$$

For the sinusoidal input ( $v_i(t) = e^{j2\pi f t}$ ), the solution can be calculated directly. During interval 1

$$\begin{aligned} v_{o,1}(t) &= e^{-2\pi f_{rc}(t-t_0)}v_{o,1}(t_0) + \frac{1}{1+j\frac{f}{f_{rc}}} \\ &\times \left[ e^{j2\pi f(t-t_0)} - e^{-2\pi f_{rc}(t-t_0)} \right] e^{j2\pi f t_0} \end{aligned} \quad (82)$$

and during interval 2

$$v_{o,2}(t) = v_{o,2}(t_0). \quad (83)$$

Filling in  $t_0 = nT_s$  and  $t = nT_s + \sigma_1$  into (82) gives the output voltage at the interval end  $v_{o,1}(nT_s + \sigma_1)$  given the input and the initial value  $v_{o,1}(nT_s)$ . Since the output voltage is continuous, the initial value of an interval is equal to the final value of the previous interval

$$\begin{aligned} v_{o,2}(nT_s + \sigma_1) &= \lim_{t' \uparrow \sigma_1} v_{o,1}(nT_s + t') \\ v_{o,1}((n+1)T_s) &= \lim_{t' \uparrow \sigma_2} v_{o,2}(nT_s + t'). \end{aligned} \quad (84)$$

Furthermore, (83) gives

$$v_{o,2}(nT_s + \sigma_1) = \lim_{t' \uparrow \sigma_2} v_{o,2}(nT_s + t'). \quad (85)$$

By chaining these expressions, the output voltage after a full cycle can be expressed as

$$\begin{aligned} v_o((n+1)T_s) &= e^{-2\pi f_{rc}\tau_1}v_o(nT_s) + \frac{1}{1+j\frac{f}{f_{rc}}} \\ &\times \left[ e^{j2\pi f\tau_1} - e^{-2\pi f_{rc}\tau_1} \right] e^{j2\pi f nT_s}. \end{aligned} \quad (86)$$

This result can be viewed as a difference equation with a solution that consists of a steady-state and a transient response. For a frequency-domain description, the transient response can be discarded. With the  $z$  transform, it can be calculated that a difference equation of the form

$$v_o((n+1)T_s) = \alpha \cdot v_o(nT_s) + \beta \cdot e^{j2\pi f nT_s} \quad (87)$$

has the steady-state solution [21, eq. (13.32)]

$$v_o(nT_s) = \frac{\beta}{e^{j2\pi f T_s} - \alpha} \cdot e^{j2\pi f nT_s}. \quad (88)$$

Applying (88) into (86) gives

$$v_o(nT_s) = \left[ \frac{e^{j2\pi f\tau_1} - e^{-2\pi f_{rc}\tau_1}}{e^{j2\pi f T_s} - e^{-2\pi f_{rc}\tau_1}} \frac{1}{1+j\frac{f}{f_{rc}}} \right] e^{j2\pi f nT_s} \quad (89)$$

where the term between brackets is defined as  $G_0(f)$ . We will now show that this is indeed  $G_0$  as defined in (76).

Equation (89) is still in the discrete-time domain and gives the output voltage sampled at the switching moments. By inserting delta impulse functions, (89) is brought into the continuous-time domain

$$\sum_{n=-\infty}^{\infty} v_o(t)\delta(t-nT_s) = \sum_{n=-\infty}^{\infty} G_0(f) \cdot e^{j2\pi f t} \delta(t-nT_s). \quad (90)$$

The aforementioned equation gives the output at the switching moments for a single input sinusoid, represented by  $e^{j2\pi f t}$ . After replacing  $e^{j2\pi f t}$  by the more generic notation  $v_i(t)$ , we can take the Fourier transform of (90), resulting in

$$\begin{aligned} \sum_{n=-\infty}^{\infty} \mathcal{F}(v_o(t)\delta(t-nT_s)) \\ = \sum_{n=-\infty}^{\infty} [G_0(f) \cdot \mathcal{F}(v_i(t))] * \delta(f-nf_s) \cdot f_s. \end{aligned} \quad (91)$$

This was derived for  $v_i(t)$  being a single sinusoidal input, but because the system is linear, it actually holds for any input signal  $v_i(t)$ .

Equation (91) corresponds to the definition of  $G_0$  in (76). The same can be done for  $v_o(nT_s + \sigma_1)$  and  $v_o(nT_s + \sigma_2)$  to find

$$\begin{aligned} G_1(f) &= G_0(f)e^{j2\pi f\tau_2} \\ G_2(f) &= G_0(f). \end{aligned} \quad (92)$$

## APPENDIX C INTEGRALS AND SUMS

In this Appendix, the parameters  $a$  and  $b$  are real numbers. The sinc function is defined as

$$\begin{aligned} \text{sinc}(x) &\equiv \frac{\sin \pi x}{\pi x} = \frac{e^{j\pi x} - e^{-j\pi x}}{j2\pi x} \\ \text{sinc}(0) &= 1 \quad \Im(\text{sinc}(x)) = 0. \end{aligned} \quad (93)$$

The integrals using [22, 3.821 9.] and [22, 3.112 2.] are

$$\int_{-\infty}^{\infty} \left| \frac{1 - e^{-j2\pi a x}}{j2\pi x} \right|^2 dx = \frac{2}{\pi^2} \int_0^{\infty} \frac{\sin^2(\pi a x)}{x^2} dx = a \quad (94)$$

$$\int_{-\infty}^{\infty} \left| \frac{1}{1+jbx} \right|^2 dx = \int_{-\infty}^{\infty} \frac{1}{(1+jbx)(1-jbx)} dx = \frac{\pi}{b}. \quad (95)$$

The sums using [22, 1.217 1.], [22, 0.233], and [22, 1.443 3.] are

$$\begin{aligned} \sum_{n=-\infty}^{\infty} \left| \frac{1}{1-jbn} \right|^2 &= 1 + 2 \sum_{n=1}^{\infty} \frac{1}{1+b^2 n^2} = \frac{\pi}{b} \\ &\approx \frac{\pi}{b}, \quad 0 < b \ll 1 \end{aligned} \quad (96)$$

$$\begin{aligned} \sum_{n=-\infty}^{\infty} \left| \frac{1 - e^{-j2\pi an}}{j2\pi n} \right|^2 &= a^2 + 2 \sum_{n=1}^{\infty} \frac{\sin^2 \pi an}{\pi^2 n^2} \\ &= a^2 + \frac{1}{\pi^2} \left[ \sum_{n=1}^{\infty} \frac{1}{n^2} - \sum_{n=1}^{\infty} \frac{\cos 2\pi an}{n^2} \right] \\ &= a. \end{aligned} \quad (97)$$

The sum using [22, 1.441 1.], [22, 1.217 1.], [22, 1.445 2.], and [22, 1.445 1.] is

$$\begin{aligned} &\sum_{n=-\infty}^{\infty} \frac{e^{j2\pi an} - 1}{j2\pi n(1 + jbn)} \\ &= \sum_{n=-\infty}^{\infty} \frac{(e^{j2\pi an} - 1)(1 + jbn - jbn)}{j2\pi n(1 + jbn)} \\ &= \sum_{n=-\infty}^{\infty} \frac{e^{j2\pi an} - 1}{j2\pi n} - b \sum_{n=-\infty}^{\infty} \frac{e^{j2\pi an} - 1}{2\pi(1 + jbn)} \\ &= a + 2 \sum_{n=1}^{\infty} \frac{\sin(2\pi an)}{2\pi n} + \sum_{n=-\infty}^{\infty} \frac{b}{2\pi(1 + b^2 n^2)} \\ &\quad - \sum_{n=-\infty}^{\infty} \frac{b \cos(2\pi an)}{2\pi(1 + b^2 n^2)} - \sum_{n=-\infty}^{\infty} \frac{b^2 n \sin(2\pi an)}{2\pi(1 + b^2 n^2)} \\ &= \frac{1}{2} + \frac{1}{2} \cdot \frac{1 + e^{-2\pi b^{-1}}}{1 - e^{-2\pi b^{-1}}} - \frac{e^{-2\pi ab^{-1}}}{1 - e^{-2\pi b^{-1}}}, \quad 0 < a < 1 \\ &\approx 1, \quad b \rightarrow 0. \end{aligned} \quad (98)$$

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