Optocoupling in CMOS

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ABSTRACT

Around 1970, a new class of circuits that monolithically integrated high power devices with control circuits were proposed. These new circuits, referred to as “power integrated circuits” (PICs) enabled low cost implementation of high power systems such as voltage regulators and high-power amplifiers. Since then, PICs have evolved and are currently used in many systems like audio amplifiers, motor controllers and automotive electronics. The revenue generated by such systems has been estimated to be more than 10 billion USD in 2016.

While according to the “Moore’s law”, continuous advancements in traditional silicon (Si) complementary metal-oxide-semiconductor (CMOS) technologies have been done to implement more processing capabilities at a lower cost, special CMOS technologies have also been developed for PICs to enable some of the “More-than-Moore” applications. These special Bipolar-CMOS-DMOS (BCD) technologies, currently known as the “smart power” integrated circuit technologies, combine the “smartness” of digital circuits with the “power” of power devices.

In many systems in which smart power technologies have been implemented, for safety and/or avoiding interference data communication between different voltages domains with galvanic isolation is required. Communication by means of light across different voltage domains is attractive for such isolated communication links. This is because light eliminates any direct conductive path between the isolated circuits and is robust to external electrical and magnetic interferences.

The principle of data communication with light across isolated voltage domains is used in so-called “optocouplers”. In optocouplers, light emitted by an emitter in one voltage domain is detected by a receiver in another voltage domain. At present, only discrete optocouplers are available; however a discrete implementation increases the cost for PICs. Monolithic implementation of optocouplers without any additional processing (standard CMOS) would be a disruptive technology, enabling several new “smart” PICs at lower cost and area requirements. Research on enabling these integrated optocouplers has been the focus of this research. Most important performance metrics of these integrated optocouplers are the area, energy-per-bit, data rate and “amount” of isolation between two voltage domains.

The main issue with the monolithic implementation of optocouplers
is the absence of an efficient light source in CMOS technologies. Being an indirect band gap semiconductor, forward biased Si light-emitting diodes (LEDs) emit light at infrared wavelengths with low efficiency, while Si photodetectors (PDs) have a relatively low responsivity at those wavelengths. However, Si avalanche mode LEDs (AMLEDs) have a broad emission spectrum in the visible range which has a significant overlap with the responsivity of Si PDs. Therefore in this thesis, the use of AMLEDs is proposed for the monolithic implementation of optocouplers. Another issue however is that Si AMLEDs have a relatively low electrical to optical efficiency, also referred to as quantum efficiency. To compensate for such a low quantum efficiency, single-photon avalanche diodes (SPADs) in CMOS technologies are proposed for the light detection side. As the name suggests, SPADs are highly sensitive PDs which can (theoretically) detect even a single photon.

In the first part of this thesis, the physics of avalanche diodes is discussed in detail which is important to understand the performance of AMLEDs and SPADs. It is shown that the avalanche phenomena can be described by random telegraph signal (RTS) phenomena in the diode currents. Further, from the analysis of these RTS phenomena, the current-voltage (I-V) characteristics in avalanche is explained and an accurate definition of experimental breakdown voltage is provided. Currently quenching-and-recharge circuits (QRCs) for SPADs are designed using some rules-of-thumb. The analysis of RTS phenomena can help in designing an accurate design of QRCs. The optimized QRCs can improve the speed of SPADs by a factor of 3 to 6 compared to conventional counterparts. The analysis of RTS phenomena is also important for obtaining high speed AMLEDs. It is demonstrated that diodes with more defects and/or higher leakage current are better for high speed AMLEDs, exactly the opposite requirements for SPADs.

In the second part of thesis, AMLEDs with integrated driver circuits were designed in a 140 nm SOI CMOS technology. A low power LED driver circuit was demonstrated which is robust to many variations in the properties of the AMLEDs and the driver circuit operating conditions. The demonstrated integrated optical transmitter can be used to achieve a low energy-per-bit for the proposed optical links. Finally, for the first time, this thesis demonstrates a monolithic optical link with very low area requirements (<0.01 mm²) in a standard CMOS technology. The data rates of a few Mbps at the energy consumption of a few nJ/bit are demonstrated.

Overall this thesis demonstrates the physics and applications of avalanche diodes for optocoupling applications. Various physics related issues of avalanche diodes are also discussed which are important for the design of AMLEDs, SPADs and the associated circuits. The results are promising and the dream of monolithic optocouplers is now closer to reality!
SAMENVATTING


Tegelijkertijd met de door de “Wet van Moore” beschreven voortdurende vooruitgang in traditionele Silicium (Si) Complementaire Metaal-Oxide-Halfgeleider (CMOS) technologieën, waardoor we steeds meer rekenkracht krijgen voor een steeds lagere prijs, zijn er ook speciale CMOS technologieën ontwikkeld voor PICs om “Meer-dan-Moore” toepassingen mogelijk te maken. In deze (meestal) Bipolaire–CMOS–DMOS (BCD) technologieën, die ook bekend staan als “Smart Power Integrated Circuit” technologieën, wordt “slimheid” van digitale schakelingen gecombineerd met de “power” van vermogenselektronica.

In veel systemen in Smart-Power technologieën is een galvanische scheidning nodig vanwege ofwel veiligheidsredenen ofwel om interferentie te voorkomen bij communicatie tussen verschillende spanningsdomeinen. Communicatie door middel van licht tussen de verschillende spanningsdomeinen is dan aantrekkelijk. De reden hiervoor is dat bij datatransmissie via licht er geen enkel elektrisch geleidend pad tussen verschillende (delen van) schakelingen nodig is en omdat een dergelijke verbinding immuun is voor externe elektrische en magnetische signalen.

Het principe van datacommunicatie met licht tussen galvanisch gescheiden delen van een elektronisch systeem wordt gebruikt in de zogenaamde “optocouplers”. In optocouplers wordt licht uitgezonden door de lichtbron in het ene spanningsdomein en wordt dat gedetecteerd door een ontvanger in een ander spanningsdomein. Tegenwoordig zijn alleen discrete optocouplers beschikbaar; deze de discrete componenten verhogen de kosten voor PICs echter behoorlijk. Een monolithische implementatie van optocouplers — zonder extra productiestappen, dus in standaard CMOS – zou een grote stap voorwaarts zijn waarmee verscheidene nieuwe “smart” PICs tegen lagere kosten en met een kleiner oppervlakte mogelijk worden.
Het onderzoek naar deze geïntegreerde optocouplers is de focus van het onderzoek beschreven in dit proefschrift. De belangrijkste eisen van deze geïntegreerde optocouplers zijn: klein oppervlakte, lage energie-per-bit, hoge datasnelheid en de “hoeveelheid” isolatie tussen twee spanningsdomeinen.

Het belangrijkste probleem voor de monolithische implementatie van optocouplers is de afwezigheid van een efficiënte lichtbron in CMOS technologieën. Aangezien Si een halfgeleidermateriaal is met een indirecte bandgap van ongeveer 1,2 eV, generereert een licht-uitzending-diode (LED) in silicium infrarood licht met een lage efficiëntie. Hieraan gerelateerd heeft een Si fotodetector (PD) een lage responsiviteit bij dergelijke golflengtes. Si LEDs in avalanche (AMLEDs) genereren daarentegen een relatief breed spectrum in het zichtbare gebied, met een grote overlap met de responsiviteit van Si PDs. In dit proefschrift wordt daarom gebruikgemaakt van AMLEDs als lichtbron voor monolithische optocouplers. Een ander probleem van Si AMLEDs is de relatief lage efficiëntie waarmee elektrische energie naar optische energie wordt omgezet (kwantum efficiëntie). Om de lage kwantum efficiëntie te compenseren, wordt een “single photon avalanche diode” (SPADs) in CMOS gebruikt voor de lichtdetectie. Zoals de naam suggereert, zijn SPADs heel gevoelige PDs, die (theoretisch) zelfs individuele fotonen kunnen detecteren.

In het eerste deel van dit proefschrift wordt in detail de fysica van avalanche-diodes besproken, wat belangrijk is om de prestaties van AMLEDs en SPADs te begrijpen. Er zal worden aangetoond dat avalanche kan worden beschreven door zogenaamd random-telegraph-ruis (RTS) in de diodestroom. Gebruikmakend van de gepresenteerde analyse van RTS kan de stroom-spanning karakteristiek (I-V) worden verklaard en kan een eenduidige definitie worden gegeven van de zogenaamde breakdown-spanning. De meeste huidige Quenching-and-Recharge (QRCs) voor SPADs worden ontworpen met behulp van vuistregels. De analyse van RTS kan gebruikt worden om de QRCs beter te ontwerpen waardoor de snelheid van SPADs verhoogd kan worden met een factor 3 tot 6 in vergelijking met conventionele ontwerpstrategieën. De RTS-analyse is ook belangrijk voor het ontwerp van snelle AMLEDs: er volgt dat diodes met meer defecten en/of een hogere lekstroom resulteren in een sneller gedrag; dit is precies het tegenovergestelde als voor SPADs.

Het tweede gedeelte van dit proefschrift introduceert AMLEDs met geïntegreerde aanstuurelektronica in een 140 nm SOI CMOS technologie. Deze zuinige LED-aanstuurschakeling is inherent robuust tegen spreiding in zowel AMLED-eigenschappen als in werkomstandigheden van de AMLEDs. De gepresenteerde geïntegreerde lichtbron met aanstuurelektronica zorgt ervoor dat een lage energie-per-bit bereikt wordt in optische links. Als laatste demonstreert het werk in dit proefschrift voor het eerst een monolithische geïntegreerde optische link, met een klein oppervlakte (< 0.01 mm²) in een standaard CMOS technologie, met een datasnelheid van enkele Mbps bij een energieverbruik van enkele nJ/bit.
Alles bij elkaar beschrijft dit proefschrift de fysica en toepassingen van avalanchediodes voor optocoupling-toepassingen. Meerdere fysica-gerelateerde problemen voor avalanchediodes zijn besproken, die belangrijk zijn voor het ontwerp van AMLEDs, SPADs en de hun aanstuur- en uitleesschakelingen. De resultaten van het werk in dit proefschrift zijn veelbelovend en zorgen ervoor dat de droom van monolitische optocouplers nu dichterbij realisatie is dan ooit tevoren.
NOTE TO THE READER

The chapters numbered two to six are based on peer-reviewed publications. They have been ordered in this thesis in a way that suits the flow for the general reader; from device physics to design, optimization, and finally to the application. This order does not necessarily match the chronological order in which they were published. In addition, because of being an independent publication, each of these chapters contains its own introduction. The reader is thus not bound to read all the chapters preceding the one he/she is particularly interested in. However, for a reader who is not properly acquainted with the topic, it is recommended to read the chapters in order.

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1.1 Introduction to optical communication

On the invention of the photophone [1], Alexander Graham Bell said that it is "the greatest invention [I ever] made, greater than the telephone". In the photophone, Bell and his colleagues transmitted sound on a beam of light over a distance of about 200 m. At present, the concept of data communication using light is ubiquitous, e.g. fiber-optic communication employed in telecommunication, the internet and television. The range of applications enabled using optical communication is a testimony to Bell’s beliefs about the photophone.

Dedicated research was conducted to enable the use of light for communication since the middle of the 20th century. As a result, optical communication have significantly evolved compared to the photophone. The arrival of semiconductor lasers [2–4], the discovery of low attenuation fibers [5] and continuous advances in the silicon (Si) technology [6–8] enabled a number of applications employing optical communication.

The progress in optical communication has been possible by the virtue of some interesting properties of light: it can propagate at an astounding speed, it is not “bothered” by external interferences, it is difficult to tap, it can be “guided” using fibers, and at some wavelengths it can be transmitted with very low losses through an optical fiber [9].

Light can also be used for data communication between circuits operating in two voltage domains, thus galvanically isolating the two domains from one another. This property has been utilized in so-called “optocouplers” (Fig. 1.1) [10]. At present, only discrete optocouplers are available which employ compound semiconductor devices (e.g. GaAs in IL4208 from Vishay semiconductors [11]) as optical transmitters; optical receivers can be implemented in Si complementary metal-oxide-semiconductor (CMOS) technologies. This hybrid integration makes the processing more complex.
and thereby more expensive. To enable the implementation of optocouplers in Si CMOS technologies has been the focus of the research in this thesis.

At present, optocouplers have not been implemented in CMOS technologies. The main reason is the mismatch between the emission spectrum of a forward biased Si LED and the responsivity of an Si photodetector (PD). Forward biased Si LEDs emit light in a relatively narrow range in the infrared region (wavelength $900 \text{ nm} \leq \lambda \leq 1150 \text{ nm}$) with low efficiencies due to an indirect band gap [12]; Si PDs have a relatively low responsivity at those wavelengths. This results in a poor optocoupling efficiency ($\eta$) between a forward biased Si LED and an Si PD [13] (Fig. 1.2(a)).

An interesting property of Si diodes operating in avalanche breakdown is the visible light emission from these diodes [14]. During avalanche, charge carriers in the depletion region are accelerated due to a very high electric field ($10^5 - 10^6 \text{ V/cm}$) attaining high energies, higher than the band gap of Si (1.1 eV). Many of these accelerated carriers cause impact ionization to generate extra carriers which are further accelerated. Some of these high energy carriers recombine and because of these recombination events, high energy photons (with lower wavelengths) are emitted. A broad emission spectrum in the 400 - 850 nm range is obtained from these avalanching diodes [13–17]. At those wavelengths, there is a significant overlap with the responsivity of the Si PDs, as illustrated in Fig. 1.2(b). This overlap increases the $\eta$ between an Si avalanche mode LED (AMLED) and an Si PD compared to the $\eta$ between a forward biased Si LED and an Si PD (Fig. 1.2(a)) [13]. A high $\eta$ is essential for the implementation of optocouplers in CMOS technologies [13, 15–17].

The integration of optocouplers in standard CMOS technologies would be cost effective due to a relatively low cost of fabrication and low area requirements [16]. Due to monolithic integration, circuit parasitics can...
also be reduced and therefore the propagation delay can be lower, when compared to discrete implementations of optocouplers. Higher levels of system integration are also possible which are advantageous for many applications [16]. Many new “smart” chips are possible; the research on these “smart” chips is referred to as “Silicon CMOS photonics” [18].

Further, the isolation layer (Fig. 1.1) in a CMOS integrated optocoupler can be implemented via oxide layers such as trench isolation layers, which can sustain a relatively high breakdown field (∼ 800 V/μm) [19]. In comparison, the mold compounds used in traditional discrete optocouplers have a lower breakdown field (∼ 50 V/μm) [19]. A high breakdown field ($E_B$) results in a high breakdown voltage $V_B = -E_B \cdot W$, where $W$ is the width of the corresponding isolation layer. A high $V_B$ is important in many safety applications [20]. For intra-chip optical communication, the isolation can be implemented via traditional oxide layers of a CMOS technology, e.g. the Medium Trench Isolation. Back end oxides can be used as an insulating material for inter-chip optical communication.

Although visible light emission from AMLEDs has been known for about half a century (section 1.3), a commercial application for adopting AMLEDs is still missing. There are some possible reasons for this. Firstly, the efficiency of light emission, also referred to as the internal quantum efficiency (IQE) of Si AMLEDs is relatively low, in the order of $10^{-5}$ [15]. In other words, for every $10^5$ electrons flowing through the diode, on average 1 photon is emitted. Secondly, because of the low IQE of the AMLEDs, high quality ultra-sensitive PDs are required as the optical receivers in Si technologies; such PDs have been available only recently. A reason could be that avalanche has been avoided as an operating regime because of the usually associated high power consumption. Lastly, it is possible that researchers lost the track of this idea after light emission from compound semiconductors were discovered [2]. Compound semiconductors can emit...
light in the visible and near infrared wavelengths in forward mode at high efficiencies because of their direct bandgap [21].

The progress in the ultra-sensitive Si PD technology has made the dream of monolithic CMOS integration of optocoupling feasible now. Due to the low IQE of the AMLEDs, of special interest are the single photon avalanche diodes (SPADs) in CMOS technology [22]. In principle, SPADs are p-n junction diodes biased above the breakdown voltage ($V_{BR}$) of the diode. At those bias conditions, the electric field is very high and theoretically a single incident photon can trigger an avalanche event; the avalanche current then swiftly increases to macroscopic values which can be detected using simple readout circuits [23]. Suitable quenching-and-recharge circuits (QRCs) are employed to quench the avalanche once triggered and reset the SPAD for further photon detection [22].

The idea of using avalanching junctions for single photon detection has been present since the origin of Si technology [24–29], however the progress was limited by the defects in devices caused during fabrication [29]. These defects caused a high dark count rate (DCR) and therefore the devices could not be reliably used for single photon detection. The introduction of the epitaxial layer resulted in high quality junctions with low defect density [29]. SPADs with a timing accuracy of tens of picoseconds (unreported DCR) were demonstrated as early as 1988 [30]. Since then, significant progress has been made; high quality ultra-sensitive SPADs in standard CMOS technologies with DCR of few Hz are now commercially available and used in several applications [31–33]. The ultra-sensitivity of these SPADs can be used to mitigate the low IQE of the AMLEDs.

Fig. 1.3 represents the schematics of the proposed optocoupler in this thesis.

Figure 1.3: Overview of the proposed optocoupler in CMOS technology: optical transmitter (AMLED), optical receiver (SPAD), isolation barrier and the required circuits can be monolithically integrated.

1.2 Applications of optocoupling

An example of application of galvanic isolation in a medical system is shown in Fig. 1.4, where a barrier is required between the medical instruments and the patients. The isolator must protect the patients from
hazardous voltages and currents while the sensors gather essential patients data. Optocouplers as isolators are attractive for such applications.

In industrial environments, potential applications are gate driver circuits employed in motor drives and solar inverters (Fig. 1.5) [34]. In these circuits, the gate driver operating at \( V_{CC} \) must be isolated from the load side which operates at \( V_{DC} \) (tens or hundreds of volts). Another potential application is the monitoring of a high-voltage measurement system.

Integrated optocouplers are also attractive for the so-called “smart power” integrated circuit (IC) technologies such as Bipolar-CMOS-DMOS (BCD) [35, 36]. These smart power technologies combine high voltage transistors with low voltage digital circuits which enables monolithic integration of high power applications such as audio amplifiers and automotive systems. In many of these applications, communication between two voltage domains while maintaining a galvanic isolation is required; this isolated communication link is enabled by an isolator.

The important performance metrics of such an isolator are:

Figure 1.4: An illustrative example of a medical system including required galvanic isolation [20].

Figure 1.5: Schematic of an optically isolated gate driver for an insulated-gate bipolar transistor (IGBT). Similar driver circuit can be employed for a power MOSFET.
1. **Isolation performance**: The maximum continuous voltage difference between two voltage domains that an isolator can tolerate is denoted as the “working isolation voltage” ($V_{\text{ISO}}$), usually expressed in kV [37]. Isolation is also measured in terms of common mode transient immunity (CMTI), expressed in kV/μs [37]. A high-frequency, high amplitude transient at one voltage domain can couple to another voltage domain capacitively; this coupling can corrupt the data transmission across an isolator. CMTI is a measure of an isolator’s capability to “tolerate” such transients.

2. **Data rate**: For data communication applications, a high data rate with a low bit-error-rate (BER) is required [37].

3. **Energy-per-bit**: This is the measure of the power consumption of an isolator and an important figure-of-merit in data communication applications [38].

4. **Area**: The area occupied by an isolator in a system. A small area implementation results in a low cost for the system.

Currently, integrated isolators are also implemented using capacitors or inductors [39, 40]. Capacitive isolators require capacitors to be implemented in the back end and can be significantly large when isolating large voltage domains (e.g. 3 mm² [39]). Inductive isolators are also usually large, e.g. >30 mm² [40]. Integrated optocouplers can enable such galvanically isolated optical links at a lower area requirements (e.g. ∼0.05 mm² [41]). The performance details of the [39] and [40] are summarized in Table 1.1. Now, the evolution of applications using light emission from silicon is described.

### 1.3 Evolution of applications using light emission from silicon

There has been interest in exploring the use of visible light emission from Si avalanche diodes since the mid 1950s. In this section, the salient features of some of these works are highlighted. The focus is only on the crystalline Si junctions which are compatible with standard CMOS processes.

Visible light from Si p-n junctions operating in avalanche was first reported by Newman in 1955 [14]. He showed the relatively wide spectrum of the emitted light from avalanching Si junctions, when compared to the emission spectrum of a Si junction operating in forward mode. As stated in section 1.1, the wide spectrum was explained to be caused by the radiative recombination of high-energy carriers during avalanche breakdown.

Around the same time, the instability in the current-voltage characteristics during avalanche breakdown was reported [42]. At the onset of avalanche, current flows in the form of unstable pulses of random duration. An extensive research on this instability phenomenon was done over for
almost a decade [42–52]. It was shown that at crystal defects in p-n junctions, the field is enhanced and the breakdown starts to occur [49]. The breakdown at these local defects is not stable and this causes avalanche current to flow as unstable pulses of random durations. These localized defects were also shown to be the spots from where the light was emitted in the diodes [46]. The term “microplasma” for these defects is originating from their light emitting nature. The light emission from avalanche breakdown served as a great characterization tool in the 1960s for the characterization of defects causing breakdown [46, 47, 50, 51]. In recent years, this light emission has also been used in photo emission microscopy (PEM) for reliability analysis of Si-based circuits and devices to investigate the breakdown locations and the destruction mechanisms [53, 54].

In 1965, Haitz reported optical coupling between two different diodes fabricated on a single substrate [55]. The photons emitted from one of the “microplasmas” in one diode increased the pulse rate during instability in the other diode. Currently, this effect is well-known in the context of SPADs as “crosstalk” [56].

Probably the first consumer application idea for making use of light emission from avalanching junctions in Si was conceived by Kabell (1968) [57]. He used these light sources for the purpose of photographic data recording. The potential of avalanche diodes for VLSI interconnects was discussed by D. Kerns et al. in 1989 [58]. In an experiment [58], the light emitted by an AMLED was guided with an optical fiber and coupled to a nearby PD. The light from the AMLED was modulated at a few kHz and a corresponding output at the receiver was observed. The power consumption, data rate and any BER performance were not reported. However, the importance of AMLEDs in implementing cost effective on-chip interconnects was highlighted. It was argued that although the light output power is small, it could be sufficient for short interconnections. Although the system was not fully integrated, the experimental demonstration of optocoupling between two Si devices kindled the interest of many researchers.

In a partly experimental work in 1992 [17], Drieënhuizen et al. discussed the idea of using AMLEDs for optocoupling applications. Their interests were similar to those of other researchers, i.e. monolithic integration of optocouplers in standard technologies. In an experiment, Drieënhuizen et al. showed optocoupling between an AMLED and a PD on the same substrate. The authors argued that their design was limited by the poor optocoupling efficiency of the waveguide between the AMLED and the photodetector. Circuits for implementation of optocouplers in standard Si technology were proposed. However, no performance data on data communication or power consumption were reported and an experimental realization of the proposed system was not demonstrated.

At this point in the timeline, the extensive research done by Snyman et al. between 1996–present is important to consider [18, 59]. Their research has been focused on improving AMLED structures to emit at submicron
wavelengths with high efficiencies and to design CMOS compatible waveguides at those wavelengths. This research has resulted in efficient and reliable AMLEDs at submicron wavelengths. The use of dielectric layers in CMOS such as the field oxide, intermetallic oxide and silicon nitride as a waveguide has been demonstrated [59, 60]. Snyman et al. have also demonstrated a “proof of concept” of optical coupling in systems utilizing solely Si components [60]. In their optical link [60], the authors reported that signals of 60-100 nA could be observed at the PD when the AMLED was driven with 0-20 V pulses. In that work [60], the frequency of operation, BER, power consumption or isolation voltage have not been reported.

Another series of work done on Si AMLEDs is by Chatterjee et al. between 2002 - 2004 [61–63]. In a reliability study [61], the authors stressed AMLEDs with (a) a DC excitation, (b) an AC excitation and (c) a high temperature environment for accelerated aging. The measurements were done for 1 month at each stress condition. (a) The DC excitation at low values of current (less than 25 mA) caused a light coalescence phenomenon (merging of light-emitting regions), however the total light emission intensity remained constant. At high values of DC currents, not such coalescence in light emission was observed. The differences in the light emission at low and high values of DC currents were explained using a hydrogen migration model. (b) For the AC excitation, a 10 V peak-to-peak voltage signal at 10 MHz superposed on a 5 V DC was applied at the AMLED, for which the $V_{BR} \sim 6.5$ V. (c) For accelerated aging, the AMLEDs were placed in a 100°C environment. It was reported that the AC and temperature stressing also had negligible effects on the light emission from AMLEDs.

In [62], Chatterjee et al. demonstrated an inter-chip optical link using Si AMLEDs. The light emitted by an AMLED of a size of $75\mu m \times 75\mu m$ was coupled through a 36" long SiO$_2$ fiber cable of a 50 $\mu m$ diameter to an Si PD. Also, the light emission from the AMLED was modulated at 100 kHz using a sinusoidal input and a corresponding modulation in the output of the PD was observed. The authors stated that the modulation speed could not be improved because of the low bandwidth of the PD. The power consumption of the transceiver circuits were not reported there. In [63], the voltage across an AMLED ($V_{AMLED}$) was modulated at frequencies in the GHz range and the temporal behavior of the light emission was observed using a high speed streak camera (Hamamatsu C4780). In one measurement setting, the $V_{AMLED}$ was modulated using a sinusoidal signal of frequency 1 GHz and the output of the camera was recorded. In another setting, the AMLED was driven by a voltage pulse with a pulse width of 2 ns and an amplitude of 5 V. Parasitics in the circuit and the heavy impedance mismatch generated high-frequency oscillatory signals in the range of 20 GHz with a modulation index of $\sim 50\%$. From the output of the streak camera, the authors demonstrated the modulation speed of Si AMLEDs in the range of tens of GHz which is very attractive for applications such as optical interconnects.
In 2011, Huang et al. demonstrated an optocoupler in a standard 0.35 \( \mu \text{m} \) CMOS technology [41]. The active area of their design was about 0.05 \( \text{mm}^2 \) which highlights the low area implementation of integrated optocouplers. The salient feature is the monolithic integration of all components on a single substrate, while no technology modification or post-processing was done. Although no data communication was shown, that work highlights the successful integration of metallic waveguides and is yet another “proof of concept” for optical coupling in a standard CMOS technology.

In 2013, a monolithically integrated optical link utilizing both avalanche AMLEDs and SPADs was demonstrated for joystick applications where the operating frequency is about 100 Hz [64]. The area of the implemented chip in a 140 nm technology was about 2.5 \( \text{mm}^2 \) and the power consumption was reported to be \( \sim 5 \text{ mW} \). The attractive idea of that work is the successful demonstration of incorporating SPADs for compensating the low internal quantum efficiency of AMLEDs (Section 1.1).

Table 1.1 summarizes the state-of-the-art of integrated optocouplers and digital isolators. Note that for the majority of these works, only a limited set of performance metrics are reported or could be estimated.

### 1.4 Scientific Challenges

The integration of AMLEDs and SPADs with the standard CMOS circuits goes along with various challenges.

Avalanche operation of these devices requires high voltage electronics. While using AMLEDs, the optical transmitter can be power hungry because of the avalanche operation. Therefore, one big challenge is that the circuits should be power efficient and achieve a low energy-per-bit while ensuring a sufficiently low BER. The avalanche process is dependent on the process, voltage and temperature [65], therefore the AMLED driver circuit should be robust to these variations while providing a sufficiently high signal to the receiver (SPADs) (Fig. 1.3).

Since SPADs operate in the Geiger mode, the LED driver circuit should be able to completely switch the AMLED between on and off conditions for proper functionality of SPADs. Although small-signal modulation speed of AMLEDs in the range of tens of GHz has been reported [63], the large signal switching speed was found to be low, in the range of kHz to MHz depending on the AMLED design (chapter 4). In this respect, a proper design of AMLEDs for achieving high speed is required.

At the receiver side, the SPADs should be efficient in terms of photon detection efficiency and should have a low DCR for a low BER. A low DCR is possible by a proper choice of layers for the active area and the guard ring in the SPAD design [66]. Moreover, the operation of SPADs is also affected by their quenching-and-recharge circuits (QRCs). Currently, these QRCs are designed using a few rules-of-thumb. More research on the physics of SPADs was required to understand and improve the operation
### 1.4. SCIENTIFIC CHALLENGES

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Table 1.1: Summary of state-of-the-art of integrated isolators.

# AMLED electrical/optical power, IQE, coupling efficiency and modulation characteristics are relevant for the integrated optocouplers energy-per-bit and speed (section 1.2, chapters 4,5).

* AMLED and PD were not monolithically integrated, ** AMLEDs and PDs were integrated. [64] used SPADs as PDs.

+ [39] implemented a capacitive isolator whereas ++ [40] reports an inductive isolator. NA implies “Not Applicable”.


of SPADs. A proper design of QRCs also improves the dynamic range of SPADs and the SPADs can then be used for higher speed.

In other aspects of the same project (Optocoupling-in-CMOS), there were challenges related to the device physics and modeling of AMLEDs. For instance, the breakdown voltage of AMLEDs should be minimized without deteriorating IQE to reduce energy-per-bit. These issues have been addressed in [67]. It has been demonstrated that the breakdown voltage preferably should be about 6 V for maximum efficiency in terms of AMLED optical output power in response to input electrical power.

On top of everything, the major design constraint for the work in this thesis was that no modification to the CMOS process was allowed. In other words, all devices and circuits were required to be compatible with a specific CMOS technology.
1.5 Optocoupling-in-CMOS project results summary

This research work has been accomplished within the framework of the Optocoupling-in-CMOS (OiC) project, sponsored by the Dutch scientific foundation (NWO-TTW). The aim of OiC was to enable technologies for the monolithic integration of optocouplers in standard CMOS technologies. This interdisciplinary research was carried out by two PhD students with the expertise from two groups: the Integrated Circuit Design (ICD) group focusing on the circuit design aspects of the project and the Integrated Devices and Systems (IDS) group focusing on the device physics, both in the department of Electrical Engineering at the University of Twente.

A number of results were obtained during the course of this interdisciplinary project. The main findings of the OiC project are briefly listed:

- An optoelectronic model for the light emission from Si AMLEDs was developed [67]. Using a physics-based model and experimental results, it was shown that a $V_{BR}$ of about $\sim 6$ V is the optimum for maximum efficiency from the AMLEDs in any technology. Below this optimum point, the efficiency of an AMLED decreases because of non-local avalanche phenomena. This result is important because this puts a constraint to the reduction of $V_{BR}$ of AMLEDs.

- A new type of AMLED, referred to as the superjunction AMLED was designed in a standard 140 nm SOI CMOS technology [67]. For breakdown voltages of $\sim 25$ V and higher, this superjunction AMLED shows a two fold increase in the IQE.

- Random Telegraph Signal (RTS) phenomena in the avalanche current were observed for diodes designed in a standard 140 nm SOI CMOS and a pure boron technology [68, 69]. It was shown that RTS phenomena can describe the steep $I-V$ characteristics in avalanche breakdown. It also provides a well defined value of the experimental breakdown voltage. The importance of the RTS analysis in the accurate design of QRCs for SPADs was discussed. The time domain analysis of the RTS phenomena can be used to estimate the dimensions of the defects causing various RTS phenomena in avalanche diodes.

- The large signal modulation speed of AMLEDs was studied for diodes designed in a pure boron technology and a standard 140 nm SOI CMOS technology [70]. It was shown that the AMLED design is important to improve their speed. The RTS phenomena help to understand the device properties related to the speed of AMLEDs.

- A monolithic optical link in a standard 140 nm SOI CMOS technology was demonstrated [13]. It was shown that the optocoupling efficiency of a link employing AMLEDs is higher than that of a link employing forward mode LEDs. The thermal effects were found to be significant at low data communication rates in the range up to a few tens of Hz.
The thermal effects can be minimized by the integration of on-chip heat sinks and/or by adopting high frequency switching.

- A low power LED driver circuit was demonstrated [71]. This charge based driver circuit was integrated and designed in a standard 140 nm SOI CMOS technology. It was shown that the designed driver circuit is robust to many variations in the properties of the AMLEDs.

- Finally, according to our best knowledge, for the first time, a complete optical link for data communication in a standard CMOS technology has been achieved using SPADs [72]. The active area of the designed system is $<0.01 \text{ mm}^2$, the data rate in the range of a few Mbps and the energy-per-bit of a few nJ/bit. The isolation is expected to be similar to capacitive isolators and transformers due to the use of similar technology layers for isolation.

### 1.6 Thesis outline

- **Chapter 2**: Random telegraph signal phenomena in avalanche diodes: Application to SPADs: This chapter, based on the work presented at European Solid - State Device Research Conference (ESSDERC 2016) [68] shows the Random Telegraph Signal (RTS) phenomena in avalanche diodes fabricated in a standard 140 nm SOI CMOS technology. Using a time domain analysis of these RTSs, the steep $I-V$ characteristics in avalanche is explained, an experimental definition of the breakdown voltage is provided and the application of the RTS analysis in the accurate design of QRCs for SPADs is discussed.

- **Chapter 3**: Random telegraph signal phenomena in ultra-shallow $p^+n$ diodes: In this chapter, published in IEEE Journal of Electron Devices Society [69], the RTS phenomena in ultra-shallow junctions, fabricated using a pure boron technology were described in detail and the geometry dependence of the RTS phenomena have been analysed. Using a time domain analysis of these RTSs, the dimensions of the defects causing RTSs have been estimated.

- **Chapter 4**: Data transmission capabilities of avalanche mode light-emitting diodes: In this chapter (published in IEEE Transactions of Electron Devices) [70], the data transmission capabilities of AMLEDs fabricated in both pure boron and a standard 140 nm SOI CMOS technology has been investigated and results are explained using physically measurable AMLED parameters. Recommendations for the design of high speed AMLEDs are discussed as well.

- **Chapter 5**: Low power wide spectrum monolithically integrated optical transmitter in SOI CMOS technology: This chapter, based on our publication in Optics Express [71], demonstrates a low power optical
transmitter in a 140 nm SOI CMOS technology. The demonstrated charge based LED driver circuit is robust to process, voltage, temperature and design variations. The driver circuit consumes a fixed energy-per-bit and emits a fixed number of photons per data bit.

- **Chapter 6: Optocoupling in CMOS:** In this chapter (presented at the IEEE Electron Devices Meeting 2018 [72]), monolithically integrated optocouplers in a standard 140 nm SOI CMOS technology are demonstrated. A low voltage AMLED with a breakdown voltage of ~ 5 V was designed to reduce power consumption of the link. The advantages of AMLEDs over forward biased Si LEDs are shown. The performance of the designed optical links was measured in terms of data rate and energy-per-bit.

- **Chapter 7: Conclusions and recommendations for future work:** In this chapter, the results of this thesis are summarized and recommendations for future work in this field are discussed.
CHAPTER 2

ANALYSIS OF RANDOM TELEGRAPH SIGNAL PHENOMENA IN AVALANCHE DIODES

Abstract

The current-voltage ($I − V$) characteristics of diodes close to the breakdown voltage is shown to be governed by Random Telegraph Signal (RTS) phenomena. A technology independent time domain analysis method is used to accurately characterize the bias dependent statistical properties of these RTS phenomena and these are shown to explain the steep $I − V$ dependency in avalanche. Further, the statistical analysis of these RTSs is used for determining the self-sustaining avalanche current or latching current that is an important parameter in designing quenching and recharge circuits (QRCs) for single-photon avalanche diodes (SPADs). Accurate design of QRCs can improve the performance of SPADs in terms of count rates and afterpulsing. Based on these results, circuit simulations have been performed to show the advantages of applying the proposed method of adopting RTS-dependent latching current in the design of QRCs.

A major part of this chapter was presented at the European Solid-State Device Research Conference 2016 [68]. Simulation results have been added in section 2.5.1 for completeness sake.
2.1 Introduction

The triggering phenomenon of avalanche in diodes has been described in [25, 73, 74] (and references therein). Although most applications treat avalanche as the limiting region for using diodes, some applications explicitly make use of the avalanche region as the operating region. Major applications include optical detectors using avalanche photodiodes (APDs) [9] and single photon avalanche diodes (SPADs) [75]. Moreover, during avalanche, silicon (Si) diodes emit light at visible wavelengths, which is attractive for monolithic integration of optical links in CMOS technologies because of strong overlap of their emission spectrum with the responsivity of standard Si detectors [14, 16, 41, 76].

A relatively high electric field in the depletion region of p-n junctions is used in APDs and SPADs. As the electric field is very high, an incoming free carrier, e.g. generated by a photon or otherwise, can trigger an avalanche event. The avalanche current then increases rapidly to large values [12, 75]. APDs are typically operated in weak avalanche region, while SPADs are utilized in Geiger mode at a reverse bias ($V_R$) beyond the breakdown voltage ($V_{BR}$). A high $V_R$ in SPADs increases their photon detection efficiency and then they can be used to detect very faint optical signals. SPADs require a quenching and recharge circuit (QRC) to be able to quench the avalanche once triggered and reset themselves for subsequent photon detection [22]. A low build up time of avalanche along with a high sensitivity results in SPAD sensors with excellent time resolution, which can be integrated with simple read out circuitry [22, 23, 77, 78]. SPADs have been used in many applications such as time-of-flight imaging and single-photon-emission computed tomography [23, 66, 79]. In this chapter, the focus is on the statistical behavior of the avalanche process and applications to SPADs.

In literature, for the quenching of avalanche, it is reported that the avalanche is self sustaining for diode currents ($I_R$) higher than 100 $\mu$A, also sometimes denoted as the latching current ($I_{lat}$) [22, 52, 66, 80]. Below this $I_{lat}$, it is reported that there is a high probability of quenching of avalanche [66]. In passive QRCs, as a rule-of-thumb, for determining the quenching resistance ($R_Q$), typically 50 k$\Omega$ per volt of excess bias $V_{EX} = V_R - V_{BR}$ is used [22]. However, experimentally $V_{BR}$ is not well defined and consequently $V_{EX}$ is ill defined; therefore unique definitions for voltages that limit the steep $I - V$ part in avalanche are introduced in this chapter. It is shown that these generally accepted rules-of-thumb to estimate e.g. $R_Q$ can result in an overestimation for high counting rate applications such as optical links and optical interconnects [38].

This chapter is outlined as follows. An experimental setup is described in section 2.2 that enables us to achieve very low external quenching, limited by the 50 $\Omega$ input resistance of the measurement setup. This setup enables measuring currents with 160 nA current resolution and 100
ps time resolution which is sufficient to accurately measure and model avalanche Random Telegraph Signal (RTS) processes. Analyses show that \( I_R \) near breakdown can be characterized as an RTS. A time domain method to analyze the RTSs is described in section 2.3. Section 2.4 presents the experimental results, the analysis of the RTSs and discusses the underlying statistics. These analyses allow to extract bias dependent statistical RTS properties such as expected values for the RTS amplitude and RTS On-time fraction as a function of \( V_R \). Combined, these are shown to fully describe the steep \( I-V \) dependency in avalanche. Using the results, a parametric self-sustaining avalanche current level \( (I_{lat,p}) \) is defined in section 2.5 which enables accurate design of e.g. active or passive quench-and-recharge circuits. The advantages of the proposed method are also shown using circuit simulations. Finally, in section 2.6, the main findings of this chapter are summarized.

2.2 Experimental Setup

Fig. 2.1(a) shows a schematic cross-section of the diode in an industrial 140 nm SOI CMOS technology [36]. The SOI technology offers isolated voltage domains which is important for the implementation of optocouplers (chapter 1). A micrograph of the diode is shown in Fig. 2.1(b). A schematic layout of the experimental setup used to characterize diodes is also shown in Fig. 2.1(c). The avalanche multiplication region of the diode is beneath the \( p^+ \) region. In this setup, there are only non optical sources of free carriers to trigger avalanche in the diode: either from thermal generation, diffusion or defects in the multiplication region. However, optical sources (photons) can also trigger avalanche. Once triggered, the avalanche contribution \( I_A \) in the total diode current \( I_R \) flows until (actively, passively or self) quenched and only after that the diode reverts to its original non-avalanching state.

In this chapter, the main focus is on the characterization and modeling of self-sustaining properties of \( I_A \). For that reason, the total \( R_Q \) was minimized, here to only 50 \( \Omega \) of the measurement setup. This was accomplished by low-ohmically biasing the cathode using a bias tee and by shunting the anode by the 50 \( \Omega \) input resistance of a high performance oscilloscope (Agilent DSO54854A). At low RTS current magnitude levels an additional high-bandwidth low-noise amplifier (LNA) was used in front of the oscilloscope input; also this amplifier has 50 \( \Omega \) input resistance. A high data acquisition rate of 5 GS/s ensures that very narrow pulses could also be detected. This setup allows measuring currents with 160 nA resolution with a noise floor of 0.4 nA/\( \sqrt{\text{Hz}} \). Measurements were done in a Faraday’s cage in complete dark conditions at a temperature of 298 K with wafer probing. The data were acquired for a total duration of 1 ms at each bias condition.

Fig. 2.2 shows the DC \( I-V \) characteristics as measured by a Source Measure unit (SMU) of a Keithley B2901A, using a 1 s integration time.
2.2. EXPERIMENTAL SETUP

Figure 2.1: (a) Schematic cross-section of a square shaped p$^+$n diode in a 140 nm SOI CMOS technology. (b) Micrograph of the designed diode. (c) Schematic layout of the experimental setup to measure the RTSs in avalanche diode.

In section 2.4 it is shown that the steep part of the DC $I-V$ curve is fully described by bias dependent statistical properties of the RTS underlying the avalanche process; also an exact determination of the experimental $V_{BR}$ is given in section 2.4. The measurements indicate that the avalanche process starts around 14.7 V with $I_R$ rising sharply between 14.8 V and 14.9 V; this chapter is focused on that voltage range.
2.3 Time Domain Analysis Procedure

An example of measured $I_R$ for various $V_R$ is shown in Fig. 2.3. It can be observed that $I_R$ comprises current pulses with more or less two levels and a random duration. Further, the amplitude and duration of these pulses increase with $V_R$. These current pulses were consistently observed among all samples and a similar increasing trend in amplitude and duration was recorded.

This type of RTS phenomena has been reported to be caused by the unstable microplasma behavior near $V_{BR}$ [45, 49, 52, 74]. These microplasmas are formed at crystal imperfections where the electric field is increased above its average value [80]. However, a thorough time domain analysis (TDA) of the statistical parameters and their relation to the DC $I - V$ characteristics was not shown there.

Fig. 2.3 also shows that the avalanche process is not self-sustaining and switches between the on-state (“ON”) and the off-state (“OFF”). This ON-OFF behavior can be described by RTS phenomena and can be characterized by a few parameters:

1. the expected on-state lifetime, $E(T_{ON})$,
2. the expected off-state lifetime, $E(T_{OFF})$, and
3. the amplitude difference between the states, $A$.

The power spectral density (PSD) of a two level RTS has a Lorentzian shape [81, 82]. Earlier, some of the RTS parameters were approximated.
2.4 BIAS DEPENDENT RTS PARAMETERS

Figure 2.3: An example of the $I_R$ at $V_R$: (a) 14.86 V (b) 14.88 V (c) 14.90 V. $I_R$ comprises pulses with almost a fixed amplitude and random duration. The pulse amplitude and duration increase for higher $V_R$.

from the PSD [45]. In this work, the TDA is utilized to extract the RTS properties more easily.

The time domain current $I_R$ is displayed as a histogram [83, 84]. Fig. 2.4 shows the histogram of $I_R$ at different $V_R$. The amplitude and the amount of time in the on-state increases with $V_R$. Following parameters of the RTS can be estimated using a Gaussian fit (Fig. 2.4(b)): the mean values of the OFF-level ($b_0$) and the ON-level ($b_1$), the standard deviation of the OFF-level ($\sigma_0$) and the ON-level ($\sigma_1$). The RTS amplitude $A$ is estimated as $A = b_1 - b_0$. In our measurement setup, $\sigma_0$ is mainly caused by the oscilloscope, while $\sigma_1$ also includes the multiplication noise [85].

To extract the time domain statistical parameters, $I_R$ is quantized into a two-level RTS using a simple level-crossing algorithm, similar to algorithms in e.g. data recovery in digital communication channels [84, 86]. The measured $I_R$ is quantized into $I_{Q,RTS}$ as:

$$I_{Q,RTS} = \begin{cases} A, & \text{if } I_R \geq I_{TH} \\ 0, & \text{otherwise.} \end{cases}$$

(2.1)

2.4 Bias dependent RTS parameters

Using the procedure described in section 2.3, statistical properties of the RTSs were obtained for various $V_R$. In the context of avalanche processes
and SPADs, the inter-arrival times (IATs) between the RTS pulses, the pulse width of RTS pulses and the RTS amplitude are the most relevant. Also the standard deviation of these give information, but is dealt with in chapter 3.

2.4.1 Inter-arrival times

From $I_{Q,RTS}$, IATs for the ON and OFF state pulses were calculated. An example of a measured probability density function (PDF) of the inter-arrival time for $V_R=14.88\,\text{V}$ is shown in Fig. 2.5; similar PDFs were obtained for other bias conditions in avalanche. These PDFs show that the IATs for the ON-state and OFF-state states are exponentially distributed which confirms that the observed RTS process is similar to a Poisson distribution [87]. The peak in the PDF at the far left hand side (indicated by an arrow) is because of the “afterpulsing” [23]. The conventionally used IAT for a certain state equals the lifetime for the other state.
2.4. BIAS DEPENDENT RTS PARAMETERS

Figure 2.5: The PDF of the inter-arrival times for the ON and OFF states at 14.88 V; the PDFs have different x-axis scales. The arrows indicate where “afterpulsing” plays a role.

2.4.2 Expected Lifetimes

Using PDFs as shown in Fig. 2.5, the expected lifetime in each state can now be estimated:

\[
E(T_{\text{ON}}) = \sum_{T_{\text{ONi}}} T_{\text{ONi}} \cdot P(T_{\text{ON}} = T_{\text{ONi}}) \tag{2.2}
\]

\[
E(T_{\text{OFF}}) = \sum_{T_{\text{OFFi}}} T_{\text{OFFi}} \cdot P(T_{\text{OFF}} = T_{\text{OFFi}}) \tag{2.3}
\]

where \(i\) is the summation index for various values of \(T_{\text{ON}}\) and \(T_{\text{OFF}}\). Fig. 2.6 shows these obtained \(E(T_{\text{ON}})\) and \(E(T_{\text{OFF}})\). Note that \(E(T_{\text{ON}}) + E(T_{\text{OFF}})\) is the expected pulse IAT which is the reciprocal of the expected RTS pulse repetition rate.

2.4.3 ON-time fraction and Amplitude

Using the derived \(E(T_{\text{ON}})\) and \(E(T_{\text{OFF}})\), we define the expected ON-time fraction \(E(D) = E(T_{\text{ON}})/(E(T_{\text{ON}}) + E(T_{\text{OFF}}))\). In other words, \(E(D)\) is the fraction of any observation time window where the RTS is in the ON-state. The RTS amplitude \(A\) has been defined in section 2.3. Fig. 2.7 shows both \(E(D)\) and \(A\) as a function of \(V_R\) where \(E(D)\) is shown on a logarithmic scale.
2.4.4 DC $I - V$ characteristics

The DC $I - V$ characteristics has been shown in Fig. 2.2. In Fig. 2.3, it was shown that the $I_R$ shows RTS behavior in the steep part of the $I - V$ characteristics. The statistical properties of the RTSs were analyzed.

Figure 2.6: Expected lifetimes (in ON and OFF states) and their sum as a function of $V_R$. For $V_R > 14.92$ V, the avalanche is self-sustaining.

Figure 2.7: Expected ON-time fraction $E(D)$ and amplitude $A$ as a function of $V_R$; $E(D)$ is on a semi-log scale because of its large dynamic range. The dashed lines explain the design of QRCs in sec. 2.5.
2.5 Applications

2.5.1 Application to SPADs

A possible application of the RTS analysis could be in the design of passively quenched SPAD QRCs. In those circuits, the quenching time ($\tau_Q$)
is usually much shorter than the recharge time ($\tau_R$) [88]. The $\tau_R = C_P R_Q$ limits the count rate in SPADs, where $C_P$ is the total capacitance (parasitic capacitance plus the intrinsic capacitance of the SPAD) at the quenching terminal of the SPAD and $R_Q$ is the external quenching resistance. An ill-defined value of $R_Q$ can result in a lower maximum count rate from SPADs. In addition, a higher $R_Q$ results in a larger area and a larger $C_P$. The latter results in a larger amount of charge per avalanche event ($Q_{AVAL}$) and hence in higher afterpulsing [88].

$R_Q$ can be accurately calculated for SPADs in any technology using the statistical properties of the RTSs. For example, in this 140 nm technology, for an 5% $E(D)$ (or 95% probability of quenching), at these quench conditions, $V_R = 14.87$ V and $A \approx 60$ $\mu$A (Fig. 2.7). If the final operating voltage ($V_f$) in a passive QRC is set to 14.87 V, $R_Q$ can be calculated using [22]:

$$R_Q = \frac{V_R - V_f}{60 \mu A}$$

(2.4)

Therefore, $R_Q$ should be $\approx 16$ k$\Omega$ per extra volt of $V_R - V_f$ (assuming $R_Q >>$ resistance of the diode). $V_R - V_f$ can be interpreted as the ill-defined $V_{EX}$ in the literature [22, 66, 88]. The traditional rule-of-thumb would suggest to use an $R_Q = 50$ k$\Omega$ [22], while using the conventional DC $I - V$ characteristics, $R_Q \geq 100$ k$\Omega$ per volt of $V_R - V_f$. By optimizing the $R_Q$, the count rate can be increased by a factor of 3 to 6 compared to traditional approaches. A parametric self-sustaining avalanche current ($I_{lat,p}$) can be defined with $E(D)$ (or probability of quenching) as the main parameter.

To explicitly show the advantage of the proposed method in reducing $Q_{AVAL}$ in integrated QRCs, a circuit simulation was done in a 140 nm SOI CMOS technology [36] to estimate $Q_{AVAL}$. The simulation setup is shown in Fig. 2.9 (inset). A verilog-A simulation model for the SPAD was used [89]. The turnoff probability was taken from experimental data (Fig. 2.7) instead of using the rule-of-thumb of $I_{lat} = 100$ $\mu$A [22]. The $Q_{AVAL} = \int_0^{T_{AVAL}} I_{AVAL}(t) dt$, where $I_{AVAL}(t)$ is the current during the avalanche event and $T_{AVAL}$ is the duration of the avalanche event. Fig. 2.9 shows that at higher $V_R - V_f$, $Q_{AVAL}$ can be significantly reduced using the proposed method for estimating $R_Q$ because of the lower $C_P$. A minimal $Q_{AVAL}$ reduces the undesired afterpulsing phenomena in addition to reducing the energy consumption.

### 2.5.2 Application to Avalanche Mode LEDs

As mentioned earlier, Si avalanche mode LEDs (AMLEDs) are attractive for monolithic integration of optical links in CMOS technologies [14]-[76].

For data communication using large signal modulation schemes (e.g. On-Off Keying) with AMLEDs [71], the randomness of the avalanche current should be considered. It is then important to have AMLEDs with a relatively high number of defects to reduce jitter and the bit-error-rate in
2.6 Conclusions

RTS phenomena in diode currents were shown to fully determine the steep current-voltage ($I - V$) dependency in avalanche. Using a time domain analysis, we determined the statistical properties of these RTS phenomena. The statistical properties were shown to fully describe the steep part of the $I - V$ characteristics. An accurate experimental definition of breakdown voltage ($V_{BR}$) was proposed based on the RTS analysis. Using the RTS analysis, the value of the self-sustaining avalanche current in diodes was parametrically determined, that can be used in an accurate design of quenching and recharge circuits (QRCs) for SPADs. It is expected that the proposed approach can significantly increase count rates while reducing afterpulsing. The advantages of the accurate design of QRCs were discussed with circuit simulations.
CHAPTER 3

RTS PHENOMENA IN ULTRA-SHALLOW SILICON AVALANCHE DIODES

Abstract

An extensive time domain analysis of the Random Telegraph Signal (RTS) phenomena in ultra-shallow silicon avalanche diodes is presented. Experiments show two distinct types of RTSs classified herein, on the basis of the temporal behavior of the amplitude, as the “decaying” and the “constant” type. These RTSs are analyzed using a model for defects reported earlier, from which their ohmic series resistance and geometrical parameters have been estimated. The results indicate that breakdown of a relatively small area defect results in a “decaying” amplitude type of RTS, and breakdown of a relatively large area defect results in a “constant” amplitude type of RTS. These two types can be explained by the differences in the thermal resistance, which is higher for the former.

This chapter is based on a manuscript published in IEEE Journal of Electron Devices Society[69].
3.1 Introduction

Deterministic and statistical carrier multiplication theories have been reported in literature to describe the triggering of avalanche in silicon (Si) diodes in [25, 73, 74, 90] (and in references therein). In applications like optical detectors based on avalanche photodiodes (APDs) [9] or single-photon avalanche diodes (SPADs) [75], the avalanche phenomenon is utilized to detect weak optical signals. Moreover, during avalanche, Si diodes emit light at visible wavelengths, which is attractive for monolithic integration of optical links in CMOS technologies because of significant overlap of their emission spectrum with the responsivity of standard Si photodetectors [14, 16, 41, 76].

Random Telegraph Signal (RTS) phenomena in the avalanche current at the onset of breakdown were reported earlier [42, 43]. Initially, the RTS phenomena were referred to as the “microplasma instability” because during breakdown, it was shown that these unstable localized defects emitted visible light [46]. Many interesting theories were reported to provide a phenomenological description of these current fluctuations [45, 48, 49, 52]. It was established that these fluctuations arise from crystal defects such as dislocations in the diodes [49]. The concept of RTS phenomena to model these fluctuations was discussed in [45]. Recently, the modeling has been revisited [74] and an elaborate overview of the evolution of this topic has also been presented in the same paper [74].

In chapter 2, it is discussed that the avalanche process and its current-voltage ($I - V$) characteristics can be described by RTS phenomena. Using the RTS analysis results, the $I - V$ characteristics could be modeled. The impact of the RTS analysis on the accurate design of quenching and recharge circuits for SPADs is also discussed in chapter 2. That work can be used to increase the count rates and to decrease the afterpulsing in SPADs. Further, the non-monotonic behavior of the noise spectral density in reverse biased diodes is caused by RTS phenomena [91]. As discussed in [74, 92], studying RTS phenomena is also useful for determining the material quality and for reliability analyses of e.g. power devices, because RTS phenomena are caused by crystal defects.

Time domain analysis (TDA) of the RTS phenomena can be used to estimate the properties of defects causing these RTSs. The TDA methods and the experimental setup to characterize RTSs were developed after 1960s. Possibly because of experimental limitations, an extensive TDA of RTS phenomena in avalanche diodes is missing in the literature. The purpose of this chapter is to address this issue. The main findings of this chapter are:

- From the temporal behavior of the RTS amplitude, it is shown that two types of defects with different local thermal impedances exist in diodes. Defects with a high thermal impedance cause a “decaying”
type of amplitude in RTSs and defects with a low thermal impedance cause a “constant” type of amplitude in RTSs.

- From the TDA, it is shown that the bumpy behavior in the current-voltage characteristics is caused by the two types of defects and not necessarily by the relatively high thermal impedance of the diode packaging as reported earlier [49].

- An existing model for these defects is improved to take into account an explicit thermal model. Using the model, both the ohmic series resistance and the dimensions of these defects are estimated.

The chapter is outlined as follows: the experimental diodes and measurement setup to measure the RTS phenomena are described in section 3.2. The analysis method for the two types of RTSs is discussed in section 3.3. In section 3.4, the geometry dependency of various RTS parameters is shown. A model for the defects causing these RTS phenomena is used for estimating some of the electrical and geometrical parameters of the defects in section 3.5. Finally, in section 3.6 the main findings of this chapter are summarized.

3.2 Experimental setup

3.2.1 Experimental diodes

In this chapter, the aim is to study RTS phenomena in the avalanche processes in devices used for optical generation and detection, requiring semiconductor-only junctions. Therefore, for the experiments, diodes were...
3.2. EXPERIMENTAL SETUP

Figure 3.2: Measured reverse I-V characteristics of all diodes at 25 °C.

designed in a pure boron (PureB) technology [93]. In this technology, a thin layer of PureB is deposited by chemical vapor deposition on a clean n-Si surface. P-type Si is obtained from this thin PureB layer and high quality ultra-shallow p⁺-n, hence abrupt asymmetric, junctions are obtained [94]. These ultra-shallow junctions are suitable for various optical detection or emission applications [93–95]. Four circular diodes, on the same die, were selected with diameters of 3 μm, 6 μm, 12 μm and 20 μm; these diodes are labeled as J3, J6, J12 and J20 respectively, where the diode name indicates its diameter. A TCAD simulated electric field of a representative device at breakdown showing a uniform lateral field in the depletion region is shown in Fig. 3.1(b). Due to the circular geometry, the lateral electric field in the depletion region should be uniform at breakdown. However, the electric field could be distorted at crystal defects, forming the preferred location of breakdown [49].

Fig. 3.2 shows DC I – V characteristics of all diodes at 25 °C in dark conditions measured by an Agilent B2901A Source/Measure Unit (SMU) using 1 s integration time. In these diodes, the avalanche starts at around 13.7 V; the reverse current (I_R) rises sharply between the reverse voltage (V_R) of 13.7 and 13.9 V. This chapter focuses on this voltage range.
3.2.2 Measurement setup and analysis method

The measurement setup and the TDA method have been described extensively in chapter 2. The relevant details are briefly summarized here.

Fig. 3.3(a) shows the experimental setup to measure the RTS phenomena. The cathode is biased at a constant voltage and the RTSs were measured across a 50 Ω resistance, thus providing a low impedance load and a low quenching. A low noise SIM 911 preamplifier was used to drive the oscilloscope input to improve the signal-to-noise ratio at the oscilloscope input. The transient data were acquired for a total duration of 1 s at each reverse bias voltage for all diodes, at a data acquisition rate of 100 MS/s. The measurements were done in a Faraday cage in dark condition using wafer probing.

Fig. 3.3(b) shows an example of the measured $I_R$ in the steep part of the $I - V$ characteristics, showing the RTS phenomena in $I_R$ in these diodes. This $I_R$ can be represented in a histogram as shown in Fig. 3.3(c). A sum of two Gaussians, $N(b_0, \sigma_0^2)$ and $N(b_1, \sigma_1^2)$, is fitted on this histogram and from the fit parameters, the mean value of the off-state ($b_0$) and the mean value of the on-state ($b_1$) are estimated. The RTS amplitude ($A$) is obtained from these fit parameters. Also, from the mean on-time ($E(T_{ON})$) and mean off-time ($E(T_{OFF})$), the mean on-time fraction ($E(D)$) can be estimated, the detailed procedure has been described in chapter 2. The mathematical equations for the estimation of relevant parameters are summarized in Eq. (3.1):

$$A = b_1 - b_0, \quad (3.1a)$$

$$E(D) = \frac{E(T_{ON})}{E(T_{ON}) + E(T_{OFF})}. \quad (3.1b)$$

$E(D)$ represents the fraction of time, in an observed time window, where the RTS is in the on-state (Fig. 3.3(b)). Note that two types of RTSs are ob-
3.3 RTS characterization

As mentioned above, two types of RTSs have been observed in these diodes. For example, for J3, at a relatively low $V_R$ (at the onset of breakdown), the RTSs were decaying in amplitude during RTS events as shown in Fig. 3.4(a). These are denoted as the “decaying” amplitude type of RTSs. At a higher $V_R$ (slightly above the breakdown voltage), another type of RTS with a constant amplitude, denoted as the “constant” amplitude type was also observed, as shown in Fig. 3.4(b). To our best knowledge, a mixture of “decaying” and “constant” types of RTSs at a given bias in a single diode has not been reported before.

A possible explanation for the two types of the RTSs was provided in [49]. It was reported that the “decaying” RTSs will be observed in diodes which do not have a good thermal contact with the outside world. For such diodes, the energy dissipated during the avalanche process would increase the temperature of the diode. This decreases the effective impact ionization coefficient ($\alpha$) and consequently decreases the avalanche current [80]. Interestingly, it was assumed that the local thermal impedance of a defect inside the diode is negligible [49]. The above explanation implies that a mixture of defects with different local thermal impedances in a single device is not possible, contrary to the observations. Assuming thermal effects as the root cause for the different RTSs would imply that there are at least two different defects in the same diode: one with a relatively low and the other with a relatively high thermal impedance with the outside world.

Figure 3.4: (a) A “decaying” type of RTS observed at a lower $V_R$. (b) Mixture of a “constant” and a “decaying” RTS at a higher $V_R$ in the same diode. The x-axis scales are different for clarity.
3.3.1 RTS classification

The measured transient data were analyzed in Matlab. From the transient data acquired for 1 s, all individual RTSs were identified and many RTS properties were extracted. These properties are the peak current ($I_{\text{peak}}$), pulse width ($PW$), the decay time constant for the “decaying” RTSs ($\tau$, see section 3.5) and the shape of the RTS.

To distinguish between the “decaying” and the “constant” types of RTS, the decay in the RTS amplitude during each pulse was estimated. Firstly, the $PW$ of each RTS was extracted from its rising and falling edges. For estimating the time constant ($\tau$), the RTS current $I_R(t)$ was fitted onto an exponentially decaying transient current model (Fig. 3.5):

$$I_R(t) = I_0 \cdot e^{-t/\tau}, \quad (3.2)$$

where $\tau$ is a fitting parameter. Based on the ratio of $PW/\tau$, the decay in the RTS amplitude was calculated. For a decay of less than 1% during the $PW$, the shape of the RTS was labeled as a “constant” amplitude type.

$$\text{RTS type} = \begin{cases} \text{constant amplitude,} & \text{if } \frac{PW}{\tau} \leq 0.01 \\ \text{decaying amplitude} & \text{otherwise.} \end{cases} \quad (3.3)$$

Fig. 3.5 shows an example of a measured and its fitted pulse for both types of RTSs.

The peak current ($I_{\text{peak}}$, see Fig. 3.5) of each RTS was also separately extracted. The $I_{\text{peak}}$ is used in section 3.5 to estimate the parameters of the defects.

Figure 3.5: Classifying the RTS: (a) A “decaying” RTS : $PW = 31.47\mu s$, $\tau = 183.9\mu s$, and a decay of $\sim 16\%$. (b) A “constant” RTS : $PW = 4.09 \times 10^3\mu s$, $\tau = 3.3 \times 10^6\mu s$. The peak current ($I_{\text{peak}}$) was separately extracted.
3.3. RTS CHARACTERIZATION

Figure 3.6: Fraction of “constant” amplitude RTSs ($F$) as a function of $V_R$.

3.3.2 Fraction of “constant” RTSs

Fig. 3.6 shows the fraction of “constant” type of RTSs ($F(V_R)$) as a function of $V_R$ for all diodes:

$$F(V_R) = \frac{N_{constant}(V_R)}{N_{constant}(V_R) + N_{decaying}(V_R)}.$$  \hspace{1cm} (3.4)

where $N_{constant}(V_R)$ and $N_{decaying}(V_R)$ are the number of “constant” and “decaying” types of RTSs per unit time respectively, observed at each $V_R$. From the $F - V$ curve, the $V_R$ at which the “constant” type of RTSs start appearing can be estimated, i.e. $F > 0$. At a few bias points, $0 < F < 1$, both types of RTSs were observed.

Fig. 3.6 shows that the “constant” type of RTSs appear for higher $V_R$ than the “decaying” type of RTSs. As discussed in section 3.4, the $V_R$ at which $F$ starts to sharply increase is the $V_R$ at which a bump in the RTS parameters, $A$ and $E(D)$, were observed.

3.3.3 Mean Pulse width

Fig. 3.7 shows the mean pulse width ($PW_{\text{MEAN}}$) of the “constant” and the “decaying” RTSs as a function of $V_R$. The mean values were obtained from all observed RTSs for each type at each bias condition. The “decaying” RTSs have a lower $PW_{\text{MEAN}}$ compared to the “constant” RTSs. This can be explained by the local heating providing a negative feedback for the
avalanche process by reducing the excess bias across the diode (section 3.5), thereby reducing $\alpha$. The avalanche at such a “hotspot” quenches relatively quickly because of a decreasing $\alpha$ [80]. Further, the $PW_{\text{MEAN}}$ increases with $V_R$ because of the increasing electric field, which helps to sustain the avalanche for a longer time. The “constant” amplitude RTSs have much larger $PW_{\text{MEAN}}$, possibly due to the absence of significant thermal effects. For “constant” RTSs, the accuracy of the estimated $PW_{\text{MEAN}}$ reduces for higher $V_R$ because of fewer events in 1 s measurement time. However, the measured accuracy is sufficient for the purpose of modeling and estimation of the defect parameters.

3.4 Bias dependent RTS parameters

The statistical properties of the RTSs, namely $A$ and $E(D)$ were obtained as a function of $V_R$ for all diodes. The arrival times between the RTS pulses, also referred to as inter-arrival times have been shown to be exponentially distributed [49, 68, 96], which confirms that the observed RTS process has a Poisson distribution.

3.4.1 RTS Amplitude

Fig. 3.8 shows that $A$ increases with $V_R$. The bump in the $A - V$ characteristics at the indicated $V_R$ (Fig. 3.8) is also important to note. Such a bump was believed to be caused by the relatively high thermal impedance of the diode with the outside world [49]. It was thought that if the diode was not in a good thermal contact with its heat sink, such $A - V$ characteris-
3.5. ESTIMATION OF MODEL PARAMETERS

In this section, the electrical and geometrical parameters of the defects causing the RTS phenomena are estimated, based on the model from [49]. First, the model and its relevant characteristics are discussed. Then, the electrical parameters of the defects are estimated using TDA results. From

Figure 3.8: RTS amplitude \( (A) \) as a function of \( V_R \). At the indicated \( V_R \), “constant” amplitude RTSs are triggered (Fig. 3.6).

tics would be obtained. However, as shown in section 3.3, at and above these \( V_R \), defects causing the “constant” amplitude RTSs are triggered; the bumpy behavior is a result of those RTSs.

3.4.2 Mean On-time fraction

Fig. 3.9 shows \( E(D) \) (Eq. (3.1b)) as a function of \( V_R \). The \( E(D) \) sharply increases as a function of \( V_R \) close to breakdown. As explained in chapter 2, the steep \( I - V \) dependency in avalanche is mainly because of this steep dependency of \( E(D) \) on \( V_R \). The bump at a higher \( V_R \) is due to the onset of the “constant” amplitude type of RTSs at higher \( V_R \) which have higher \( PW_{\text{MEAN}} \), as discussed in section 3.3. As the size increases (from J3 towards J20), the \( V_R \) range over which the RTS phenomena are observed (the range in which \( 0 < E(D) < 1 \)) decreases. This can be attributed to an increasing number of crystal defects with increasing size of the diode [97].

The \( A - V \) and \( E(D) - V \) characteristics are used to estimate a few properties of defects in section 3.5.

3.5 Estimation of model parameters

In this section, the electrical and geometrical parameters of the defects causing the RTS phenomena are estimated, based on the model from [49]. First, the model and its relevant characteristics are discussed. Then, the electrical parameters of the defects are estimated using TDA results. From
CHAPTER 3. RTS PHENOMENA IN ULTRA-SHALLOW SILICON AVALANCHE DIODES

Figure 3.9: Mean ON-time fraction ($E(D)$) as a function of $V_R$. At and above the indicated $V_R$, $E(D) = 1$; the diodes were in a continuously On-state above these $V_R$.

the electrical parameters, geometrical details of the defects causing the two types of RTSs are estimated.

Fig. 3.10(a) shows the schematic cross-section of a defect causing these RTS phenomena. For simplicity, a defect is modeled as a cylinder of height $w$ and diameter $d$ and avalanche is assumed to be confined in this cylinder [49]. As reported earlier, $w$ is the depletion layer width [49, 74].

Fig. 3.10(b) shows the circuit model of the defect. The bistable switch models the on-off switching characteristics of the defect causing the RTS phenomenon. The charge fluctuations in the defect region control this switch [74]. The $V_a(t)$ in the model corresponds to the voltage at which the multiplication factor ($M$) is unity [49]. Once the avalanche is triggered, the free carriers partially neutralize the space charge and thereby reduce the electric field. Then, the applied $V_R$ should be increased to keep $I_R$ constant; the resistance produced by this partial neutralization of space charge because of high injection in the defect region is denoted as the space charge resistance ($R_S$) (section 3.5.3) [12, 48, 49, 80, 98].

Once the avalanche is triggered, the temperature of the defect region $T(t)$ starts to increase because of the energy dissipated during the avalanche process. This rise in temperature is modeled by a thermal model incorporating a thermal resistance and thermal capacitance, $R_{TH}$ and $C_{TH}$ respectively [99], as shown in Fig. 3.10(c).
3.5. ESTIMATION OF MODEL PARAMETERS

Figure 3.10: (a) Schematic cross-section of a cylindrical defect of height $w$ and diameter $d$ inside a p$^+$-n junction. (b) Circuit model for the defect comprising of avalanche sustaining voltage $V_a$, series resistance $R_S$, bistable switch and diode capacitance $C_d$. (c) A thermal model to estimate the temperature of the defect region.

With Fig. 3.10(b), the RTS current $I_R(t)$ is:

$$I_R(t) = \frac{V_R - V_a(t)}{R_S}. \quad (3.5)$$

An elevated $T(t)$ results in an increased $V_a(t)$ [49], thereby providing a negative feedback for the avalanche process. In these devices, the doping $N_D \sim 10^{17}$ [cm$^{-3}$], and for such $N_D$, the $V_a$ can be approximated with a linear dependence on $T(t)$ [100, 101]:

$$V_a(t) = V_{a0} + \beta \cdot (T(t) - T_0), \quad (3.6)$$

where $\beta$ (∼ 5 mV/K) is the thermal coefficient for the breakdown voltage [101], $T_0$ is the initial temperature at $t = 0$ and $V_{a0}$ is $V_a$ at $T = T_0$. The $T(t)$ due to self heating is:

$$T(t) = T_f - (T_f - T_0) \cdot e^{-t/\tau}, \quad (3.7a)$$

$$T_f \approx T_0 + \frac{R_{TH}V_R(V_R + \beta T_0 - V_{a0})}{R_S + \beta R_{TH}V_R}, \quad (3.7b)$$

$$\tau = \frac{R_{TH}R_SC_{TH}}{R_S + \beta R_{TH}V_R}. \quad (3.7c)$$

The physical variables in the above equations are:

- $T_f$ is the temperature in thermal equilibrium at $t \to \infty$ assuming that the avalanche is not quenched.
- $R_{TH}$ ([K/W]) is the thermal resistance of the avalanche region.
- $C_{TH}$ ([Ws/K]) is the thermal capacitance of the avalanche region.
An expression for $R_{TH} = (T_f - T_0)/(V_R I_R)$ has been derived in [49]:

$$R_{TH} = \frac{1}{\pi k d (\frac{w}{d})^2} \left( \frac{w}{4d} \sqrt{1 + \left(\frac{w}{d}\right)^2} + \frac{1}{8} \ln \left( \frac{\sqrt{1 + \left(\frac{w}{d}\right)^2} + \frac{w}{d}}{\sqrt{1 + \left(\frac{w}{d}\right)^2} - \frac{w}{d}} \right) \right). \quad (3.8)$$

where $k (~ 2 \text{ W/cm-K})$ is the thermal conductivity of Si [12]. In these devices, for typical values of $w$ and $d$ (section 3.5.3), $\beta R_{TH} V_R$ is much smaller than $R_S$, therefore $\tau \approx R_{TH} C_{TH}$ (Eq. (3.7c)).

Using Eqs. (3.5)–(3.7), $I_R(t)$ can be written as:

$$I_R(t) = \frac{V_R - V_{a0}}{R_S} - \beta \cdot \frac{(T_f - T_0)}{R_S} \cdot (1 - e^{-t/\tau}). \quad (3.9)$$

The peak current ($I_{peak}$) of this $I_R(t)$ is given by:

$$I_{peak} = I_R(t = 0) = \frac{V_R - V_{a0}}{R_S} = \frac{V_{EX}}{R_S}. \quad (3.10)$$

It is important to note that $I_{peak}$ depends only on the initial excess bias ($V_{EX} = V_R - V_{a0}$) and $R_S$. From the slope of the measured $I_{peak} - V_{EX}$ characteristics, the $R_S$ can be estimated:

$$\frac{1}{R_S} = \frac{dI_{peak}}{dV_R} = \frac{dI_{peak}}{dV_{EX}}. \quad (3.11)$$

Further, from the estimated $R_S$, the effective $d$ of the defects are estimated in section 3.5.3.

Eq. (3.9) indicates that $I_R(t)$ decays with a time constant $\tau \approx R_{TH} C_{TH}$. The decay time constant of $I_R(t)$ has been estimated in section 3.3; the results are shown here for completeness of the model.

In this section, two lumped defects in one diode are modeled: one causing the “decaying” amplitude RTSs and the other causing the “constant” amplitude RTSs. Multiple defects with similar breakdown voltage and resistance cannot be distinguished using TDA.

### 3.5.1 Estimating $V_{a0}$

As mentioned before, $V_{a0}$ is defined as the voltage at which $M = 1$ at $T = T_0$ [49]. However, estimating this voltage is by no means straightforward. In chapter 2, an alternative definition of the experimental breakdown voltage ($V_{BR}$) was proposed; $V_{BR}$ being the voltage at which $M = 2$. This is estimated from the SMU measured $I - V$ characteristics and the RTS weighted $E(D) \cdot A - V$ characteristics. The term $E(D) \cdot A$ represents the average current of the RTS current pulses. The current during the on-state of the RTSs is due to impact ionization.

Both the SMU measured $I - V$ and the RTS weighted $E(D) \cdot A - V$ curves were extrapolated (dashed lines in Fig. 3.11(a)). The intersection of these extrapolated curves is the voltage at which the RTS weighted current...
3.5. ESTIMATION OF MODEL PARAMETERS

Figure 3.11: Estimation of $V_{a0}$ for J6. (a) The intersection of SMU $I - V$ and RTS weighted $E(D) \cdot A - V$ characteristics is the voltage at which $M = 2$. From the intersection of two types of curves, the $V_{a0}$ for the defects causing “decaying” ($V_{a,\text{decaying}}$) and “constant” amplitude RTSs ($V_{a,\text{constant}}$) are estimated. (b) Summary of $V_{a,\text{decaying}}$ and $V_{a,\text{constant}}$ for all diodes.

<table>
<thead>
<tr>
<th>Diode</th>
<th>$V_{a,\text{decaying}}$ (V)</th>
<th>$V_{a,\text{constant}}$ (V)</th>
</tr>
</thead>
<tbody>
<tr>
<td>J3</td>
<td>13.81</td>
<td>13.87</td>
</tr>
<tr>
<td>J6</td>
<td>13.75</td>
<td>13.81</td>
</tr>
<tr>
<td>J12</td>
<td>13.73</td>
<td>13.80</td>
</tr>
<tr>
<td>J20</td>
<td>13.72</td>
<td>13.76</td>
</tr>
</tbody>
</table>

The “constant” amplitude RTSs have a higher $V_{a,\text{constant}}$. These voltages are used in estimating $V_{EX}$ to calculate $R_S$.

There are two different types of defects in these diodes causing two types of RTSs and the $V_{a0}$ is different for both defects. Fig. 3.11(a) shows the example procedure for estimating $V_{a0}$ for both types of defects in J6, i.e. $V_{a,\text{decaying}}$ and $V_{a,\text{constant}}$; a similar procedure was carried out for other diodes and the results are summarized in Fig. 3.11(b). The “constant” amplitude RTSs have a higher $V_{a,\text{constant}}$. These voltages are used in estimating $V_{EX}$ to calculate $R_S$. contribution equals the leakage current component, hence $M = 2$. This voltage is denoted as $V_{M=2}$ and will be used as approximation for $V_{a0}$. In reality, $V_{a0}$ would be somewhat lower than $V_{M=2}$. 
Figure 3.12: Mean peak current ($I_{\text{peak}}$) of various diodes as a function of $V_{\text{EX}}$: (a) for the “decaying” amplitude RTSs. (b) for the “constant” amplitude RTSs. The y-axis scales are different for clarity.

### 3.5.2 Peak current

Fig. 3.12 shows the mean value of measured $I_{\text{peak}}$ (Fig. 3.5) as a function of $V_{\text{EX}} (= V_R - V_{a0})$ of all diodes and for both types of RTSs. The mean values were estimated using all observed RTSs of each type at all bias conditions. It is observed that $I_{\text{peak}}$ increases with $V_{\text{EX}}$.

The offset in $I_{\text{peak}}$ around $V_{\text{EX}} = 0$ is most likely because the actual $V_{a0}$ is lower than $V_{M=2}$. In the measurement duration of 1 s (section 3.2), no RTS phenomenon was observed at $V_R < V_{a0}$.

The RTSs are caused by localized defects inside the devices [45, 49, 74]. The observed increase in $I_{\text{peak}}$ for larger diodes is most likely due to the spreading of avalanche to secondary defects, once a single defect is triggered. As the size of the diode increases, the number of defects causing RTS phenomena increases (see also appendix B). This could cause the avalanche triggered at a defect site to trigger secondary defects by two mechanisms [102]: 1) drift and diffusion of free carriers in lateral direction and 2) emission of secondary photons. As reported in [102], this spreading of avalanche occurs in a few tens of picoseconds. Fig. 3.13(a) illustrates this process.

Hence, the measured $I_{\text{peak}}$ could originate from breakdown at multiple defects (Fig. 3.13(b)):

$$I_{\text{peak}} = \sum_{i=1}^{n} \frac{V_R - V_{a,i}}{R_{S,i}}$$

(3.12)

where $V_{a,i}$ and $R_{S,i}$ are the breakdown voltage and the series resistance of the $i^{th}$ defect.

### 3.5.3 Series resistance and geometrical details of the defects

As stated before, the resistance due to the partial neutralization of space charge in the avalanche region is referred to as the space-charge resistance
3.5. ESTIMATION OF MODEL PARAMETERS

Figure 3.13: a) Illustration of spreading of avalanche where multiple defects in a single diode are involved. First defect 1 is triggered, then defect 2 is triggered by secondary photons emitted by defect 1 and defect 3 by lateral transport of free carriers. b) Equivalent circuit model for modeling the spreading effect for three defects.

(RS). Using Eq. (3.11) and Fig. 3.12, RS was estimated for the two types of defects for all diodes. An example of the procedure for estimating RS is shown in Fig. 3.14(a). The estimated RS of defects in all diodes are shown in Fig. 3.14(b). The results indicate that the defects causing “decaying” amplitude RTSs have a higher RS than the defects causing “constant” amplitude RTSs. The relatively low RS for larger diodes can also be explained by a simultaneous breakdown of multiple defects (Fig. 3.13):

\[
\frac{1}{R_S} = \sum_{i=1}^{n} \frac{1}{R_{S,i}}. \tag{3.13}
\]

Alternatively, each component in RS can be described by the dimensions of a single defect and the properties of the diode (impurity concentration). For a defect inside a single sided abrupt p⁺−n junction, its RS,i is given by [12]:

\[
R_{S,i} = \frac{2(w - x_A)^2}{\pi \epsilon_s v_s d_i^2} \tag{3.14}
\]

where w is the depletion region width, x_A is the avalanche region width where most of the multiplication takes place [12], \(\epsilon_s = 1.04 \times 10^{-12} \text{ F/cm}^2\) is the permittivity of Si, \(v_s = 1 \times 10^7 \text{ cm/s}\) is the saturation velocity of electrons and holes and \(d_i\) is the diameter of the \(i^{th}\) defect. For simplicity, \(v_s\) is assumed the same for electrons and holes [12, 74]. For single-sided abrupt junctions: \(x_A \sim 0.3w\) [74].

From the estimated RS, the value of the effective diameter \(d\) is estimated and results are tabulated in Fig. 3.14(c) for both types of defects for all devices. According to Eqs. (3.13) and (3.14), \(d\) is an effective value possibly
CHAPTER 3. RTS PHENOMENA IN ULTRA-SHALLOW SILICON Avalanche DIODES

Figure 3.14: a) Example procedure for estimating $R_S = \frac{dV_{EX}}{dl_{peak}}$ for “decaying” amplitude RTSs in J6. b) Estimated $R_S$ of the defects causing “decaying” and “constant” amplitude RTSs. c) Estimated diameter $d$ for both types of defects in all diodes. The $d$ for defect causing “constant” amplitude RTSs in J12 is larger than that in J20. This is also observed from optical measurements (appendix C).

obtained from the breakdown of many defects:

$$d = \sqrt{\sum_{i=1}^{n} d_i^2},$$  \hspace{1cm} (3.15)

where $d_i$ is the diameter of the $i^{th}$ defect. The first observation from Fig. 3.14(c) is that the defects causing “decaying” amplitude RTSs have a relatively smaller $d$ than the defects causing “constant” amplitude ones. The second observation is the increasing effective size of the defects with the size of the device as discussed and explained by the spreading of avalanche to multiple defects in large diodes. The estimated $d$ is in the same order of magnitude as the reported values in the literature obtained from optical measurements [49, 52, 74]. Also, the optical measurements on these devices confirmed this (appendix C).

3.5.4 Thermal time constant

Fig. 3.15 shows the estimated thermal time constant ($\tau \approx R_{TH}C_{TH}$) of the “decaying” amplitude RTSs (section 3.3). The figure shows that $\tau$ increases with increasing $V_{EX,\text{decaying}}$.

It was observed using the light emission properties of the defects that the $d$ remains almost constant over the instability region [49]. The $w$ increases with increasing $V_{EX,\text{decaying}}$ and therefore the $R_{TH}$ should increase with increasing $w/d$ (Eq. 3.8), also predicted in [49]. Also, $C_{TH}$ increases
3.6 Conclusions

An extensive time domain analysis of RTS phenomena in the diode current in avalanche was presented. Using the analysis results, the dependency of RTS parameters (amplitude and mean on-time fraction) on the size of the diode was shown. The amplitude of the RTSs was shown to increase with the area of the diode which was explained by the spreading of avalanche to more defects in larger diodes. The steep DC measured $I - V$ dependency close to breakdown was shown to be due to the steep dependency of on-time fraction of RTSs on bias voltage. Two different types of RTSs were observed in these diodes: a “decaying” and a “constant” amplitude type and a comprehensive analysis of both RTSs was done. It was proposed that the “decaying” amplitude RTSs are caused by defects having relatively high thermal impedance. A model for the defects causing these RTS phenomena from the literature was adopted, and some of the defect parameters were estimated using the time domain analysis. The ohmic series resistances proportionally with the volume of the defect [103]. This complies with the observed increasing behavior of $\tau$ with $V_{EX,decaying}$. At a given $V_{EX}$, $\tau$ decreases for larger diodes because of larger $d$ (section 3.5.3).
of these defects were estimated to be hundreds of ohms to a few kΩ. The
diameter of the defects was estimated to be in the order of a few μm. Further, it was argued that the “decaying” amplitude RTSs are caused
by relatively small area defects and the “constant” amplitude RTSs by
relatively large area defects. The demonstrated analysis procedure for
estimating a defect’s series resistance and its dimensions can be readily
used in other devices as well.
CHAPTER 4

DATA TRANSMISSION CAPABILITIES OF SILICON AVALANCHE MODE LEDS

Abstract

The data transmission capabilities of silicon avalanche mode light-emitting diodes (AMLEDs) were investigated and the results are correlated to the multiplication noise and leakage current. The incoming data were modulated using pulse position modulation and the bit-error-rate (BER) and jitter in the transmitted data were measured. The results indicate an intrinsically low speed in terms of BER and jitter. From various size AMLEDs, temperature variations and optical excitations, it is shown that the speed can be improved by using AMLEDs with a (1) relatively high multiplication noise, (2) relatively high leakage current and (3) higher charge-per-bit. Design recommendations for the high speed AMLEDs are discussed.

This chapter is based on a manuscript published in IEEE Transactions of Electron Devices [70].
4.1 Introduction

The significant overlap of the emission spectrum of silicon (Si) avalanche mode light-emitting diodes (AMLEDs) with the responsivity of Si photodetectors (PDs) makes them attractive for monolithic integration of optocouplers in standard CMOS [16, 17, 104, 105]. In [13], we have demonstrated optocoupling between Si AMLEDs and PDs in standard CMOS and analyzed the effects of the link length on the optocoupling efficiency. For digital data communication, the output of an AMLED needs to be modulated in response to the incoming data. The maximum data rate in such a system is typically limited by how fast the LED can be turned on and off [9, 106].

The small signal modulation speed of AMLEDs has been reported in tens of GHz [63, 107], which is determined by the transit time $\tau_t \sim w_d/v_{sat}$, where $w_d$ is the depletion region width and $v_{sat} \sim 10^7$ cm/s is the saturation velocity of electrons and holes; $\tau_t$ is typically a few tens of ps. However, the large signal switching speed depends on the availability of free carriers in the multiplication region and triggering of a new avalanche event. Without any experimental proof, it was claimed that this speed is also in tens of GHz [108]. We show that triggering of a new avalanche event can be a relatively slow process. If an avalanche would not be triggered during transfer of a data bit, it leads to a bit-error in the communication link. In addition, the delay variation in turning on of avalanche leads to jitter in the transmitted data. Obviously, a high bit-error-rate (BER) and jitter are undesirable. In this work, it is shown that:

- AMLEDs with more defects have more multiplication noise and achieve higher speed.
- AMLEDs with a relatively high leakage current have a high number of free carriers to trigger an avalanche, which improves speed.
- Higher speed can also be obtained by driving AMLEDs at a higher bias, which increases the avalanche triggering probability, at the cost of increased power consumption.

Based on these results, design recommendations for high speed AMLEDs are discussed. The presented results are important for the implementation of low power optical links using AMLEDs [13, 41, 104, 105].

4.2 Experimental AMLEDs and noise

For our experiments, the AMLEDs were fabricated in a pure boron (PureB) technology, a schematic cross-section of which is shown in Fig. 4.1. The anode was formed by chemical vapor deposition of PureB in windows to the n-Si surface through an oxide layer. The n-Si substrate had a nominal doping concentration of $10^{15}$ cm$^{-3}$ and an n-enhancement layer was formed.
Figure 4.1: Schematic cross-section of a PureB AMLED (not to scale, all dimensions in μm). The oxide is ～ 0.3 μm thick. Diameter (d) of an AMLED is defined by the dimensions of the n-enhancement layer.

under the anode by implanting phosphorus to achieve a local doping concentration of 10^{17} \text{ cm}^{-3}. A detailed description of the processing is given in [94] where similar PureB diodes were fabricated with the addition of a buried n^{+} layer. Four circular diodes, on the same die were selected with diameters (d) of 8 μm, 15 μm, 20 μm and 30 μm; we label these diodes as J8, J15, J20 and J30 respectively.

Fig. 4.2 shows the DC I-V characteristics of the AMLEDs measured using a Keysight B2901A Source/Measure Unit (SMU) and 1 second integration time. The breakdown voltage (V_{BR}) of the AMLEDs, defined as the reverse voltage (V_{R}) at which the reverse current (I_{R}) starts to sharply increase, is ～ 13.7 V. An example of the light-emission from such an AMLED is also shown in the inset.

We have shown that in avalanche diodes, I_{R} exhibits Random Telegraph Signal (RTS) phenomena in the steep part of the I-V characteristics (chapter 2) [68]. Initially close to V_{BR}, the RTSs are short in duration. As the V_{R} increases, the RTSs become longer and their amplitude increases [69]. Ultimately, the RTSs disappear and the diode is in a continuously on-state. In the on-state, there is also multiplication noise (σ_{M}) present in the avalanche current [69, 85].

The RTS phenomena were measured as done previously (Fig. 4.3) (chapter 3) [69]. We define σ_{M} as the difference between the noise in the on-state of the device (σ_{1}) and the measurement setup noise (σ_{0}): σ_{M} = \sqrt{σ_{1}^2 - σ_{0}^2} (Fig. 4.3c) [85].

Fig. 4.4 shows an example of the measured transient I_{R} for all AMLEDs in a continuous on-state. Fig. 4.5 shows the estimated σ_{M} for all AMLEDs as a function of their diameter (d). A higher σ_{M} can be observed for larger AMLEDs which can be explained by the higher amount of defects in a larger device [69]; the noise generated by each defect is added up [97]. The linear dependency of σ_{M} on d indicates that the number of defects increases linearly with the area of the diode.
4.2. EXPERIMENTAL AMLEDS AND NOISE

Figure 4.2: Measured reverse $I$-$V$ characteristics of PureB AMLEDs. Inset shows light emission from J8, biased at a reverse current of 1 mA, captured using a Nikon D3100 camera with an integration time of 30 seconds.

Figure 4.3: (a) Random Telegraph Signal (RTS) measurement setup. (b) Measured RTSs in $I_R$. (c) Histogram of the measured RTSs. $\sigma_0$ is the noise from the experimental setup whereas $\sigma_1$ also includes multiplication noise ($\sigma_M$).
Figure 4.4: Example of measured transient avalanche currents when the AMLEDs are in on-state; these data include the measurement setup noise ($\sigma_0$).

4.3 Pulse position modulation speed

4.3.1 AMLED driver circuit

Light is emitted from an AMLED only during avalanche [14]. For low power receivers in digital optical links using e.g. single-photon avalanche diodes, the LED should be switched between completely on and off conditions. Therefore, the voltage across an AMLED ($V_{\text{AMLED}}$) should be modulated between below and above $V_{\text{BR}}$ by driver circuit.

Fig. 4.6 shows the schematics of the driver circuit along with illustrative transient waveforms that we used to modulate $V_{\text{AMLED}}$. The circuit can also limit the charge-per-bit ($Q_b$) through the AMLED. Limiting $Q_b$ limits the energy-per-bit ($E_b$), which is an important figure-of-merit in optical links.

We briefly describe the functionality of this circuit. For further details of this circuit, we refer the reader to chapter 5 [71], in which we have demonstrated an integrated implementation of this driver circuit. Initially the AMLED is biased below $V_{\text{BR}}$ and then $V_{\text{AMLED}} = V_{\text{LOW}} = V_{\text{BIAS}} - V_{\text{DD}}$. First the reset switch M1 is opened using the control signal RST. Then the control signal IN is set high and $V_{\text{AMLED}}$ initially increases to:

$$V_{\text{AMLED}} = V_{\text{BIAS}} - V_{\text{DD}} \cdot \left( \frac{C_p}{C_p + C_Q} \right),$$

(4.1)
where the total parasitic capacitance $C_P = C_{PAR} + C_{AMLED}$, with $C_{PAR}$ the circuit parasitic capacitance and $C_{AMLED}$ the AMLED capacitance. The initial excess bias ($V_{EX,i} = V_{AMLED} - V_{BR}$) is:

$$V_{EX,i} = V_{BIAS} - V_{DD} \cdot \left( \frac{C_P}{C_P + C_Q} \right) - V_{BR}. \quad (4.2)$$

The initial $I_{AMLED} \approx V_{EX,i}/R_{AMLED}$ where $R_{AMLED}$ is the resistance of the AMLED, and $I_{AMLED}$ charges $C_Q$, thereby reducing $V_{AMLED}$ and $I_{AMLED}$. As $V_{AMLED}$ reduces to $V_{BR}$, the avalanche is quenched. The maximum $Q_b$ equals $C_QV_{EX,i}$:

$$Q_b = \int_{0}^{T_{ON}} I_{AMLED}(t) \, dt \leq C_Q V_{EX,i} \leq C_Q V_{DD}, \quad (4.3)$$

where $T_{ON}$ is the pulse width of the control signal IN.

Due to the relatively low internal quantum efficiency of AMLEDs ($10^{-5}$) [71], high speed optical measurements are not possible using conventional photodetectors. Here, we measure $I_{AMLED}$ as an indication of the light emission. The relation between $I_{AMLED}$ and the light emission from AMLEDs is discussed in section 4.3.7.

In this work, the circuit of Fig. 4.6(a) was designed on a printed circuit board (PCB) which, in contrast to the CMOS integrated implementation of [71], resulted in $C_P \sim 20$ pF.
4.3.2 Pulse position modulation and bit error rate

AMLEDs have a relatively low efficiency and therefore for fast and low power receivers, they should be integrated with highly sensitive photodetectors such as single-photon avalanche diodes (SPADs) [109]. SPADs are p–n junctions biased above breakdown where an incoming photon can trigger an avalanche. The current then swiftly rises to macroscopic levels which can be detected using readout circuits [88].

For data communication applications using SPADs, pulse position modulation (PPM) is a modulation scheme of choice [109]. Fig. 4.7 illustrates the transient waveforms of the control signal IN of our driver circuit for a two level PPM. In the transmitter, if an avalanche is not triggered during a data bit, there is no light emission and consequently a bit error occurs in the transmitted data. Therefore, the BER is defined as the fraction of the number of data bits where no avalanche was detected, as illustrated in Fig. 4.7 (Note that BER > 0.5 is possible according to this definition).

We varied $V_{LOW}$ by adjusting $V_{BIAS}$ and $V_{DD}$ in the driver circuit. As a result, the leakage current of the AMLED in the off-state is tuned. $V_{BIAS}$ and $V_{DD}$ were also adjusted to achieve different $V_{EX,i}$ for each $V_{LOW}$ (Eq. (4.2)). We will show in section 4.3.4 that as $V_{LOW}$ or $V_{EX,i}$ increases, the BER decreases.

Furthermore, to study the effect of varying $Q_b$, the $C_Q$ was varied between three settings (Fig. 4.6(a)): 470 pF, 800 pF and $C_Q \to \infty$ (implemented as a short yielding no quenching). During an avalanche event,
4.3. PULSE POSITION MODULATION SPEED

Figure 4.7: (Left) Illustrative transient waveforms of the control signal IN for bit “0” in (a,b) and for bit “1” in (c,d) when data are modulated using a two-level PPM; $T_{\text{bit}} = 1/f_s$ is the bit duration time. (Right) Illustrative example of BER: For bit “0”, avalanche is triggered in (a) and therefore there is no bit error in the transmitted data whereas avalanche is not triggered in (b) causing a bit error. Similarly for bit “1”, there is no bit error in (c) whereas (d) causes a bit error. Hence, (b) and (d) contribute to BER.

some of the carriers flowing through the depletion region of the AMLED can get trapped. These trapped carriers are randomly released, causing free carriers that may trigger another avalanche event. This phenomenon is well-known as “afterpulsing” in SPADs [88], and is an undesired phenomenon in SPADs limiting their count rate [88]. However as shown in section 4.3.4, the mechanism yielding afterpulsing in SPADs helps to reduce the BER in AMLEDs.

To test the performance of the AMLEDs, a Pseudo Random Bit Sequence (PRBS) with a length of $2^{10}$ bits was generated. The control signals (IN and RST in Fig. 4.6(b)) for PRBS data were generated using a Keysight 33200A dual channel arbitrary waveform generator at a data rate ($f_s$) of 100 kbps.

Fig. 4.8 summarizes all biasing settings used in our measurements. The transient data at the oscilloscope were acquired at 2 GS/s (0.5 ns resolution). The performance of the AMLEDs was measured in terms of jitter and BER. The jitter is defined as the time delay between the time at which $V_{\text{AMLED}} > V_{\text{BR}}$ and the turn-on of avalanche, see Fig. 4.6(b). In our measurements, due to the used PRBS, the BER can be reliably measured down to $10^{-3}$ and therefore is lower limited to $10^{-3}$.
Figure 4.8: Bias settings of the driver circuit. For all three $C_Q$ settings, by tuning $V_{DD}$ and $V_{BIAS}$, different values of $V_{LOW}$ and $V_{EX,i}$ are obtained.

### 4.3.3 Functionality

Fig. 4.9 demonstrates an example of the measured $I_{AMLED}$ for J8 and J30; J8 shows a larger BER ($\sim 0.98$) than J30 ($\sim 0.2$) because of a higher multiplication noise in the latter (see section 4.3.4).

An ideal probability density function (PDF) of jitter for a two level PPM would be two dirac delta functions at the time instants of the rising edge of $V_{AMLED}$ (Fig. 4.6(b) and Fig. 4.7). However, in Fig. 4.9 large jitter in the turn-on time in both AMLEDs can be observed. This is because of a lack of free carriers and a defect count in these devices to trigger an avalanche. The PDFs of jitter are used in sections 4.3.5, 4.3.6 and 4.4.1 to demonstrate the effect of the leakage current and defects on the speed of AMLEDs.

### 4.3.4 BER

The probability of triggering of an avalanche during a pulse when $V_{AMLED} > V_{BR}$ depends on the number of free carriers in the depletion region ($N_d$) and the probability that a free carrier triggers avalanche ($P_a$). For a related analysis, a model of the triggering phenomenon was reported to explain the dark count rate of the SPADs [87]. In that model, the arrival of free carriers in the depletion region was modeled by a Poisson process. The probability that an avalanche is triggered ($P_d$) in a pulse of duration $T_{ON}$ is given by [87]:

$$ P_d = 1 - e^{-N_d P_a}, \quad (4.4a) $$

with $N_d = N_{d1} + N_{d2} + N_{t1} + N_{t2} + N_{opt}, \quad (4.4b)$

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<tr>
<th>$V_{DD}$ (V)</th>
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<th>$\sim V_{EX,i}$ (V)</th>
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and $P_a = 1 - e^{-\frac{V_{EX}}{\eta V_{BR}}}$. \hspace{1cm} (4.4c)

Further, $\text{BER} = 1 - P_d = e^{-N_d \cdot P_a}$. \hspace{1cm} (4.4d)

In the above equations, $N_{d1} = I_R T_{ON}/q_e$ is the number of carriers generated during the pulse where $I_R$ is the leakage current and $q_e$ is the elementary charge. Some of the carriers generated before the arrival of the pulse could remain in the depletion region due to the finite bandwidth of the AMLED; $N_{d2}$ is the number of those carriers. $N_{t1}$ is the number of released carriers from traps during the pulse (causing “afterpulsing” in SPADs). Similar to $N_{d2}$, $N_{t2}$ is the number of carriers that are released from traps before the arrival of the pulse. $N_{opt}$ is the number of optically generated carriers (section 4.3.6). Finally, $\eta$ is a fit parameter that depends on the defect density, temperature and device properties [87].

$N_d$ and $P_a$ are functions of the bias, temperature, device properties and driver circuit operating conditions. Exact solutions of these functions are difficult to obtain. However, the elegance of this model is its ability to explain the observed trends [87].

Fig. 4.10 shows the measured BER for all AMLEDs at different bias conditions (Fig. 4.8) in case of PPM. The BER

1. decreases with increasing $V_{LOW}$ at a given $V_{BIAS}$,
2. decreases with increasing $V_{BIAS}$ at a given $V_{LOW}$,
3. reduces for a higher $C_Q$, and
4. is lower for larger diodes.
The relatively high BER is due to a low defect density, therefore low $P_a$, in these AMLEDs. The defect density is low because of the circular geometry and the presence of the implicit guard ring (Fig. 4.2) [94].

The decreasing BER for a higher $V_{\text{LOW}}$ (at a fixed $V_{\text{BIAS}}$) is because of an increase of $N_{d2}$. A higher $V_{\text{BIAS}}$ (at a fixed $V_{\text{LOW}}$) causes a higher $N_{d1}$ and a higher $P_a$ (Eq. (4.4c)), consequently resulting in a lower BER.

A higher $C_Q$ causes a higher $Q_b$ (Eq. (4.3)) and therefore more carriers are trapped during an avalanche event. These trapped carriers can be released during the next bit, increasing the probability of another avalanche. This effect results in a lower BER for a higher $Q_b$.

As the size of an AMLED increases, the multiplication noise increases (Fig. 4.5) indicating an increasing number of defects for larger diodes. This results in a higher $P_a$ in Eq. 4.4(d) and therefore in a lower BER for larger devices.

When driving AMLEDs with $C_Q \to \infty$, the BER further reduces. However, a higher amount of energy is then dissipated in the AMLEDs which increases its temperature. An elevated temperature results in a higher $V_{\text{BR}}$; an example of this self-heating can be observed from the measured $I_{\text{AMLED}}(t)$ in Fig. 4.11. A higher $V_{\text{BR}}$ reduces $P_a$ (Eq. 4.4(c)) which tends to increase the BER (Eq. 4.4(d)). Therefore, BER cannot be reduced by
operating AMLEDs at a very high $Q_b$ because of this self-heating effect.

In conclusion, for a given AMLED, a reduced $Q_b$ causes a high BER which implies a constraint to reduce $Q_b$ for an optical link\(^1\). Also, the $Q_b$ cannot be arbitrarily increased because of increasing power consumption and self-heating.

### 4.3.5 Effect of temperature

The parameters $N_d$ and $P_a$ can be increased by operating the AMLEDs at elevated temperatures. The effect of temperature on the $I$-$V$ characteristics of a circular 12 μm diode (J12) on the same die is shown in Fig. 4.12(a).

Using the same driver circuit and PRBS data, BER and jitter were measured at three temperatures for J12. By adjusting $V_{\text{BIAS}}$ and $V_{\text{DD}}$, $V_{\text{LOW}}$ was varied between $V_{\text{BR}} - 0.1$ V, $V_{\text{BR}} - 1.1$ V and $V_{\text{BR}} - 2.1$ V at all temperatures (Fig. 4.8). Fig. 4.12(b) shows a lower BER at higher temperatures, which is due to a higher $N_d$ and $P_a$.

Fig. 4.13 shows an example of the jitter PDFs at three temperatures. Due to an increased $N_d$ and $P_a$, an avalanche is triggered faster at higher temperatures. Consequently, a lower standard deviation of the jitter PDFs can be observed at higher temperatures.

The quantum efficiency for high energy photons emitted from AMLEDs has a negative temperature coefficient [110], therefore a higher $Q_b$ would

\(^1\)We focused only on the transmitter, in general $Q_b$ is also lower limited because of the signal-to-noise ratio requirements at the receiver side.
Figure 4.12: (a) $I$-$V$ characteristics of J12 for three different temperatures. Different $V_{BR}$ at all temperatures are indicated. (b) Measured BER for diode J12 at $V_{BIAS} = V_{BR} + 5.9$ V, $C_Q = 470$ pF. $V_{L1} = V_{BR} - 2.1$ V, $V_{L2} = V_{BR} - 1.1$ V and $V_{L3} = V_{BR} - 0.1$ V are the $V_{LOW}$ at each temperature. The BER is shown on a logarithmic scale for clarity.

be required at higher temperatures to ensure the same number of photons at the receiver of an optical link.

4.3.6 Effect of external illumination

To demonstrate the effect of increasing $N_d$ without changing $P_a$, free carriers were generated using optical illumination. An increasing $N_d$ results in a lower BER (Eq. 4.4(d)).

An external LED with an emission spectrum around 650 nm was used to illuminate J12 at $T = 25^\circ$C. Fig. 4.14(a) shows the measured short circuit

Figure 4.13: Measured PDFs of jitter for bit “0” and bit “1” for J12 at three temperatures. At all temperatures, $V_{BIAS} = V_{BR} + 5.9$ V, $V_{L1} = V_{BR} - 2.1$ V and $C_Q = 470$ pF. Vertical scales are different for clarity.
4.3. PULSE POSITION MODULATION SPEED

The short circuit current ($I_{SC}$) of J12 as a function of the external LED current ($I_{opt}$). $I_{SC}$ is the measured current through the AMLED at $V_R = 0$. For higher $I_{opt}$, more carriers are generated optically and therefore $I_{SC}$ increases [12]. This implies a higher $N_{opt}$ and therefore a higher $N_d$ (Eq. 4.4(b)).

Fig. 4.14(b) shows the measured BER as a function of $I_{SC}$ when the driver circuit was operated at $V_{BIAS} = 17.6$ V, $V_{DD} = 6$ V and $C_Q = 470$ pF. For comparison, Eq. (4.4d) is also plotted with $N_d \approx I_{SC}T_{ON}/q_e$ and a fitting parameter $\eta = 7 \times 10^{12}$, showing good agreement with the measurements.

The corresponding jitter PDFs in Fig. 4.15 demonstrate a lower standard deviation for higher $I_{SC}$, also because of a higher $N_d$. The standard deviation of the jitter was estimated to be $\sim 10$ ns at $I_{SC} = 774.5$ pA.

![Figure 4.14: a) Measured short circuit current ($I_{SC}$) in response to current in the external illumination source ($I_{opt}$). (b) Measured and fitted BER as a function of $I_{SC}$. All axes are on a log scale.](image)

![Figure 4.15: Example of measured PDF of jitter for “0” and “1” at two $I_{SC}$ settings. The driver circuit was operated at the same bias settings as in Fig. 4.14(b). Y-axis scales are different for clarity.](image)
4.3.7 Impact on light output

For an optical link, the photon transmission to a nearby photodetector is important. We have already shown a linear relation between $Q_b$ and the emitted photon flux from an AMLED [13, 71]. Therefore, triggering of an avalanche during a data bit impacts the light emission from an AMLED for that bit [71, 111].

A high BER in the transmitted data implies that light would not be emitted from an AMLED during the data bit transfer. Assuming an ideal receiver, this would result in a high BER of the overall optical link, which is obviously undesired.

Furthermore, a high jitter in the turn-on time of an avalanche would cause a high jitter in the light output from an AMLED. This would limit the application of AMLEDs in optical links using time critical modulation schemes, such as PPM [109].

4.4 Design recommendations

From the results in section 4.3, we can conclude that jitter and BER in AMLEDs can be reduced through some design techniques. We discuss some of the design recommendations for high speed AMLEDs.

4.4.1 Effect of defect density

By the use of electron beam induced scanning electron microscopy it was shown [112] that avalanche is favored at the defect sites, indicating that defect locations are suitable for the triggering of avalanche. In modern CMOS technologies, the defect density can be increased by increasing the proximity of the depletion region to oxide interfaces, such as shallow trench isolation (STI) [77].

To demonstrate the effect of STI, a $p^+n$ AMLED with an STI interface and another device without an STI interface were selected from a 140 nm SOI CMOS technology. Fig. 4.16(a) shows the schematic cross-sections for these devices, denoted as S7 and C12 [79]. S7 is square in shape with an edge length of 7 μm, whereas C12 is a circular device with a diameter of 12 μm.

Fig. 4.16(b) shows the $I$-$V$ characteristics for these AMLEDs. The different $V_{BR}$ for S7 and C12 can be attributed to the different doping of the n-layer in these devices. Assuming one-sided abrupt junctions, the n-well doping in S7 and C12 is estimated as $6 \times 10^{16}$ and $9 \times 10^{16}$ cm$^{-3}$ respectively [12]. Fig. 4.16(b) shows an example of the light emission from these devices. The PPM speed of these AMLEDs was measured using the same driver circuit and PRBS data (Fig. 4.6). Figs. 4.16(c) and 4.16(d) show examples of the measured transients at the indicated bias settings. As the $V_{EX,i}$ at these bias settings is almost same (~4.9 V), the difference in the peak current of the transients can be explained by the lower resistance of
C12 due to a higher n-well doping, combined with geometric effects [12]. Figs. 4.16(e) and 4.16(f) show examples of the measured PDFs of jitter in S7 and C12 respectively.

For S7, the minimum attainable BER of $10^{-3}$ (section 4.3) and a standard deviation of jitter of $\sim 37$ ns were measured. The measured energy-per-bit at a data rate of 100 kbps was $\sim 23.8$ nJ/bit. In comparison, C12 showed a higher BER and jitter. These results demonstrate that an STI interface at the junction can help AMLEDs to achieve a higher speed.

### 4.4.2 Other recommendations

A three terminal AMLED structure was proposed and demonstrated [15] to increase its quantum efficiency. The basic idea is that the third terminal (emitter) injects more carriers, hence higher $N_d$, in the depletion region thereby triggering avalanche, which results in a low BER and jitter. Consequently, such a device concept results in high speed.

Another so-called $n^+pn^+pn^+$ device structure has been proposed to increase the efficiency of an AMLED [113]. This structure comprises two reverse biased light-emitting junctions and two forward-biased junctions to increase intra-band transitions by providing extra carriers. Such a device structure could also improve the speed.

Ideally, for a high speed AMLED, the maximum attainable speed is only limited by the time constants of the driver circuit (and not by triggering speed of avalanche in AMLEDs), which can be in the range of tens of picoseconds in modern CMOS technologies. This could result in a maximum attainable speed in the Gbps range. Furthermore, the ultimate achievable speed is limited by the small-signal modulation speed, which has been shown to be in the range of tens of GHz [63, 107].

### 4.5 Conclusions

The data transmission capabilities of silicon avalanche mode LEDs (AMLEDs) fabricated in CMOS was investigated. The data were modulated using pulse position modulation (PPM) and the bit-error-rate (BER) and the jitter in the transmitted data were measured. The results were correlated to the multiplication noise ($\sigma_M$) and the leakage current of the AMLEDs. The $\sigma_M$ of these AMLEDs increases with their size because of more defects in larger devices. It was shown that AMLEDs with a higher $\sigma_M$ display a lower BER. Through temperature and optical excitations, it was shown that the leakage current also improves the triggering of avalanche, thereby reducing BER and jitter. A PPM speed of 100 kbps at the energy consumption of 23.8 nJ/bit has been obtained from the high-speed AMLEDs. The triggering rate can be improved using AMLED designs to inject more carriers in the depletion region and some recommendations for such high speed
AMLED designs were proposed. The presented results are important for the design of low power monolithic optical links.
4.5. CONCLUSIONS

Figure 4.16: (a) Schematic cross-section of a square shaped AMLED (S7) and a circular AMLED (C12) in a 140 nm SOI CMOS technology. (b) Reverse I–V characteristics measured using a Keysight B2901A SMU with 1 s integration time. The light emission from these AMLEDs, biased at a reverse current of 1 mA, is also shown. The emission was captured using a Nikon D3100 camera with an integration time of 30 s. (c) An example of the measured transients for S7 for \(2^{10}\) bits when the driver circuit was operated with \(V_{\text{BIAS}} = 19.6\) V, \(V_{\text{DD}} = 5\) V and \(C_Q = 470\) pF. (d) Measured transients for C12 at \(V_{\text{BIAS}} = 16.1\) V, \(V_{\text{DD}} = 5\) V and \(C_Q = 470\) pF. (e,f) Measured PDFs of jitter for bit “0” and bit “1” at the same bias settings for the same devices.
CHAPTER 5

OPTICAL TRANSMITTER USING AVALANCHE LEDs IN SOI CMOS TECHNOLOGY

Abstract

This chapter presents a low power monolithically integrated optical transmitter with avalanche mode light emitting diodes (AMLEDs) in a 140 nm silicon-on-insulator CMOS technology. Silicon AMLEDs exhibit wide-spectrum electroluminescence (400 nm < \( \lambda \) < 850 nm), which has a significant overlap with the responsivity of silicon photodiodes. This enables monolithic CMOS integration of optocouplers, for e.g. smart power applications requiring high data rate communication with a large galvanic isolation. To ensure a certain minimum number of photons per data pulse (or per bit), LED drivers must be robust against process, operating conditions and temperature variations of the LED. Combined with the AMLED’s steep current-voltage curve at relatively high breakdown voltages, this conventionally results in high power consumption and significant heating. The presented transmitter circuit is intrinsically robust against these issues, thereby enabling low power operation.

This chapter is based on a publication in Optics Express [71].
5.1 Introduction

Many smart power applications require data communication with galvanic isolation. Currently this is achieved with inductive isolators (transformers), capacitive isolators or discrete optocouplers [20]. Integrated transformers are big and significantly add to cost and size while they are also prone to external electro-magnetic interference (EMI) [20]. Capacitive isolators can be integrated in the backend, but are relatively big when isolating between voltage domains that have a large voltage difference. Optocouplers on the other hand are immune to EMI effects and monolithic integration of optocouplers is attractive for smart power and on chip communication applications [38].

Wide spectrum electroluminescence (EL) from silicon (Si) p-n junctions operating in avalanche mode has been reported earlier [14, 114, 115]. Avalanche mode light emitting diodes (AMLEDs) are fast with reported small signal modulation speed in the range of tens of GHz [63]. An AMLED as a light source in a CMOS integrated optocoupler has also been proposed [16, 17, 41, 115, 116]. The coupling efficiency between the AMLED and an Si PD has been reported to be higher as compared to the same LED in forward mode of operation [13, 116]. This is because of the stronger overlap between the emission spectrum of Si AMLEDs and the spectral responsivity of Si PDs [17].

For optocoupling applications with a sufficiently low bit-error-rate (BER), the AMLED driver must ensure a certain minimum number of photons at the receiver side for data communication. However, optoelectronic properties of AMLEDs are sensitive to process, voltage and temperature (PVT) variations [65]. Together with their steep current-voltage ($I-V$) curve at relatively high voltages, this easily results in high power consumption, and significant heating [13, 116] which are bottlenecks to implement power efficient On-Off Keying (OOK) LED driver circuits in optocoupling applications. In this chapter, an AMLED driver circuit is introduced to solve these issues, enabling low power Si integrated optical transmitters.

Section 5.2 of this chapter describes several physics related properties of the AMLED that are relevant for this work. An estimate of the transmission efficiency of the designed optical link is estimated in section 5.3 which is essential to characterize the AMLED in terms of its photon flux output. The principle of the driver circuit is to drive the AMLED (per data bit) with a minimum quantity of avalanche charge required to get certain amount of detectable photons at the PD, independent of PVT variations. The circuit to implement these features is introduced in section 5.4. The robustness, low power consumption and emission properties of the AMLED integrated with the driver circuit are demonstrated using the measurement results in section 5.5. Section 5.6 discusses the potential of using the AMLED with the introduced driver circuit in a monolithically integrated CMOS optocoupler. This chapter is concluded in section 5.7.
5.2 Optoelectronic properties of the AMLED

Fig. 5.1(a) shows a schematic cross-section of an AMLED (not to scale) in a 140 nm SOI CMOS technology [36]. The Medium Trench Isolation (MTI) regions and Buried Oxide (BOX) layer isolate the high voltage at the AMLED from the CMOS circuitry and provide galvanic isolation from the receiver. Fig. 5.1(b) shows the TCAD simulated 2-D electric field profile (in the y-z plane), for the regions in the dashed box in Fig. 5.1(a). The field was simulated above breakdown: at a reverse bias ($V_{\text{BIAS}}$) of 18 V for an AMLED having a breakdown voltage ($V_{\text{BR}}$) of $\sim$ 17 V. Avalanche breakdown and hence avalanche mode light emission is initiated in the region with the highest electric field [117], indicated in Fig. 5.1(b). Further, light is emitted mainly from the n$^+$ periphery that is closest to the p$^+$ contact (along x-axis) in Fig. 5.1(b). Figs. 5.1(c)-5.1(d) show the TCAD simulated electron and the hole current density for the dashed region in Fig. 5.1(a).

A schematic top view of the AMLED and the integrated PD, including their dimensions is shown in Fig. 5.2(a). Two identical diodes have been
5.2. OPTOELECTRONIC PROPERTIES OF THE AMLED

Figure 5.2: (a) Schematic top view of the AMLED and the PD which are basically identical diodes; all dimensions are in \( \mu \text{m} \). (b) Micrograph of the designed AMLED-PD structure. (c) Micrograph of the light emission from the AMLED using 25 s camera integration time, biased at a 4 mA DC current using an Agilent B2901A. (d) Emitted photon spectral flux density \((\Phi(\lambda))\) at \( T = 25^\circ\text{C}\); measured vertically using an Avantes ADC-1000-USB spectrometer and an integration time of 30 s. The arbitrary units [a.u.] are because of the unknown capture efficiency of the spectrometer.

When used, one acting as an AMLED and the other acting as a PD. Fig. 5.2(b) shows the micrograph of the AMLED and PD. This PD is only used to measure photon flux of the AMLED as discussed in section 5.5.2. Fig. 5.2(c) shows an EL-micrograph of the AMLED demonstrating emission at one side (along the x-axis indicated in Fig. 5.2(a)) as explained by TCAD simulation results in Fig. 5.1(b)-5.1(d). Fig. 5.2(d) shows the emission spectrum of the AMLED as measured vertically. Fabry-perot interference in the back-end causes the ripples in this vertical emission spectrum.

Measured reverse \( I-V \) characteristics of the AMLED are shown in Fig. 5.3(a); for circuit simulations, these characteristics were used in a table-based (interpolated) \( I-V \) model combined with a junction capacitance model. \( V_{BR} \), as indicated in Fig. 5.3(a), is defined as the voltage at which the \( I_{AMLED} \) starts to sharply increase. Fig. 5.3(b) shows a linear relation between the AMLED avalanche charge \((Q_{AMLED})\) and the vertically emitted number of photons as calculated by Eq. (5.2) (section 5.3). For Fig. 5.3(b), the AMLED was biased at several DC currents \((I_{AMLED})\) using an Agilent B2901A source and measurement unit (SMU) (with measurement integration time = 1 s) and the vertical emission spectrum was measured.
Figure 5.3: (a) Measured AMLED reverse $I$-$V$ characteristics at several temperatures using an Agilent B2901A and with 1s integration time. The inset shows the $I$-$V$ characteristics (on linear scale) in the breakdown region to explicitly show the resistance ($\sim 1.4 \, k\Omega$) of the AMLED at $T = 25 ^\circ C$. (b) Estimated total number of vertically emitted photons (arbitrary units) as a function of AMLED charge ($Q_{AMLED}$) at $T = 25 ^\circ C$.

using an Avantes ADC-1000-USB spectrometer with measurement integration time ($T_{\text{integration}}$) of 30 s at each setting. $Q_{AMLED}$ is calculated as $Q_{AMLED} = \int_0^{T_{\text{integration}}} I_{AMLED}(t) \, dt$.

5.3 Optical link transmission efficiency, $\eta_{TE}$

From a system level perspective, the total number of photons received at the PD per bit ($N_{\text{photons,PD}}$) in response to the electrical energy spent in the AMLED per bit ($E_b$) is important. As discussed in section 5.5.2, $E_b$ is pro-
Optical Link Transmission Efficiency, $\eta_{TE}$

Figure 5.4: Schematics of the AMLED-PD to estimate the optical transmission efficiency ($\eta_{TE}$). For clarity, important dimensions are enlarged. $x_{LED}$ is the width of the AMLED (=45 $\mu$m), $x_2$ (= 7.2 $\mu$m) is the separation between the AMLED and the PD and $z_{PD} = 1.5$ $\mu$m is the thickness of the SOI layer.

Portion of the number of electrons flowing through the AMLED per bit ($N_{\text{electrons,AMLED}}$). For estimating $N_{\text{photons,PD}}$ in response to $N_{\text{electrons,AMLED}}$, the total coupling quantum efficiency of the link ($\eta_{\text{system}}$) should be determined, that is defined as $\eta_{\text{system}} = \frac{N_{\text{photons,PD}}}{N_{\text{electrons,AMLED}}} \propto \frac{N_{\text{photons,PD}}}{E_b}$. This $\eta_{\text{system}}$ can be written as a product of two efficiencies. The first one is the internal quantum efficiency ($IQE$) of the AMLED which relates the number of photons emitted per bit from the AMLED ($N_{\text{photons,AMLED}}$) to $N_{\text{electrons,AMLED}}$ [21]. The second efficiency component is the transmission efficiency of the optical link between the AMLED and the PD, denoted as $\eta_{TE}$.

$$\eta_{\text{system}} = IQE \cdot \eta_{TE}$$

$$= \frac{N_{\text{photons,AMLED}}}{N_{\text{electrons,AMLED}}} \cdot \frac{N_{\text{photons,PD}}}{N_{\text{photons,AMLED}}}$$

(5.1)

The IQE is estimated from DC measurements in section 5.5.2. In this section, the $\eta_{TE}$ is estimated. Fig. 5.4 represents the schematic structure of the design. The following steps describe the $\eta_{TE}$ estimation procedure:

1. The total number of photons emitted by the AMLED ($N_{\text{photons,AMLED}}$) is calculated. As shown in Fig. 5.2(c), light emission occurs predominantly at the lower edge of the AMLED. This light emitting region is modeled as a line of length $x_{LED}$, emitting uniformly and isotropically [118, 119]. Total number of photons emitted by the AMLED ($N_{\text{photons,AMLED}}$) is given by:

$$N_{\text{photons,AMLED}} = \int \Phi(\lambda) \, d\lambda,$$

(5.2)

where $\Phi(\lambda)$ is the photon spectral flux density, shown in Fig. 5.2(d).

2. The total number of photons received at the PD ($N_{\text{photons,PD}}$) is calculated; it is assumed that $N_{\text{photons,PD}}$ is limited only by [12]:
• Absorption losses in the AMLED. The absorption coefficient ($\alpha$) is $\lambda$ dependent and for this layout of the AMLED and the PD (as shown in Fig. 5.2(a)), most of the photons emitted at short $\lambda$ are absorbed in the AMLED itself before reaching the PD.

• The finite solid angle of the PD over the AMLED. The solid angle is limited either by the critical angle at the Si-SiO$_2$ interface or by the (apparent) size of the PD (Fig. 5.4). Due to refraction at the Si-SiO$_2$ interface, the apparent height of the PD seen at each point along the $x$-axis of the AMLED is different: the apparent height of the PD ($y'_{PD}$) is reduced to $y'_{PD}(x)$. Similarly, the width of the PD ($z_{PD}$) is reduced to $z'_{PD}(x)$.

• The transmittance at the Si-SiO$_2$ interfaces ($T_x(x,y)$), which is given by Fresnel’s equations [21]. To simplify calculations, constant ($\lambda$-invariant) refractive indices ($n$) have been used for Si ($n = 3.9$) and SiO$_2$ ($n = 1.5$).

Along the $x$-direction, the lateral dimensions of the PD are much larger than the absorption length of the photons in the spectral region of interest. Therefore it is assumed that all the photons that reach the PD are detected.

Along the $z$-direction, for the DC measurements (section 5.5.2), any photon absorbed within a diffusion length from the depletion edge of the PD will contribute to the photocurrent [12].

Under these assumptions and using Fig. 5.4, the received photon spectral flux density per unit length at the PD is:

$$\Phi_{PD}(x, y, \lambda) = \Phi_{LED}(\lambda) \cdot \exp\left(-\alpha(\lambda) \cdot r_1(x, y'(x))\right) \cdot T_x(0, y'(x)) \cdot T_x(x_2, y) \cdot \Omega(x, y),$$

(5.3)

where:

• $\Phi_{LED}(\lambda) = \frac{\Phi(\lambda)}{x_{LED}}$ is the emitted photon spectral density per unit length.

• $r_1(x, y'(x)) = \sqrt{x^2 + (y'(x))^2}$ is the distance travelled by photons emitted at $(x, 0)$ within the AMLED, while propagating towards $(x_2, y)$.

• $T_x(0, y'(x))$ is the transmittance of the AMLED-SiO$_2$ interface and $T_x(x_2, y)$ is the transmittance of the SiO$_2$-PD interface.

• $\Omega(x, y) = \frac{d\Omega}{dy} = \frac{z'_{PD}(x) \cdot \cos(\theta)}{4\pi r_1^2(x, y'(x))}$ is the solid angle per unit length subtended by the PD over the $dx$ section of the AMLED.

The total number of photons received at the PD because of emission from the entire light emitting area of the AMLED ($N_{photons,PD}$) is given
by:

\[ N_{\text{photons, PD}} = \int_{0}^{-x_{\text{LED}}} \int_{0}^{y_{\text{PD}}(x)} \int_{\lambda} \Phi_{\text{PD}}(x, y, \lambda) \, d\lambda \, dy \, dx. \]  

(5.4)

3. Using numerical integration in Eq. (5.4), the \( \eta_{\text{TE}} = \left( \frac{N_{\text{photons, PD}}}{N_{\text{photons, AMLED}}} \right) \) of the link is estimated as \( 3 \times 10^{-4} \).

It is emphasized that the \( \eta_{\text{TE}} \) of this link is low due to mainly the sub-optimum (side-by-side) layout of the AMLED and the PD, as shown in Fig. 5.2(a) and 5.2(b). A possible direction to improve \( \eta_{\text{TE}} \) is discussed in section 5.6.

### 5.4 AMLED driver circuit for an optocoupler

The main idea of the circuit is to drive the AMLED with a minimum amount of avalanche charge per data bit \( Q_{b} \), required to get a certain amount of photons at the PD, independent of PVT variations, with a relatively small area and relatively low demands on the driver circuit (including timing demands).

Fig. 5.5 shows the principle of the self-quenched AMLED driver circuit (using idealized time domain waveforms) that limits the \( Q_{b} \) by dynamically quenching the avalanche process. The voltage across the AMLED is denoted as \( V_{\text{AMLED}} (= V_{\text{BIAS}} - V_{\text{CAP}}) \). The excess bias voltage across the AMLED \( (V_{\text{EX}}) \) is defined as the extra voltage above \( V_{\text{BR}} \) [22], \( V_{\text{EX}} = V_{\text{AMLED}} - V_{\text{BR}} \). The current through the AMLED is denoted as \( I_{\text{AMLED}} \) and the series resistance of the AMLED for \( V_{\text{AMLED}} > V_{\text{BR}} \) as \( R_{\text{AMLED}} \). \( R_{\text{AMLED}} \) is estimated from the measured \( I-V \) characteristics of Fig. 5.3(a) and is assumed constant for \( V_{\text{AMLED}} > V_{\text{BR}} \) for simplicity [22].

The junction capacitance of the AMLED in and near avalanche is modeled by capacitance \( C_{\text{AMLED}} \). The resistance of the driver circuit is negligible in comparison to \( R_{\text{AMLED}} \) and hence is ignored. A simplified model for the AMLED is also shown in Fig. 5.5 including \( C_{\text{AMLED}} \) [22]. For this section, it is assumed that \( C_{\text{AMLED}} << C_{Q} \) for simplicity reasons.

The operating principle is described using Fig. 5.5. Initially, the voltage across the \( C_{Q} \), \( V_{\text{CAP}} - V_{\text{DRV}} \) is 0. First reset switch M1 is opened (using control signal RST) after which the input (IN) is set high at \( t = 0 \). As IN is set high, the \( V_{\text{DRV}} \) becomes low and instantly \( V_{\text{CAP}} \) also becomes low, the initial \( V_{\text{AMLED}} = V_{\text{BIAS}} - V_{\text{CAP}} \approx V_{\text{BIAS}} \) and \( V_{\text{EX}} = V_{\text{BIAS}} - V_{\text{BR}} \) (assuming \( C_{\text{AMLED}} << C_{Q} \)). The initial \( V_{\text{EX}} \) yields an initial \( I_{\text{AMLED}} = V_{\text{EX}} / R_{\text{AMLED}} \).

After the AMLED goes into the avalanche, \( I_{\text{AMLED}}(t) \) charges the quenching capacitor \( C_{Q} \) which results in approximately an exponentially increasing \( V_{\text{CAP}} \), hence in approximately an exponentially decreasing \( V_{\text{AMLED}} \) and \( V_{\text{EX}} \). As \( V_{\text{AMLED}} \) approaches \( V_{\text{BR}} \), the avalanche is quenched.

For \( t > T_{\text{ON}} \), first IN is set low turning the driver off and then M1 is closed to reset the \( V_{\text{CAP}} \) to \( V_{\text{DD}} \). Since the avalanche quenches itself by
reducing $V_{AMLED}$ to $V_{BR}$, this circuit is denoted as a self-quenched driver circuit.

$V_{AMLED}(t)$, $V_{EX}(t)$, $I_{AMLED}(t)$ and $Q_b(t)$ can now be estimated: assuming the total resistance to be $R_{AMLED}$ and the total capacitance to be $C_Q$, Eqs. (5.5) describe the mentioned physical quantities for $0 \leq t \leq T_{ON}$. The initial $V_{EX}$ has been denoted as $V_{EX,0}$:

$$V_{AMLED}(t) = V_{BIAS} - V_{EX,0} \cdot \left(1 - \exp\left(-\frac{t}{R_{AMLED}C_Q}\right)\right), \quad (5.5a)$$

$$V_{EX}(t) = V_{EX,0} \cdot \exp\left(-\frac{t}{R_{AMLED}C_Q}\right), \quad (5.5b)$$

$$I_{AMLED}(t) = \frac{V_{EX,0}}{R_{AMLED}} \cdot \exp\left(-\frac{t}{R_{AMLED}C_Q}\right), \quad (5.5c)$$

$$Q_b = \int_{0}^{T_{ON}} I_{AMLED}(t)dt = C_Q V_{EX,0} \cdot \left(1 - \exp\left(-\frac{T_{ON}}{R_{AMLED}C_Q}\right)\right) \leq C_Q V_{EX,0}. \quad (5.5d)$$
Eq. (5.5d) shows that $Q_b$ is limited to $C_Q V_{EX,0} \leq C_Q V_{DD}$.

The complete schematics of the implemented circuit are shown in Fig. 5.6(a). The driver circuit comprises a chain of inverters (with enable functionality to switch $C_Q$) and a reset transistor. Selecting a $C_Q$ value is achieved using a number of parallel identical drivers (D1 to D7) connected to the AMLED anode which can be controlled using their respective enable (EN) signals. Using D8 (identical to D1 to D7), the AMLED can be operated without any quenching (Fig. 5.10(c)-(d)). To enable the measurement of fast AMLED current transients at the onset of the avalanche, a differential structure is adopted and a measurement circuit was implemented using an open drain PMOS differential amplifier [9]. The measurement setup enables us to measure at frequencies up to 2.8 GHz. A micrograph of the implemented chip in a 140 nm CMOS SOI technology is shown in Fig. 5.6(b) [36]. It is shown that this driver circuit is robust to many physics issues related to the power dissipation and PVT variations of an AMLED.

5.5 Measurement results

In this section, to demonstrate the functionality of the circuit, an example of the measured transient avalanche current ($I_{AMLED}$) waveform is shown. Further, the measured energy-per-bit ($E_b$) of the presented transmitter and its photon flux output with respect to $E_b$ is presented. The robustness of $E_b$ against process, voltage, temperature, design and pulse width variations is then demonstrated.

5.5.1 Functionality

With an off-state $V_{AMLED} \approx V_{BR}$, in the on-state the AMLED is driven to $V_{AMLED} \approx V_{BR} + V_{EX,0}$ where $V_{EX,0}$ is $V_{DD}$ divided between $C_Q$ and $C_{AMLED}$ at $t = 0$ (using the simplified model of the AMLED shown in Fig. 5.5):

$$V_{EX,0} = V_{DD} \frac{C_Q}{C_Q + C_{AMLED}}.$$  \hspace{1cm} (5.6)

Measured transient waveforms of $I_{AMLED}$ at $T_{ON} = 35$ ns, pulse repetition rate ($f_s$) = 10 Mbit/s are shown in Fig. 5.7 for three different $C_Q$ settings at $V_{DD}=1.8$ V. $C_{AMLED}$ was measured to be about 650 fF at $V_{AMLED} = 15$ V (close to $V_{BR}$). For a lower $C_Q$, $V_{EX,0}$ is lower (Eq. (5.6)) and hence the magnitude of the $I_{AMLED}$ ($= V_{EX,0}/R_{AMLED}$) decreases (Fig. 5.3(a)). The charging rate of $C_Q$ is limited by the $R_{AMLED}$ (estimated as $\sim 1.4$ k$\Omega$ from Fig. 5.3(a)). Simulated transient data (using Spectre [120]) were obtained using a lookup table based model of the DC AMLED $I$-$V$ characteristics (Fig. 5.3(a)) in combination with a junction capacitance model for $C_{AMLED}$ (see dashed area in Fig. 5.5(a)).
Figure 5.6: (a) Complete schematics of the integrated AMLED driver and the measurement circuit. (b) Micrograph of the implemented chip. The chip was packaged without any top cover to measure vertical light emission from the AMLED. Also, no tiling was used over the AMLED area to avoid light blocking by the backend. Total area of the chip is 0.38 mm², the area of the main driver is 0.024 mm² and the area of the AMLED is 0.0033 mm².

### 5.5.2 Energy-per-bit and emitted photon flux per bit of the AMLED

From an electrical point of view $E_b$ is a key Figure of Merit (FoM) [38]; $E_b$ is defined as the energy required to transfer $Q_b$ through the AMLED:

$$E_b = V_{\text{BIAS}}Q_b,$$

where $V_{\text{BIAS}}$ is the DC bias voltage at the AMLED cathode (Fig. 5.6). $E_b$ is limited by $C_Q$ (Eq. (5.5d), Eq. (5.6)):

$$E_b = V_{\text{BIAS}}Q_b \leq V_{\text{BIAS}}C_QV_{\text{EX,0}} \leq V_{\text{BIAS}}C_QV_{\text{DD}}.$$  

Fig. 5.8(a) shows an example of the measured $E_b$ as a function of $C_Q$. The simulated $E_b$ in Fig. 5.8(a) was obtained using Spectre [120], showing a good agreement with the measured $E_b$. The slight difference could
5.5. MEASUREMENT RESULTS

Figure 5.7: (a) Control signals (IN and RST). (b) Measured transient waveforms of \( I_{AMLED} \) for three different \( C_Q \) settings at \( f_s = 10 \) Mbit/s. For clarity, only three settings are shown. The jitter at lower \( C_Q \) setting is because of a lower \( Q_b \) (chapter 5). Simulated transient data are also shown only for \( C_Q = 14.7 \) pF for clarity.

be explained by the variations in \( C_{AMLED} \) with \( V_{AMLED} \), which has been assumed constant in the simplified model (Fig. 5.5). The loss in the driver circuit was estimated to be about 21 pJ/bit.

For on-chip optical data communication, the lateral photon transmission to a nearby PD is relevant. To measure this, a calibration PD was integrated next to the AMLED (see Figs. 5.2(a)-5.2(b)). The AMLED is operated in OOK mode at \( f_s \) and the average PD photocurrent (\( I_{PD} \)) is measured. From \( I_{PD} \) and \( f_s \), the number of photons received at the PD per bit (\( N_{photons,PD} \)) can be estimated:

\[
N_{photons,PD} = \frac{I_{PD}}{\eta_{PD} \cdot q_e \cdot f_s},
\]

where \( q_e (= 1.6 \times 10^{-19} \) C) is the elementary charge. The quantum efficiency of the PD (\( \eta_{PD} \)) is assumed to be unity in the wavelength region of interest because the dimensions of the PD are much larger than the absorption length of the photons [12]. Fig. 5.8(b) shows an example of the measured \( N_{photons,PD} \) (on left y-axis) at different \( E_b \) settings at \( T_{ON} = 35 \) ns and \( f_s = 10 \) Mbit/s.
Figure 5.8: (a) Measured and simulated $E_b$ as a function of $C_Q$. (b) Estimated $N_{\text{photons,PD}}$ and $N_{\text{photons,AMLED}}$ as a function of $E_b$. $N_{\text{photons,PD}}$ and $N_{\text{photons,AMLED}}$ are photons per bit.

From $\eta_{\text{TE}} = 3 \times 10^{-4}$ (as derived in section 5.3 for this sub-optimum AMLED-PD layout) and $N_{\text{photons,PD}}$, the total number of photons emitted by the AMLED per bit ($N_{\text{photons,AMLED}}$) for different $E_b$ settings can be estimated (Eq. (5.10)):

$$N_{\text{photons,AMLED}} = \frac{N_{\text{photons,PD}}}{\eta_{\text{TE}}}.$$  \hspace{1cm} (5.10)

An example of the estimated $N_{\text{photons,AMLED}}$ is also shown in Fig. 5.8(b) (on right y-axis). In turn, from $N_{\text{photons,AMLED}}$ and the number of electrons through the AMLED per bit ($N_{\text{electrons,AMLED}} = \frac{Q_b}{q_e} = \frac{E_b}{V_{\text{BIAS}}q_e}$), the internal quantum efficiency ($IQE$) of the AMLED (Eq. 5.1) is estimated to be about $1.4 \times 10^{-5}$, comparable to what was also reported for Si earlier [16]. Using $IQE$ and $\eta_{\text{TE}}$ (section 5.3), $\eta_{\text{system}}$ is estimated as $4 \times 10^{-9}$ [13].
5.5. MEASUREMENT RESULTS

Figure 5.9: (a) Measured vertical photon spectral flux density for several \( C_Q \) using an Avantes ADC-1000-USB spectrometer with an integration time of 30 s. (b) Measured vertical photon flux density per bit \( (\Phi_b) \) as a function of \( E_b \).

Fig. 5.9(a) shows the measured \( \Phi(\lambda) \) of the vertical emission of the AMLED (along the z-axis in Fig. 5.4), driven at \( T_{ON} = 35 \) ns, \( f_s = 10 \) Mbit/s, for several values of \( C_Q \), demonstrating that the \( \Phi(\lambda) \) is almost proportional to \( C_Q \), with a minor effect on the spectrum. Only four \( C_Q \) settings are shown for clarity. The increase in intensity with increasing \( C_Q \) is because of increasing \( Q_b \).

Fig. 5.9(b) shows the vertical photon flux density per bit, \( \Phi_b = \frac{1}{f_s} \int \Phi(\lambda) d\lambda \), as a function of \( E_b \), demonstrating a linear relation between \( E_b \) and \( \Phi_b \).
5.5.3 Robustness of $E_b$

The robustness of this driver circuit to process, voltage, temperature, design and pulse width ($T_{ON}$) variations is demonstrated using $E_b$ as the FoM. Note that using the results shown in Fig. 5.8(b), $E_b$ can be translated into $N_{photons, PD}$.

For process variations, three different samples of the n$^+$p AMLED, D1, D2 and D3 from the same processing batch were used.

To explicitly show robustness of the proposed driver circuit with design variations, $E_b$ for a p$^+$n diode was also measured, driven by a replica driver circuit (Fig. 5.6). The physical properties of both the square shaped AMLEDs are summarized in the table below:

Figure 5.10: Measured $E_b$ of an AMLED driven by the self-quenched driver circuit compared to using a traditional non-quenched driver circuit. (a) The n$^+$p AMLED: process and voltage variations. (b) The n$^+$p AMLED: temperature variations and $E_b$ vs $C_Q$ for the p$^+$n AMLED. (c) $E_b$ for non-quenched circuits for the n$^+$p and p$^+$n AMLED. (d) Effect of pulse width ($T_{ON}$) on $E_b$ for the n$^+$p AMLED.
Figs. 5.10(a)-5.10(b) show measured $E_b$ as a function of $C_Q$ across PVT and AMLED design variations, demonstrating that $E_b$ is hardly affected by these variations but can properly be tuned by setting different $C_Q$ values. $E_b$ is non-linear with respect to $C_Q$ because the pulse width ($T_{ON}$) is not sufficient to charge large $C_Q$ completely (Eq. (5.5d) and Fig. 5.5). A slight difference between the $E_b$ of the $n^+p$ AMLED and the $p^+n$ AMLED is because of the different $R_{AMLED}$, $C_{AMLED}$ and $V_{BR}$. The higher linearity in $E_b$ with respect to $C_Q$ in this $p^+n$ AMLED is because of its low $R_{AMLED}$.

Fig. 5.10(c) shows $E_b$ for conventional (non-quenched) OOK drivers that show a large sensitivity to PVT variations and (especially) design variations. The measured $E_b$ for the two types of AMLEDs are drastically different which is because of the different $I-V$ characteristics.

In data communication using OOK, the pulse width ($T_{ON}$ in Fig. 5.5) is of importance. Fig. 5.10(d) shows the impact of the pulse width ($T_{ON}$) on $E_b$: for the self-quenched driver, the $E_b$ is mainly determined by the selected $C_Q$, for pulse repetition rate ($f_s$) to $\sim$ 10 Mbit/s. At lower $T_{ON}$, the $E_b$ drops due to mainly $R_{AMLED}$ that determines the maximum $Q_b$ and hence the $E_b$ (Eq. (5.5d), Eq. (5.8)).

For comparison, the non-quenched operation of the AMLED in Fig. 5.10(d) shows a strong $T_{ON}$ (hence timing) dependency. The simulated $E_b$ (using Spectre) shows a good agreement with the measured $E_b$. For the non-quenched driver circuit, $E_b$ can be estimated as $E_b \approx V_{BIAS}I_{AMLED}T_{ON} = V_{BIAS}(V_{EX}/R_{AMLED})T_{ON}$. Using the self-quenched circuit, $E_b$ (thus power dissipation) is always lower than $C_QV_{DD}V_{BIAS}$ independent of $T_{ON}$ (Eq. (5.8)).

5.6 Application in opto-couplers

In this section, first the potential of the proposed optical transmitter for application in opto-couplers is discussed. In a further section, the aspects of the bit rate are addressed.

5.6.1 Optocouplers in CMOS technology

Although this work mainly focused on the transmitter in a fully Si-integrated optical link, the demonstrated optical transmitter aims at applications in a complete optical link in Si. For the minimum $E_b$ setting ($\sim$ 53 pJ/bit), the $n^+p$ AMLED emits 220 photons per bit isotropically (Fig. 5.8(b)). Using Fig. 5.8 and the model presented in section 5.3, it is possible to optimize the optical link geometry to receive e.g. 5 photons at the PD out of $N_{\text{photons,AMLED}}$. To receive more than 5 photons out of 220, it is required that $\eta_{TE} > 2 \times 10^{-2}$. For instance, when the AMLED and the PD are aligned as shown in Fig.

<table>
<thead>
<tr>
<th>shape</th>
<th>Edge length</th>
<th>$V_{BR}$</th>
<th>$R_{AMLED}$</th>
<th>$C_{AMLED}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>$n^+p$ square</td>
<td>45 μm</td>
<td>$\sim$ 17 V</td>
<td>1.4 kΩ</td>
<td>650 fF</td>
</tr>
<tr>
<td>$p^+n$ square</td>
<td>45 μm</td>
<td>$\sim$ 14.4 V</td>
<td>560 Ω</td>
<td>585 fF</td>
</tr>
</tbody>
</table>
5.11, the $\eta_{TE}$ of the link is estimated to be $2 \times 10^{-2}$ using the method in section 5.3. Further suggestions to improve $\eta_{TE}$ such as patterning the link, improving the AMLED design have been extensively discussed in [13]. Obviously, it is desired to have an optimized optical transmission path for waveguiding the photons from the AMLED to the PD. This will improve the coupling quantum efficiency, as well as reduce the crosstalk among multiple channels in a multi-channel communication environment.

If 5 photons are received at the PD per bit, this is typically sufficient to operate a well-designed Single Photon Avalanche Diode (SPAD) as a PD [22, 23, 109, 121–124]. SPADs are p-n junctions biased above $V_{BR}$ so that an incoming photon in the depletion region can generate free carriers thus triggering an avalanche. The macroscopic avalanche current can then be easily measured by using digital simple read out circuitry. The design of SPADs and SPAD read out circuits are well-known in CMOS technologies [23, 122, 123]. Recently, a SPAD designed in the same technology as presented in this work has been reported [79].

Further, in the current link geometry a large jitter would occur if 5 photons per bit were statistically received at the PD. This is because many free carriers are generated by the photons outside the depletion region which subsequently diffuse towards the depletion region triggering the SPAD after some ill determined diffusion time. Therefore, it is recommended to design links with a higher $\eta_{TE}$ and to capture photons mainly in the depletion region. This could be achieved by using e.g., less doping in the PD which results in a wider depletion region [12].
5.6.2 Bit rate

The bit rate in such an optical link would ultimately be limited by the speed of the detector (SPADs or the PDs). AMLEDs can be designed to achieve a relatively high speed (see chapter 4).

For SPADs, the achievable bit rate is limited by their deadtime requirement. To reduce the unpredictable and hence undesired afterpulsing phenomena in SPADs, the deadtime of the SPADs after each photon counting event has to be increased, which limits the bit rate of the SPADs [23, 79]. For the SPADs that were reported in this technology [79], the bit rate would be limited to about 10 Mbit/s based on their reported deadtime (~ 100 ns).

Using conventional photodiodes, higher bit rates can be achieved, however at the cost of increased power consumption in the AMLED. This is because of its required continuous operation mode to increase the signal-to-noise ratio (thereby to reduce the bit error rate) at the PD. A data rate of 3 Gbit/s has been reported with an integrated Si receiver in a standard 180 nm CMOS technology using off chip illumination [125].

5.7 Conclusion

In this chapter, a low power monolithically integrated optical transmitter using avalanche mode LEDs (AMLEDs) was designed in a standard 140 nm SOI CMOS technology. The novel self-quenched driver circuit resolves many physics issues related to power dissipation and PVT variations of an AMLED. This work successfully demonstrates a low power wide spectrum optical transmitter in a CMOS technology which can be integrated with standard silicon photodetectors. It further reinforces the promise of enabling AMLEDs as light sources in silicon CMOS technologies for monolithic integration of optocouplers. [13, 16, 41, 116].
CHAPTER 6

OPTOCOUPLING IN CMOS

Abstract

For on-chip data communication with galvanic isolation, a monolithically integrated optocoupler is strongly desired. For this purpose, silicon (Si) avalanche mode LEDs (AMLEDs) offer a great potential. However such AMLEDs have a relatively low internal quantum efficiency (IQE) and high power consumption. For the first time, in this work, data communication in a monolithically integrated optocoupler is experimentally demonstrated. The novelty of this work is the use of highly sensitive single-photon avalanche diodes (SPADs) for photodetection to compensate for the low IQE of AMLEDs. We investigated our optocoupler realized in a standard 140 nm CMOS SOI technology, without post-processing, for various LED designs and points of operation. The power consumption of the AMLEDs is minimized through a novel AMLED design and employment of a low power LED driver circuit. The advantages of AMLEDs over forward biased Si LEDs are also demonstrated. For the best AMLED design, the achievable data rate is few Mbps and the energy consumption a few nJ/bit. The active area of the proposed systems is < 0.01 mm².

This chapter is based on our work presented at the IEEE Electron Devices Meeting 2018 [72].
6.1 Introduction

Many smart power applications require data communication between different voltage domains with galvanic isolation. Currently this is achieved using inductive isolators, capacitive isolators or discrete optocouplers [20]. Such isolators have various disadvantages: they are large in size, source of extra complexity and costly. In addition, some are also sensitive to external interferences. Conversely, when monolithically integrated, optocouplers in standard CMOS technologies are largely free of such issues. Communication by means of light is also attractive because light is robust to external electrical or magnetic interferences.

So far, optocouplers have not been monolithically integrated in standard CMOS because of the poor overlap between the emission spectrum of a (traditional) forward biased Si LED and the responsivity of a Si photodetector (PD) (Fig. 6.1(a)). Therefore, in most discrete optocouplers, LEDs are designed using III-V compounds e.g. GaAs [11] and PDs are typically implemented using Si. This heterogeneous integration makes fabrication more complicated and thereby expensive.

In avalanche mode, Si AMLEDs have an emission spectrum in the visible wavelength region [14]; this spectrum has a significant overlap with the responsivity of Si PDs (Fig. Fig. 6.1(b)). The spectrum of AMLEDs is due to the radiative recombination of high-energy carriers during avalanche breakdown [14]. However, AMLEDs have a poor IQE ($^{-3}$) [15, 71]. Also, systems implemented with AMLEDs can be power hungry because of the avalanche operation under high voltage [64, 71].

The significant development of SPADs in CMOS [123] opens new routes for monolithic integration of optocouplers. SPADs are highly sensitive PDs and can be used to compensate for the poor IQE of AMLEDs. SPADs also have a count rate of up to tens of Mbps, which is sufficient for many smart power applications. For the first time, we report the integration of Si AMLEDs-SPADs for optocoupling applications (Fig. 6.2). It was shown that the breakdown voltage ($V_{BR}$) of AMLEDs can be reduced down to 6 V without degrading their IQE [67]. In this work, we have designed novel AMLED structures to reduce their $V_{BR}$ for low power consumption and then we employed these AMLEDs for optocoupling focusing on power consumption, speed and bit-error-rate (BER). The LEDs and SPADs were designed in a 140 nm CMOS SOI technology; the LED driver and SPAD quenching-and-recharge circuits (QRCs) were implemented externally using off-the-shelf components. The proposed optocoupler (LEDs and SPADs) occupy much less area ($\sim 0.008 \text{ mm}^2$) compared to inductors ($\sim 1.9 \text{ mm}^2$ in [126]) and capacitors ($\sim 1.6 \text{ mm}^2$ in [39]) of inductive and capacitive isolators respectively. The LED driver and QRCs can be integrated on-chip at relatively low area requirements [71, 123]. The results suggest a great potential of the AMLED-SPAD combination for monolithic optocoupling applications.
Chapter 6. Optocoupling in CMOS

6.2 Experimental Devices

6.2.1 AMLEDs and SPAD designs

Fig. 6.3 shows schematic cross-sections of the two LEDs (A1, A2) and the SPAD (S1). A1 is a vertical n⁺-pwell diode. For optimized light emission with a minimal $V_{BR}$, A2 was implemented using closely spaced p⁺-n⁺.

Figure 6.2: Proposed optical link comprising of AMLEDs and SPADs. The LED driver circuit drives the LED in response to the incoming data to transmit photons across the isolation barrier. Some of these transmitted photons are detected across the barrier by the SPADs with quenching-and-recharge circuits (QRCs). The data is demodulated from the output of QRCs.

Figure 6.1: Schematic illustration of a relatively (a) poor overlap between the emission spectrum of a forward biased Si LED (FMLED) and the responsivity of a Si PD. (b) good overlap between the emission spectrum of a Si avalanche mode LED (AMLED) and a Si PD.
Figure 6.3: Schematic cross-section (not to scale) of LEDs (A1, A2) and SPAD (S1); all dimensions in $\mu$m. Medium Trench Isolation (MTI) layers provide galvanic isolation between LEDs/SPADs.

Layers in an n-well. To reduce the dark count rate (DCR) of S1, the depletion region was isolated from the shallow trench isolation using a lowly doped guard ring [66]. The galvanic isolation was implemented using medium trench isolation (MTI); the MTI in this technology can provide an isolation of > 100 V [36]. Fig. 6.4 shows the micrograph of the devices A1-S1 and A2-S1. A “C”-shaped layout of A2 was designed to increase the coupling efficiency between A2 and S1.

Fig. 6.5 shows the reverse $I-V$ characteristics of the devices. The high leakage current for A2 is most likely because of the presence of band-to-band tunneling. This was verified by the temperature dependence of the $V_{BR}$. For A2, $V_{BR}$ was practically constant between 0 – 75 °C: possibly because the negative temperature dependency of Zener breakdown counteracts the positive temperature dependency of avalanche breakdown [12]. Fig. 6.6 shows the AMLED light emission profiles and spectra.

The DCR of the SPADs were measured using a passive QRC. Fig. 6.7 shows the dependence of the DCR on the excess bias voltage $V_{EX}$ (the excess voltage at which the SPAD is biased above breakdown) for different temperatures. The inset also shows the DCR measurement setup schematics. Fig. 6.8 shows the photon detection probability (PDP) of these SPADs, indicating their higher sensitivity in the visible wavelengths.
Figure 6.4: Micrograph of (a) A1-S1 (b) A2-S1. “K” and “A” indicate cathode and anode respectively. Closest distance between the active areas of the LED and the SPAD in both cases is 10 μm. The “C” shaped layout of A2 was chosen to increase coupling efficiency ($\eta_{CE}$) between LED and PD (Fig. 6.10). A circular SPAD is used to reduce edge breakdown. “HW” is the handle wafer contact, acting as heat sink.

S1 was biased above breakdown and the avalanche firing rate (AFR) of S1 was measured when the corresponding LED was biased at the same DC current level in avalanche mode (AM) or in forward mode (FM), i.e. an FM-LED. In dark conditions, AFR is equivalent to DCR. For the AM operation, the measured AFR was ~ 4x higher than that for the FM operation (Fig. 6.9). Despite that the IQE for an FMLED is about two orders of magnitude higher [127] than that of an AMLED, Fig. 6.9 demonstrates better spectral overlap for AMLED-SPAD than that for FMLED-SPAD. Fig. 6.10 shows the DC coupling between the A1-S1 and A2-S1 when S1 was operated as a “conventional” photodiode. A higher coupling efficiency ($\eta_{CE}$) is observed for A2-S1 than for A1-S1.

### 6.2.2 LED driver circuit schematics

Fig. 6.11 describes the equivalent circuit of our LED driver. It is a self-quenched driver circuit that limits the amount of charge-per-bit ($Q_b$) and energy-per-bit ($E_b$) through the LED. For more details about this driver circuit, please refer to chapter 5 in which a fully integrated version was presented.

### 6.2.3 Data modulation and BER definition

For SPADs, the data are encoded in their timing response [109]. Therefore, an appropriate modulation scheme for data communication using SPADs is a pulse position modulation (PPM) [109]. Fig. 6.12 shows the schematics of the LED driver control signals IN and RST for this modulation scheme.
6.2. EXPERIMENTAL DEVICES

Figure 6.5: Reverse $I - V$ characteristics of the devices measured using an Agilent B2901A with 1 s integration time. $V_{BR}$ (indicated) is defined as the voltage at which $I_R$ starts to sharply increase. $V_{BR}$ for A1 had a temperature coefficient of $\sim 16$ mV/K whereas $V_{BR}$ for A2 showed negligible temperature dependency. The temperature coefficient of $V_{BR}$ for S1 was measured to be $\sim 8$ mV/K.

Figure 6.6: (Left) Visible light emission profiles from A1 and A2 (on same scale as Fig. 6.4). AMLEDs were biased at a constant reverse DC current of 10 mA and the images were captured using a Nikon D3100 camera using 30 s integration time. For A1, light is emitted mostly from the edge closest to the $p^+$ region due to high field and current crowding [71]. For A2, light is emitted from the regions between the $n^+$ and $p^+$ regions, indicating a lateral breakdown. (Right) Normalized emission spectra of the two AMLEDs for two $I_R$ values.
A PRBS data sequence with a length of $2^{10}$ bits was generated and the performance of the optical links was measured in response to this data sequence across all LEDs and operating conditions.

The received data is decoded from the timing response of the SPAD: if the SPAD is triggered only during the first half of the bit duration, data bit is demodulated as a “0”, and if the SPAD is triggered only during the last half of the bit duration, data bit is demodulated as “1”. For all other cases, data cannot be reliably decoded and those cases were counted as bit-error (Note: BER > 0.5 is possible as per this definition). Using the demodulated data, the BER of the link was estimated.

### 6.3 Optical Link Performance

Fig. 6.13 shows an example of the measured transients of the control signals of the driver circuit, $I_{AMLED}$ and output of QRC ($V_{OUT}$) at the specified operating conditions. Figs. 6.14 and 6.15 show the estimated BER of the system for different operating conditions when the LEDs were operated in FM and AM respectively. Both in AM and FM, A2–S1 show a lower
6.3. OPTICAL LINK PERFORMANCE

Figure 6.8: Photon detection probability (PDP) of SPADs as a function of wavelength of incident photons for different SPAD excess bias [79]. A high sensitivity for photons in the visible spectrum can be observed.

Figure 6.9: Avalanche firing rate (AFR) as a function of $V_{EX}$ when S1 was biased above breakdown and illuminated with the integrated Si LED (A1). When A1 was biased using a reverse current in avalanche mode (AM), a higher AFR was obtained compared to the case when A1 was biased using a same value of forward mode (FM) DC current. In dark conditions, AFR is equivalent to DCR.
Figure 6.10: DC coupling: S1 was operated as a “conventional” PD and its short circuit current ($I_{PD}$, which is reverse current at $V_R = 0$) was measured when the corresponding LEDs (A1/A2) were biased in AM and FM using DC currents ($I_{LED}$). A2-S1 shows a higher $\eta_{CE} = I_{PD}/I_{LED}$ than A1-S1 (Fig. 6.4).

Figure 6.11: AMLED driver circuit with illustrative transients [71]. Initially, RST is set high, thus turning off the switch M1. Then IN is set high and initially voltage across an AMLED ($V_{AMLED} = V_{BIAS} - V_{CAP}$) increases to $\sim V_{BIAS}$ (assuming quenching capacitance $C_Q \gg$ parasitic capacitance $C_{PAR}$). As $V_{AMLED} > V_{BR}$, avalanche is triggered and the avalanche current ($I_{AMLED}$) charges $C_Q$, thereby increasing $V_{CAP}$ and reducing $V_{AMLED}$. As $V_{AMLED}$ approaches $V_{BR}$, the avalanche is quenched. The total charge per bit through the AMLED ($Q_b$) is limited to $C_Q(V_{BIAS} - V_{BR})$. The energy-per-bit ($E_b$) is then limited to $Q_bV_{BIAS}$. $I_{AMLED}$ is directly measured on a Keysight DSO-X 3024A oscilloscope. In our setup, $Q_b$ and therefore $E_b$ could be tuned by tuning $C_Q$ and $V_{BIAS}$ (with $V_{BIAS} < V_{DD} < V_{BR}$). A simplified model for an AMLED is shown in the right hand side dashed area [22, 71].
6.3. OPTICAL LINK PERFORMANCE

Figure 6.12: Illustrative transient waveforms of the control signals IN and RST for bit “0” and for bit “1” when data were modulated using a two-level PPM. $T_{\text{bit}} = 1/f_S$ is the bit duration time where $f_S$ is the data rate. For bit “0”, photons are transmitted in the first half of $T_{\text{bit}}$, whereas for bit “1”, photons are transmitted in second half.

BER than A1–S1 because of the higher $\eta_{\text{CE}}$ (see Fig. 6.10). At higher $f_S$, $T_{\text{ON}}$ reduces (Fig. 6.11) and therefore $Q_b$ decreases [71]; a lower $Q_b$ reduces the amount of photons per bit ($P_b$) and thereby increases the BER.

In AM, a much lower BER is obtained compared to FM operation. At higher $f_S$, A2 shows a lower BER also because of its lower resistance (~ 25 $\Omega$) than A1 (~ 850 $\Omega$): for a fixed $T_{\text{ON}}$, a low resistance results in a high $Q_b$ and consequently a high $P_b$, therefore a low BER [71]. Fig. 6.16 shows the measured BER of the optical link as the $E_b$ through the LEDs was varied.

Figure 6.13: For A1–S1, $V_{\text{BIAS}} = 20 \text{ V}$, $V_{\text{DD}} = 4 \text{ V}$, $C_Q = 100 \text{ pF}$, $f_S = 100$ kbps, $V_{\text{EX,SPAD}} = 1 \text{ V}$: (Left) Example of measured transients of the control signals IN and RST (top); measured $I_{\text{AMLED}}$ and SPAD QRC output ($V_{\text{OUT}}$) in response to those control signals (bottom). For marked bit 1, SPAD is triggered after some delay whereas SPAD is not triggered for bit 2 causing a bit-error. For bits 3,4,6,7 and 8, SPAD is triggered when AMLED is triggered. For bit 5, SPAD is triggered multiple times. (Right: a zoomed response of $I_{\text{AMLED}}$ and $V_{\text{OUT}}$ (scales enhanced for clarity)).
for several $f_S$ in FM and AM. In AM, A2 requires $\sim 2\times$ lower $E_b$ than A1 for a similar BER which demonstrates the advantages of our low $V_{BR\ AMLED}$ design. The bare BER performance measured here can be improved using (e.g. Hamming) error-correction techniques, requiring a limited number of (low voltage) logic gates.

These results demonstrate that SPADs combined with AMLEDs are
6.4 Conclusions

In this work, for the first time, we have successfully demonstrated data communication using a monolithically integrated optical link in a standard CMOS technology without any post processing. The optical link was implemented using Si AMLEDs and SPADs. The advantages of AMLEDs over forward biased Si LEDs were shown. The coupling efficiency of LEDs with Si PDs was improved by a novel AMLED design. Poor quantum efficiency of AMLEDs was compensated by the use of highly sensitive SPADs. The energy consumption of the system was minimized by the design of novel AMLEDs and low power driver circuits. The results show strong potential of AMLEDs-SPADs combination for monolithic optical links.
Figure 6.17: Application example: IEEE logo (8-bit resolution) transmitted through our optical link A1-S1 (in AM) at 1 Mbps. From the received bits, image was reconstructed without any error-correction. Bits which could not be resolved from SPAD output were estimated as 0 or 1 randomly using uniform distribution.
Figure 6.18: Application example: Flag of Twente transmitted through our optical link A1-S1 (in AM) at 1 Mbps.
CHAPTER 7

CONCLUSIONS AND RECOMMENDATIONS

This chapter summarizes the most important conclusions of this thesis. Recommendations for future work are also discussed.

Chapter 2

This chapter focused on the Random Telegraph Signal (RTS) phenomena in diode avalanche currents. RTS phenomena were shown to determine the steep current-voltage ($I - V$) dependency in avalanche. Using a time domain analysis (TDA), the statistical properties of these RTS phenomena were determined. An accurate experimental definition of breakdown voltage ($V_{BR}$) was proposed based on the RTS analysis. Further, the value of the self-sustaining avalanche current in diodes was parametrically determined which can be used for an accurate design of quenching-and-recharge circuits (QRCs) for SPADs. It is expected that the proposed approach can significantly increase count rates while reducing afterpulsing. The advantages of the accurate design of QRCs were discussed with circuit simulations.

This work can be extended by an experimental demonstration of the proposed accurate QRCs.

Chapter 3

In this chapter, the RTS phenomena of avalanche diodes comprising ultrashallow junctions, fabricated using a pure boron technology, were analyzed using the TDA method presented in chapter 2. From these results, the device size dependency on the RTS parameters (amplitude and mean on-time fraction) was discussed. The amplitude of the RTSs was shown to increase with the size of the diode which was explained by the spreading
of avalanche of a single defect to more defects in larger diodes. Two different types of RTSs were observed in these diodes: a “decaying” and a “constant” amplitude type and a comprehensive analysis of both RTSs was done. It was proposed that the “decaying” amplitude RTSs are caused by defects with a relatively high thermal impedance. By modeling these RTS phenomena and using the TDA, some of the defect parameters were estimated. The ohmic series resistances of these defects were estimated to be hundreds of ohms to a few kΩ. The effective diameter of the defects was estimated to be in the order of a few μm. Further, it was argued that the “decaying” amplitude RTSs are caused by relatively small (effective) area defects and the “constant” amplitude RTSs by relatively large area defects.

For future work, measurements on low defect density diodes would give additional information about the properties of the individual defects causing RTSs. An ideal device for such a study would be a diode with defects having significantly distinct breakdown voltages. Such devices were available in early Si technologies possibly because of the process variation. The processing is relatively more uniform in modern CMOS technologies and therefore such devices need to be explicitly designed. A possible approach would be the use of junctions with different breakdown voltages in a single device. Geometric variations (e.g. a sharp edge at one location of a device) can also result in defects having different breakdown voltages. Simultaneous optical measurements and TDA can be used to completely characterize defects in terms of their geometry and electrical parameters.

High speed optical measurements such as those used in photo emission micrography (PEM) can be used to investigate the propagation of RTSs in time domain. For the two types of RTSs, another hypothesis was also discussed but could not be verified: the “decaying” RTSs are caused by the defects located at the periphery of a diode whereas “constant” RTSs are caused by the defects in the active area of a diode. This hypothesis can be verified with high speed and highly sensitive optical measurements as well.

Chapter 4

This chapter discussed the data transmission capabilities of Si avalanche mode LEDs (AMLEDs) fabricated in CMOS technologies. The data were modulated using a pulse position modulation scheme, and the bit-error-rate (BER) and the jitter in the transmitted data were measured. The results were correlated to the multiplication noise ($\sigma_M$) and the leakage current of the AMLEDs. The $\sigma_M$ of these AMLEDs were shown to increase with the size of the AMLEDs because of more defects in larger diodes. It was shown that AMLEDs with a higher $\sigma_M$ display a lower BER and jitter. Through temperature and optical excitations, it was demonstrated that an
CHAPTER 7. CONCLUSIONS AND RECOMMENDATIONS

Elevated leakage current improves the triggering rate of avalanche, thereby reducing the BER and jitter. Based on the outcome of this research, design recommendations to improve the speed of the AMLEDs were discussed.

In this chapter, a three terminal AMLED device structure has been proposed for high speed but not yet verified through measurements. The demonstration of such a device structure could be important for design of high speed optical links.

It was observed that diodes with an STI interface achieve a higher speed, however have a lower leakage current. A possible explanation for this could be that defects formed near the STI do not contribute to the leakage current, but do affect the RTS behavior. However, the effect of an STI interface on the leakage current could not be investigated due to the lack of diodes with a different geometry. From the $I-V$ characteristics of diodes with different sizes, the contribution of an STI interface on the leakage current and RTS behavior can be extracted in a future work.

Chapter 5

A low power monolithically integrated optical transmitter using AMLEDs was demonstrated in a standard 140 nm SOI CMOS technology. A novel self-quenched driver circuit was proposed which resolves many physics issues related to power dissipation and process, voltage and temperatures (PVT) variations of an AMLED. This work successfully demonstrated a low power wide spectrum optical transmitter in a CMOS technology which can be integrated with standard Si PDs. It further reinforced the promise of enabling AMLEDs as light sources in silicon CMOS technologies for monolithic integration of optocouplers.

Chapter 6

In this chapter, for the first time, we have successfully demonstrated data communication using a monolithically integrated optical link in a standard CMOS technology without any post processing. The optical link was implemented using Si AMLEDs and SPADs. The advantages of AMLEDs over forward biased Si LEDs were shown. The coupling efficiency of LEDs with Si PDs was improved by a novel AMLED design. Poor quantum efficiency of AMLEDs was compensated by the use of highly sensitive SPADs. The energy consumption of the system was minimized by the design of novel AMLEDs and low power driver circuits. A comparison between an optical link employing an forward biased Si LEDs and SPAD, i.e. FMLED-SPAD structure with that of an AMLED-SPAD structure was also done and the advantages of AMLEDs over FMLEDs was clearly demonstrated. The results show strong potential of AMLEDs-SPADs combination for monolithic optical links.
The achieved coupling efficiency between the AMLEDs and the PDs ($\eta_{CE}$) can be improved by the use of waveguiding structures, e.g. metallic waveguides as shown in [41]. A higher $\eta_{CE}$ would result in a lower energy-per-bit ($E_b$) for an optical link.

**General recommendations**

The SPADs need to be designed to achieve a low dark count rate (DCR) for a large dynamic range of an optical link. For a low DCR, the depletion region of a SPAD should be located as far as possible from the oxide interfaces. The oxide interfaces induce defects in the depletion region that degrade the quality of SPADs. These interface defects can be reduced by the use of guard rings surrounding SPADs. A downside is that a guard ring reduces the $\eta_{CE}$ because of the high absorption coefficient of Si. A lower $\eta_{CE}$ increases the $E_b$ requirement at the transmitter. Therefore, an optimum value for the width of the guard ring would be required for a low DCR and a low $E_b$.

In this project, the devices consisted of shallow junctions. Integrated optocouplers however require lateral coupling of light and consequently deeper junctions are preferred for SPADs to reduce the impact of back-end oxide interfaces.

The speed of AMLEDs is expected to increase at smaller technology nodes because of a higher number of defects and smaller distances from the oxide interfaces. Therefore, AMLEDs have an intrinsic advantage with the technology downscaling. However, the same scaling makes the design of SPADs in those technologies more challenging. The design of SPADs in smaller technology nodes is currently under active research.

The maximum operating speed of SPADs is expected to be about tens of Mbps. The use of time-interleaved structures can increase the achievable data rate of an optical link at the expense of power, area and more complicated driver and readout circuits.

Some technologies offer SiGe junctions; SiGe PDs have a higher responsivity at infrared wavelengths. Employing the SiGe junctions could also be an attractive way to implement optical links in those technologies.

Reliability and degradation analysis of systems implemented using avalanche diodes were not addressed in the scope of this thesis and are an important topic for future research as well.

**Original contributions of this thesis**

- The $I-V$ characteristics of the diodes operating in avalanche was explained using the RTS phenomena [68]. Using a time domain analysis, an accurate definition of experimental breakdown voltage was provided.
• Two types of RTSs have been shown in the avalanche current of diodes [69]. These RTSs were explained to be caused by the different thermal impedance of defects in the silicon. Based on a physics based model, the properties of the defects (ohmic series resistance and effective diameter) were estimated. The effective diameter of the defects were also estimated using optical measurements and a correlation with the TDA measurements was shown. The proposed model and analysis method can be used to estimate the properties of the defects for diodes in any technology.

• The dependency of the RTS parameters on the size of the diode was discussed [69]. For a larger diode, the increasing amplitude of the RTSs was explained by the spreading of avalanche of a single defect to more defects. The multiplication noise also increases for larger diodes because of an increasing number of defects.

• The application of the analysis of RTS phenomena for accurate design of quenching-and-recharge circuits (QRCs) was discussed [68]. It is expected that accurate QRCs can increase the count rates, while reducing afterpulsing effects in SPADs.

• Data transmission capabilities of AMLEDs were investigated and the results were correlated to physically measurable multiplication noise and leakage current of AMLEDs [70]. It was demonstrated that a large number of defects and a high leakage current are required for high speed AMLEDs.

• The AMLEDs were integrated with low power driver circuits to implement a monolithic optical transmitter in a standard 140 nm CMOS technology [71]. Using extensive electrical and optical measurements, the robustness of this transmitter to the process, voltage, temperature and operating conditions was shown. The light emitted by the transmitter was detected using a laterally integrated photodetector, hence demonstrating optocoupling in CMOS. From the experimental results, the IQE of Si AMLEDs was estimated to be in the order of $10^{-5}$.

• Finally, for the first time, data communication using monolithically integrated optocouplers in a standard CMOS technology without any post-processing was demonstrated [72]. A data rate of few Mbps with energy consumption of a few nJ/bit was shown.


[107] K. Xu, “Integrated Silicon Directly Modulated Light Source Using p-Well


[121] A. Khanmohammadi, R. Enne, M. Hofbauer, and H. Zimmermann,


APPENDIX

MULTIPLICATION NOISE IN ON-STATE

The multiplication noise during avalanche ($\sigma_M$) can also be estimated from the measured RTSs. Firstly, the transient noise in the On-state of an RTS ($I_n(t)$) was estimated from the measured ($I_{\text{Meas}}(t)$) and the fitted data ($I_R(t)$), see Eq. (3.2):

$$I_n(t) = I_{\text{Meas}}(t) - I_R(t). \quad (A.1)$$

Fig. A.1(a) illustrates an example of the procedure of estimating $I_n(t)$ for “decaying” amplitude RTSs. A similar procedure was used for “constant” amplitude RTSs. At a given bias, $I_n(t)$ was estimated for each RTS of either type. These $I_n(t)$’s were represented as a histogram. An example of the histogram obtained for the “decaying” amplitude RTSs is shown in Fig. A.1(b). A Gaussian ($N(b_n, \sigma_n^2)$) was fitted on this $I_n(t)$ histogram. $\sigma_n$ is the summation of the $\sigma_M$ and the oscilloscope noise ($\sigma_0$) according to $\sigma_M = \sqrt{\sigma_n^2 - \sigma_0^2}$. $\sigma_M$ is a measure of the fluctuation in the multiplication factor ($M$) due to the statistical nature of the avalanche process [85].

The estimated $\sigma_M$ for both types of RTSs for all diodes are shown in Fig. A.1(c)-(d). $\sigma_M$ remains almost constant with $V_{\text{EX}}$. During the On-state of an RTS, $M$ is very high [74]. A constant $\sigma_M$ implies that the fluctuation in $M$ is relatively constant with $V_{\text{EX}}$. This has been reported earlier that the noise becomes independent of $M$ for large values of $M$ [85, 128]. Further, noise decreased with increasing avalanche current if the defects would be stable and would not cause any RTS fluctuations [128]. In particular, the unstable defects increase the noise [128]. In our operating bias range, the increasing noise contributed by the unstable defects possibly counteract the decrease in noise induced by the increasing current and therefore a relatively constant $\sigma_M$ is observed.
Figure A.1: a) Example procedure for estimating the noise transient $I_n(t)$. b) Example histogram and the fitted Gaussian of the obtained $I_n$ of the “decaying” amplitude RTSs in J3 at $V_R = 13.87$ V. c) $\sigma_M$ for the “decaying” amplitude RTSs. d) $\sigma_M$ for the “constant” amplitude RTSs.

In addition, $\sigma_M$ increases for larger diodes. This is because for a larger diode, the number of defects increases, resulting in noise originating from multiple RTS phenomena [97].
A mean $I_{\text{peak}}$ was assumed in section 3.5. As discussed there, in large diodes, the avalanche most likely spreads to nearby defects by lateral transport of free carriers and emission of secondary photons. This effect can be observed from the $I_{\text{peak}}$ histogram. An example of the histogram of the $I_{\text{peak}}$ for “decaying” amplitude RTSs for all diodes at indicated bias settings are shown in Fig. B.1. A wider distribution in $I_{\text{peak}}$ for large diodes is obtained. This is likely due to the random fluctuations in the triggering of avalanche at different defects in larger diodes.
Figure B.1: Histogram of the “decaying” $I_{\text{peak}}$ at specified bias points in all diodes. Y-axis scales are different for clarity. The wider distribution of $I_{\text{peak}}$ in larger diodes can be attributed to random fluctuations at more number of defects.
**APPENDIX C**

**LIGHT EMISSION PROFILES**

It is commonly known that during avalanche, Si p-n junctions emit visible light [14, 16], though with a low internal quantum efficiency ($\sim 10^{-5}$) [71]. The shallow junctions in these devices allow to observe the light emission from these devices using a visible wavelength camera.

Fig. C.1(a) shows some micrographs of these diodes. Fig. C.1(b) shows the light emission from these devices during the instability region captured using a Nikon D3100 camera with an integration time of 30 s. These emissions were observed at a relatively high $V_R$; at those $V_R$ mostly “constant” amplitude type of RTSs were observed ($F \sim 1$). At lower $V_R$, the light emission intensity was too low to be detected by our camera. Further, for smaller diodes, the light emission appears to be uniform whereas for larger diodes (e.g. J20), the light emitting spots are aligned at the periphery. The light emission from localized spots imply that an RTS in avalanche diodes is a localized phenomenon.

Fig. C.1(c) shows the normalized intensity profiles of the light emission. A non-uniform emission from the localized spots can be seen for the large diodes (e.g. J20). Fig. C.1(d) shows the contour plots of the light emission and localized light emitting spots in large devices can be observed.

From Fig. C.1(b) and the camera resolution, the area of the light emitting spots can be estimated. At the measured camera settings, one image pixel area ($A_{\text{pixel}}$) $\sim 0.002 \, \mu m^2$. From the intensity profiles (Fig. C.1(c)), the number of pixels with an intensity higher than a threshold ($N_{\text{pixels}}$) was estimated. Then, the area of the light emitting spot was estimated as $A_{\text{spot}} = N_{\text{pixels}} \times A_{\text{pixel}}$. From $A_{\text{spot}}$, an effective $d$ of the light emitting spot was estimated as $d = \sqrt{4A_{\text{spot}}/\pi}$; the results are shown in Fig. C.1(e). A close agreement with the estimated $d$ in Fig. 3.14(c) was obtained. The $d$ for J12 is larger than that for J20 as was also estimated in Fig. 3.14(c). The slight difference with the $d$ estimated using the $I_{\text{peak}} - V_{\text{EX}}$ characteristics can be attributed to the low intensity of emission at some pixels that could
Figure C.1: a) Device micrographs. b) Light emission captured using a Nikon D3100 Visible wavelength camera with an integration time of 30 s. c) Normalized intensity profiles, d) Normalized contour profiles of the light emission from these junctions. Note that (b) is to scale whereas for (c) and (d) the axis scales are different to enhance the clarity. e) Estimated $d$ of the defects causing “constant” amplitude RTSs from the light emitting spots.

not be measured by the available camera (Nikon D3100).
LIST OF PUBLICATIONS

Peer reviewed


Others


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