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SYSTEM FOR PARALLEL RADIO RECEPTION WITH DIGITALLY CONTROLLED ANALOG MIXER AMPLIFIERS

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Abstract of WO2018083387 (A1)

The parallel reception system (SYS) comprises a plurality of receiving devices (DIS1-DISN), each comprising an amplifying circuit (CA) in a frequency transformation stage coupled with the antenna and configured to perform a frequency transposition of the signal (V_{in}) received by said antenna. The analog amplifier circuit (CA) comprises transconductance amplifier units (UA_j) in parallel, each comprising a PMOS transistor (P1) and an NMOS transistor (N2) the gates of which are connected to the input node (I) and the drains to the output node (O). A control means (MCOM) is configured to generate a digital control signal, of which each bit (B_j) respectively controls the supply of each amplifier unit (UA_j) according to a sinusoidal wave representation at a frequency of interest.



